

Retrascope RISC-V Benchmark - Bug #9475

Picorv32Hx8kdemoVerilogPrinterTestCase: ERROR: line 1:0 no viable alternative at input '('

02/06/2019 11:25 AM - Sergey Smolov

Status:	Closed	Start date:	02/06/2019
Priority:	Normal	Due date:	
Assignee:	Alexander Kamkin	% Done:	100%
Category:	Verilog Translator	Estimated time:	0.00 hour
Target version:		Spent time:	0.00 hour
Detected in build:	master	Published in build:	
Platform:			
Description			
When running the <code>ru.ispras.verilog.parser.sample.Picorv32Hx8kdemoVerilogPrinterTestCase</code> , the following error appears:			
<pre>ERROR: line 1:0 no viable alternative at input '(' ERROR: [Internal] null java.lang.StackOverflowError at org.antlr.runtime.BaseRecognizer.mismatchIsUnwantedToken (BaseRecognizer.java:127) at org.antlr.runtime.BaseRecognizer.recoverFromMismatchedToken (BaseRecognizer.java:593) at org.antlr.runtime.BaseRecognizer.match (BaseRecognizer.java:115) at ru.ispras.verilog.parser.grammar.VerilogParser.begin_block_statement (VerilogParser.java:139 92) at ru.ispras.verilog.parser.grammar.VerilogParser.statement (VerilogParser.java:15145) ...</pre>			
The same error appears for the following test cases:			
<code>ru.ispras.verilog.parser.sample.Picorv32IcebreakerVerilogPrinterTestCase</code>			
<code>ru.ispras.verilog.parser.sample.Picorv32PicosocVerilogPrinterTestCase</code>			
<code>ru.ispras.verilog.parser.sample.Picorv32SimpleuartVerilogPrinterTestCase</code>			
<code>ru.ispras.verilog.parser.sample.Picorv32SpiflashVerilogPrinterTestCase</code>			
<code>ru.ispras.verilog.parser.sample.Picorv32SpimemioVerilogPrinterTestCase</code>			
Related issues:			
Related to Verilog Translator - Task #9811: macro with parameters		Closed	09/05/2019

History

#1 - 09/06/2019 02:59 PM - Sergey Smolov

- Related to Task #9811: macro with parameters added

#2 - 04/19/2020 05:54 PM - Sergey Smolov

- % Done changed from 0 to 100

- Status changed from New to Resolved

#3 - 04/19/2020 05:55 PM - Sergey Smolov

- Status changed from Resolved to Closed