

Retrascope - Task #9310

substitute SMT-LIB variables those names are equal to builtin commands

10/05/2018 04:53 PM - Sergey Smolov

Status:	Verified	Start date:	10/05/2018
Priority:	Normal	Due date:	
Assignee:	Sergey Smolov	% Done:	100%
Category:	Utility	Estimated time:	0.00 hour
Target version:	1.0	Published in build:	
Detected in build:	master		
Description			
Names for variables in HDL designs may be equal to builtin commands of SMT-LIB language (like "select"). When passing constraints to solver, such variables should change their names.			
Related issues:			
Related to Retrascope MC Benchmark - Bug #9309: ru.ispras.retrascope.engine.s...		Closed	10/05/2018

History

#1 - 10/05/2018 04:53 PM - Sergey Smolov

- Related to Bug #9309: ru.ispras.retrascope.engine.smv.testbench.sample.vcegar.VcegarPiBusAssertSmvTestbenchTestCase:line 2 column 34:
invalid declaration, builtin symbol select added

#2 - 10/08/2018 12:25 PM - Sergey Smolov

- % Done changed from 0 to 100
- Status changed from New to Resolved

Done in [f4f5dda9](#)

#3 - 10/08/2018 05:45 PM - Sergey Smolov

- Status changed from Resolved to Verified