

Verilog Translator - Task #9232

remove typedefs from texas97-tests/PPC60X_bus/src/define.v

08/17/2018 10:51 AM - Sergey Smolov

| | | | |
|---------------------------|-----------------|----------------------------|------------|
| Status: | Closed | Start date: | 08/17/2018 |
| Priority: | High | Due date: | |
| Assignee: | Mikhail Lebedev | % Done: | 100% |
| Category: | | Estimated time: | 0.00 hour |
| Target version: | 0.1 | Published in build: | |
| Detected in build: | master | | |
| Description | | | |

History

#1 - 08/17/2018 12:04 PM - Mikhail Lebedev

- % Done changed from 0 to 100
- Status changed from New to Resolved

#2 - 08/18/2018 02:04 PM - Sergey Smolov

- Status changed from Resolved to Verified

#3 - 08/18/2018 02:04 PM - Sergey Smolov

- Status changed from Verified to Closed