

Veritool - Bug #9184

ERROR: Unable to read config file: /usr/lib/x86_64-linux-gnu/ivl/veritool.conf

08/01/2018 04:04 PM - Sergey Smolov

Status:	New	Start date:	08/01/2018
Priority:	Normal	Due date:	
Assignee:		% Done:	0%
Category:		Estimated time:	0.00 hour
Target version:		Spent time:	0.00 hour
Detected in build:	0.2.7-beta-20130717	Published in build:	
Platform:	Linux x64		

Description

Running the tool on the attached Verilog module with such parameters, as '--c --module=blocks --clk=clk --rst=rst --all blocks.v', causes the following error trace:

```
ERROR: Unable to read config file: /usr/lib/x86_64-linux-gnu/ivl/veritool.conf
: error: target_design entry point is missing.
error: Code generator failure: -2
veritool failed
```

History

#1 - 08/01/2018 04:18 PM - Sergey Smolov

If symlinks will be created with the following commands, the problem will be solved from user side:

```
$ sudo ln -s /lib/ivl/veritool.conf /usr/lib/x86_64-linux-gnu/ivl/veritool.conf
$ sudo ln -s /lib/ivl/veritool.tgt /usr/lib/x86_64-linux-gnu/ivl/veritool.tgt
```

Files

blocks.v	573 Bytes	08/01/2018	Sergey Smolov
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