

## Verilog Translator - Task #8725

### Using Fortress's BitVector in VerilogLiteral

02/14/2018 03:38 PM - Alexander Kamkin

<b>Status:</b>	Closed	<b>Start date:</b>	02/14/2018
<b>Priority:</b>	Normal	<b>Due date:</b>	
<b>Assignee:</b>	Alexander Kamkin	<b>% Done:</b>	0%
<b>Category:</b>		<b>Estimated time:</b>	0.00 hour
<b>Target version:</b>	0.1	<b>Published in build:</b>	
<b>Detected in build:</b>	svn		
<b>Description</b>			
Use BitVector instead of BigInteger and byte[] in VerilogLiteral class.			

#### History

**#1 - 02/14/2018 03:38 PM - Alexander Kamkin**

- Target version set to 0.1

**#2 - 02/22/2018 02:34 PM - Alexander Kamkin**

- Status changed from New to Resolved

**#3 - 04/09/2018 02:14 PM - Alexander Kamkin**

- Status changed from Resolved to Closed