

## Retrascope - Feature #8220

### BV\_INC6 VHDL function support

05/26/2017 06:25 PM - Sergey Smolov

<b>Status:</b>	Closed	<b>Start date:</b>	05/26/2017
<b>Priority:</b>	Normal	<b>Due date:</b>	
<b>Assignee:</b>	Sergey Smolov	<b>% Done:</b>	100%
<b>Category:</b>	Engine (Parser)	<b>Estimated time:</b>	0.00 hour
<b>Target version:</b>	0.2		
<b>Published in build:</b>	1.0.1-beta-170912		
<b>Description</b>			
Run for testing the following:			
<pre>args=/home/ssedai/projects/retrascope.git/build/resources/test/plasma/mlite_cpu.vhd --target vhdl- testbench --toplevel mlite_cpu --overwrite-existing --engine cfg-random-test-generator --test-len 10 --clk-name CLK --rst-name RESET_IN</pre>			
The following error log is produced:			
<pre>BV_INC6(A =&gt; COUNT_REG) java.lang.UnsupportedOperationException: BV_INC6(A =&gt; COUNT_REG)     at ru.ispras.retrascope.parser.vhdl.IgOperationVisitor.visitSubprogram(IgOperationVisitor.java :617)     at org.zamia.instgraph.IGDefaultOperationVisitor.visitOperation(IGDefaultOperationVisitor.java :28)     at ru.ispras.retrascope.parser.vhdl.IgOperationVisitor.visitOperation(IgOperationVisitor.java: 138)     at ru.ispras.retrascope.parser.vhdl.IgOperationVisitor.getNode(IgOperationVisitor.java:750)     at ru.ispras.retrascope.parser.vhdl.IgStructureVisitor.getOperationNode(IgStructureVisitor.jav a:555)     at ru.ispras.retrascope.parser.vhdl.IgStructureVisitor.visitAssignment(IgStructureVisitor.java :551)     at org.zamia.instgraph.IGDefaultSequentialStatementVisitor.visit(IGDefaultSequentialStatementV isitor.java:57)     at ru.ispras.retrascope.parser.vhdl.IgStructureVisitor.visit(IgStructureVisitor.java:376)     at org.zamia.instgraph.IGDefaultSequentialStatementVisitor.visit(IGDefaultSequentialStatementV isitor.java:54)     at ru.ispras.retrascope.parser.vhdl.IgStructureVisitor.visit(IgStructureVisitor.java:376)     at ru.ispras.retrascope.parser.vhdl.IgStructureVisitor.getSubGraph(IgStructureVisitor.java:584 )     at ru.ispras.retrascope.parser.vhdl.IgStructureVisitor.visitIf(IgStructureVisitor.java:473)     at org.zamia.instgraph.IGDefaultSequentialStatementVisitor.visit(IGDefaultSequentialStatementV isitor.java:63)     at ru.ispras.retrascope.parser.vhdl.IgStructureVisitor.visit(IgStructureVisitor.java:376)     at org.zamia.instgraph.IGDefaultSequentialStatementVisitor.visit(IGDefaultSequentialStatementV isitor.java:54)     at ru.ispras.retrascope.parser.vhdl.IgStructureVisitor.visit(IgStructureVisitor.java:376)     at ru.ispras.retrascope.parser.vhdl.IgStructureVisitor.visitProcess(IgStructureVisitor.java:23 5)     at ru.ispras.retrascope.parser.vhdl.IgStructureVisitor.visit(IgStructureVisitor.java:366)     at ru.ispras.retrascope.parser.vhdl.IgStructureVisitor.visitInstantiation(IgStructureVisitor.j ava:259)     at ru.ispras.retrascope.parser.vhdl.IgStructureVisitor.visit(IgStructureVisitor.java:364)     at org.zamia.instgraph.IGModule.accept(IGModule.java:101)     at ru.ispras.retrascope.parser.vhdl.VhdlParser.parse(VhdlParser.java:165)     at ru.ispras.retrascope.parser.basis.HdlParser.start(HdlParser.java:86)     at ru.ispras.retrascope.basis.Engine.start(Engine.java:214)     at ru.ispras.retrascope.basis.ToolChain.start(ToolChain.java:112)     at ru.ispras.retrascope.basis.Engine.start(Engine.java:214)     at ru.ispras.retrascope.Retrascope\$ToolRun.start(Retrscope.java:211)</pre>			

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at ru.ispras.retrascop.e.Retrascope.main (Retrascope.java:419)
at ru.ispras.retrascop.e.Retrascope.main (Retrascope.java:371)
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## History

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### #1 - 06/10/2017 01:25 PM - Sergey Smolov

- Status changed from *New* to *Resolved*
- % Done changed from 0 to 100

Done in [50b8b605](#)

### #2 - 09/02/2017 10:15 PM - Sergey Smolov

- Subject changed from *VHDL functions complete support* to *BV\_INC6 VHDL function support*

### #3 - 09/03/2017 10:55 AM - Sergey Smolov

- Status changed from *Resolved* to *Verified*

### #4 - 09/12/2017 03:25 PM - Sergey Smolov

- Status changed from *Verified* to *Closed*
- Published in build set to 1.0.1-beta-170912