

Verilog Translator - Bug #6363

src/test/verilog/fifo0/mem_2p.v: AbstractMethodError

10/21/2015 06:38 PM - Sergey Smolov

Status:	Closed	Start date:	10/21/2015
Priority:	High	Due date:	
Assignee:	Alexander Kamkin	% Done:	100%
Category:		Estimated time:	0.00 hour
Target version:	0.1	Published in build:	
Detected in build:	svn		
Platform:			

Description

Exception in thread "main" java.lang.AbstractMethodError:
ru.ispras.verilog.parser.calculator.StandardIntegerOperations\$12.validTypes([Lru/ispras/fortress/data/Data;)Z
at ru.ispras.fortress.calculator.OperationGroup.isSupported(OperationGroup.java:109)
at ru.ispras.fortress.transformer.OperationReducer.isSupported(OperationReducer.java:148)
at ru.ispras.fortress.transformer.OperationReducer.calculate(OperationReducer.java:195)
at ru.ispras.fortress.transformer.OperationReducer.reduce(OperationReducer.java:76)
at ru.ispras.fortress.transformer.Transformer.reduce(Transformer.java:73)
at ru.ispras.fortress.transformer.OperationReducer.analyzeOperands(OperationReducer.java:112)
at ru.ispras.fortress.transformer.OperationReducer.<init>(OperationReducer.java:65)
at ru.ispras.fortress.transformer.Transformer.reduce(Transformer.java:70)
at ru.ispras.verilog.parser.model.basis.Expression.reduce(Unknown Source)
at ru.ispras.verilog.parser.model.basis.Expression.reduce(Unknown Source)
at ru.ispras.verilog.parser.processor.VerilogGenerateProcessor.reduce(Unknown Source)
at ru.ispras.verilog.parser.processor.VerilogGenerateProcessor.reduce(Unknown Source)
at ru.ispras.verilog.parser.processor.VerilogGenerateProcessor.reduce(Unknown Source)
at ru.ispras.verilog.parser.processor.VerilogGenerateProcessor.onDeclarationBegin(Unknown Source)
at ru.ispras.verilog.parser.walker.VerilogNodeVisitor\$13.onBegin(Unknown Source)
at ru.ispras.verilog.parser.walker.VerilogNodeVisitor.onBegin(Unknown Source)
at ru.ispras.verilog.parser.core.TreeWalker.onBegin(Unknown Source)
at ru.ispras.verilog.parser.core.TreeWalker.start(Unknown Source)
at ru.ispras.verilog.parser.VerilogBackend.start(Unknown Source)
at ru.ispras.verilog.parser.VerilogBackends.start(Unknown Source)
at ru.ispras.verilog.parser.VerilogTranslator.start(Unknown Source)

History

#1 - 10/22/2015 11:09 AM - Sergey Smolov

- Status changed from *New* to *Resolved*

This problem comes from changes in Fortress interface, fixed in r406.

#2 - 10/22/2015 11:09 AM - Sergey Smolov

- % Done changed from 0 to 100

#3 - 11/02/2015 05:00 PM - Sergey Smolov

- Status changed from *Resolved* to *Verified*

#4 - 03/01/2018 02:42 PM - Alexander Kamkin

- Status changed from *Verified* to *Closed*