

MicroTESK - Bug #6106

zero opcodes for instructions in Tarmac log

06/23/2015 12:23 PM - Sergey Smolov

Status:	Closed	Start date:	06/23/2015
Priority:	Normal	Due date:	
Assignee:	Andrei Tatarnikov	% Done:	0%
Category:		Estimated time:	0.00 hour
Target version:	2.2	Spent time:	0.00 hour
Detected in build:	svn	Published in build:	
Platform:			

Description

Most of instructions that are represented in Tarmac log have zero opcodes. Today only two instructions definitely have non-zero opcodes - that are hlt and add.

Here is a sample of Tarmac log that demonstrates this bug:

```
882 clk IT (882) 00002648 00000000 A svc_ns : movz x0, #0x0, LSL #48
883 clk IT (883) 00002654 00000000 A svc_ns : movk x0, #0x0, LSL #32
884 clk IT (884) 00002660 00000000 A svc_ns : movk x0, #0x0, LSL #16
885 clk IT (885) 0000266c 00000000 A svc_ns : movk x0, #0x1, LSL #0
886 clk IT (886) 00002678 00000000 A svc_ns : movz x1, #0x0, LSL #48
887 clk IT (887) 00002684 00000000 A svc_ns : movk x1, #0x0, LSL #32
888 clk IT (888) 00002690 00000000 A svc_ns : movk x1, #0x0, LSL #16
889 clk IT (889) 0000269c 00000000 A svc_ns : movk x1, #0x1, LSL #0
890 clk IT (890) 000026a8 91000420 A svc_ns : add x0, x1, #1, LSL #0
891 clk IT (891) 000026ac 00000000 A svc_ns : movz x0, #0x0, LSL #48
892 clk IT (892) 000026b8 00000000 A svc_ns : movk x0, #0x0, LSL #32
893 clk IT (893) 000026c4 00000000 A svc_ns : movk x0, #0x0, LSL #16
894 clk IT (894) 000026d0 00000000 A svc_ns : movk x0, #0x1, LSL #0
895 clk IT (895) 000026dc 00000000 A svc_ns : movz x1, #0x0, LSL #48
896 clk IT (896) 000026e8 00000000 A svc_ns : movk x1, #0x0, LSL #32
897 clk IT (897) 000026f4 00000000 A svc_ns : movk x1, #0x0, LSL #16
898 clk IT (898) 00002700 00000000 A svc_ns : movk x1, #0x1, LSL #0
899 clk IT (899) 0000270c 91000420 A svc_ns : add x0, x1, #1, LSL #0
900 clk IT (900) 00002710 d45bd5a0 A svc_ns : hlt #57005
```

History

#1 - 08/25/2015 12:51 PM - Sergey Smolov

- Status changed from New to Closed