

Retrascope - Task #6049

VHDL test printer: write documentation to project wiki

06/14/2015 12:54 PM - Sergey Smolov

Status:	Closed	Start date:	06/14/2015
Priority:	Normal	Due date:	
Assignee:	Igor Melnichenko	% Done:	100%
Category:	Engine (Printer)	Estimated time:	0.00 hour
Target version:	0.1	Published in build:	20150701
Detected in build:	svn		
Description			
Add information to project Wiki about VHDL test generation engine and it's command line options.			

History

#1 - 06/17/2015 03:47 PM - Igor Melnichenko

- Status changed from New to Resolved

- % Done changed from 0 to 100

#2 - 06/17/2015 04:12 PM - Sergey Smolov

- Status changed from Resolved to Verified

#3 - 07/01/2015 11:45 AM - Sergey Smolov

- Status changed from Verified to Closed

- Published in build set to 20150701