

Retrascope - Task #5859

Названия модулей, извлекаемых из VHDL-описания

04/18/2015 05:21 PM - Igor Melnichenko

Status:	Closed	Start date:	04/18/2015
Priority:	Normal	Due date:	
Assignee:	Sergey Smolov	% Done:	100%
Category:	Engine (Parser)	Estimated time:	0.00 hour
Target version:	0.1	Published in build:	20150701
Detected in build:	svn		
Description			
Предлагаю использовать в качестве названий модулей только имена соответствующих entity из VHDL-описания. Например, чтобы из b01.vhd извлекался модуль с названием B01, а не WORK.B01.			

History

#1 - 04/20/2015 09:10 AM - Sergey Smolov

- Status changed from New to Open

#2 - 04/20/2015 09:11 AM - Sergey Smolov

- Category changed from Engine (Transformer) to 64

#3 - 04/20/2015 09:27 AM - Sergey Smolov

- Status changed from Open to Resolved

- % Done changed from 0 to 100

Done in r1834.

#4 - 04/20/2015 04:12 PM - Igor Melnichenko

- Status changed from Resolved to Verified

#5 - 07/01/2015 11:50 AM - Sergey Smolov

- Status changed from Verified to Closed

- Published in build set to 20150701