

MicroTESK - Task #5672

Support for UNPREDICTED and UNDEFINED

03/03/2015 11:35 AM - Andrei Tatarnikov

Status:	Closed	Start date:	03/03/2015
Priority:	Urgent	Due date:	
Assignee:	Artem Kotsynyak	% Done:	100%
Category:	MIR	Estimated time:	0.00 hour
Target version:	2.2	Spent time:	0.00 hour
Detected in build:	svn	Published in build:	150611
Description			
Support for handling branches of instruction logic marked as UNPREDICTED and UNDEFINED:			
1. In the nML translator (keywords UNPREDICTED or UNDEFINED);			
2. In the coverage extractor (corresponding constraints)			

History

#1 - 03/03/2015 03:14 PM - Andrei Tatarnikov

- % Done changed from 0 to 10

The "unpredicted" and "undefined" keywords were supported in the translator: r3227.

Note: The initial implementation is raw and is a subject to review in the future.

#2 - 03/12/2015 08:08 AM - Alexander Kamkin

- Subject changed from [translator][coverage] Support for UNPREDICTED and UNDEFINED to Support for UNPREDICTED and UNDEFINED

- Category set to MIR

- Target version set to 2.2

#3 - 03/31/2015 10:22 AM - Alexander Kamkin

- Assignee changed from Andrei Tatarnikov to Artem Kotsynyak

Coverage Extractor should take into account UNPREDICATED and UNDEFINED.

#4 - 06/08/2015 06:14 PM - Artem Kotsynyak

- Status changed from New to Resolved

- % Done changed from 10 to 100

r3855

#5 - 06/15/2015 01:31 PM - Andrei Tatarnikov

- Status changed from Resolved to Closed

- Published in build set to 150611