

Verilog Translator - Bug #5567

VerilogStaticChecker.ExpressionVisitor is not abstract and does not override abstract method getOperandOrder() in ExprTreeVisitor

01/22/2015 02:55 PM - Sergey Smolov

Status:	Closed	Start date:	01/22/2015
Priority:	High	Due date:	
Assignee:	Alexander Kamkin	% Done:	0%
Category:		Estimated time:	0.00 hour
Target version:	0.1	Published in build:	
Detected in build:	svn		
Platform:			
Description			
\$ ant			
Buildfile: /home/ssedai/projects/veritrans.svn/veritrans/build.xml			
init:			
parser:			
[echo] antlr VerilogLexer.g			
[echo] antlr VerilogParser.g			
[echo] antlr VerilogTreeBuilder.g			
build:			
[javac] Compiling 50 source files to /home/ssedai/projects/veritrans.svn/veritrans/bin/main			
[javac] /home/ssedai/projects/veritrans.svn/veritrans/src/main/java/ru/ispras/verilog/parser/processor/VerilogStaticChecker.java:68:			
error: VerilogStaticChecker.ExpressionVisitor is not abstract and does not override abstract method getOperandOrder() in ExprTreeVisitor			
[javac] private final class ExpressionVisitor implements ExprTreeVisitor {			
[javac] ^			
[javac] 1 error			
BUILD FAILED			
/home/ssedai/projects/veritrans.svn/veritrans/build.xml:91: Compile failed; see the compiler error output for details.			

History

#1 - 01/22/2015 03:09 PM - Sergey Smolov

- Status changed from New to Resolved

Саша починил.

#2 - 01/22/2015 03:09 PM - Sergey Smolov

- Status changed from Resolved to Verified

#3 - 01/22/2015 03:09 PM - Sergey Smolov

- Status changed from Verified to Closed