

MicroTESK - Bug #5514

The implementation of 'coerce' needs a review

12/19/2014 10:57 AM - Andrei Tatarnikov

Status: Closed	Start date: 12/19/2014
Priority: High	Due date:
Assignee: Andrei Tatarnikov	% Done: 100%
Category:	Estimated time: 0.00 hour
Target version: 2.2	Spent time: 0.00 hour
Detected in build: svn	Published in build: 2.2.14
Platform:	
Description	
The implementation is very simple: size is changed, extra bits are set to zero. Sign extension is not supported, which causes bugs. Also, conversion from different data type families (e.g float to integer is not supported)/	

History

#1 - 03/12/2015 10:31 AM - Alexander Kamkin

- Subject changed from [model]The implementation of 'coerce' needs a review to The implementation of 'coerce' needs a review
- Category set to ISA Simulator
- Target version set to 2.1

#2 - 03/19/2015 10:16 AM - Alexander Kamkin

- Category changed from ISA Simulator to 90

#3 - 03/25/2015 06:29 AM - Alexander Kamkin

- Target version changed from 2.1 to 2.2

#4 - 08/07/2015 01:15 PM - Andrei Tatarnikov

- Status changed from New to Resolved
- % Done changed from 0 to 100

See r4613 and r4638.

Implementation was reviewed:

1. Currently, coercion is supported for INT, CARD and BOOLEAN. For all other types an exception is raised.
2. Sign extension is done on coercion to INT (signed integer). In all other cases, zero extension is performed.

Support for other types will be implemented when we face such a need.

#5 - 08/07/2015 07:21 PM - Andrei Tatarnikov

- Status changed from Resolved to Closed
- Published in build set to 2.2.14