

## Retrascope - Task #4877

### Разработка конвертера тестовой последовательности в VCD-формат

05/05/2014 07:46 AM - Alexander Kamkin

<b>Status:</b>	Verified	<b>Start date:</b>	05/05/2014
<b>Priority:</b>	Normal	<b>Due date:</b>	
<b>Assignee:</b>	Maxim Chudnov	<b>% Done:</b>	0%
<b>Category:</b>	Engine (Printer)	<b>Estimated time:</b>	0.00 hour
<b>Target version:</b>	1.1	<b>Published in build:</b>	
<b>Detected in build:</b>	master		
<b>Description</b>			
Требуется разработать транслятор XML-представления тестовой последовательности в VCD-формат.			

#### History

##### #1 - 05/05/2014 07:54 AM - Alexander Kamkin

XML-формат создается в рамках задачи [#4876](#).

Транслятор создается в рамках задачи [#4878](#) (задача сводится к созданию backend'a для потокового транслятора).

##### #2 - 05/26/2014 10:48 AM - Sergey Smolov

- Subject changed from *[basis][testgen] Разработка конвертера тестовой последовательности в VCD-формат* to *[basis][testgen] Разработка конвертера тестовой последовательности в VCD-формат*

##### #3 - 12/30/2014 01:50 PM - Sergey Smolov

- Target version changed from 0.1 to 0.2

##### #4 - 03/13/2015 03:03 PM - Sergey Smolov

- Subject changed from *[basis][testgen] Разработка конвертера тестовой последовательности в VCD-формат* to *Разработка конвертера тестовой последовательности в VCD-формат*

- Category set to 79

##### #5 - 03/11/2016 04:26 PM - Sergey Smolov

- Target version changed from 0.2 to 2.0

##### #6 - 10/25/2016 12:22 PM - Sergey Smolov

- Detected in build changed from svn to master

##### #7 - 02/12/2019 01:35 PM - Alexander Protsenko

- Assignee changed from Alexander Protsenko to Alexander Kamkin

- Status changed from New to Feedback

##### #8 - 07/23/2019 11:47 AM - Sergey Smolov

- Assignee changed from Alexander Kamkin to Alexander Protsenko

- Status changed from Feedback to New

Test-to-VCD printer is still needed in Retrascope. Other tools for functional verification of HDL descriptions have such functionality.

##### #9 - 02/06/2020 02:42 PM - Sergey Smolov

- Target version changed from 2.0 to 1.1

- Assignee changed from Alexander Protsenko to Maxim Chudnov

##### #10 - 08/12/2020 02:59 PM - Maxim Chudnov

- Status changed from New to Resolved

##### #11 - 08/19/2020 11:44 AM - Sergey Smolov

- Status changed from Resolved to Open
- File IMG\_20200819\_113930.jpg added
- File IMG\_20200819\_113930.jpg added

Engine-generated VCD files probably incorrect. See screenshots for more details.

#### #12 - 09/10/2020 09:35 PM - Maxim Chudnov

- Status changed from Open to Resolved

#### #13 - 09/10/2020 10:36 PM - Sergey Smolov

- Status changed from Resolved to Open

The following test fails: **ru.ispras.retrascopengine.test.printer.vcd.VcdVerilogTestSuite#fifo\_mod**. Here is the crash log:

```
2020.09.10 22:35:23.668. INFO: Running: vcd-printer

2020.09.10 22:35:23.668. INFO: Options: {args=--clk-name clk --rst-name rst --include-path D:\Bot\projects\retrascopengine\src\test\verilog\fifo_mod D:\Bot\projects\retrascopengine\src\test\verilog\fifo_mod\fifo.v D:\Bot\projects\retrascopengine\src\test\verilog\fifo_mod\ram.v --module-name fifo_mod --engine cfg-random-test-generator;vcd-printer --vcd fifo_mod.vcd --target vcd --test-len 100, test=<test>}

java.lang.NullPointerException
    at ru.ispras.retrascopengine.test.printer.vcd.TestVcdVisitor.<init>(TestVcdVisitor.java:111)
    at ru.ispras.retrascopengine.test.printer.vcd.VcdPrinter.start(VcdPrinter.java:81)
    at ru.ispras.retrascopengine.basis.Engine.start(Engine.java:332)
    at ru.ispras.retrascopengine.basis.ToolChain.start(ToolChain.java:111)
    at ru.ispras.retrascopengine.basis.Engine.start(Engine.java:332)
    at ru.ispras.retrascopengine.Retrascope$ToolRun.start(Retrascopengine.java:243)
    at ru.ispras.retrascopengine.Retrascope.main(Retrascopengine.java:501)
    at ru.ispras.retrascopengine.Retrascope.main(Retrascopengine.java:403)
    at ru.ispras.retrascopengine.util.ToolTest.runTest(ToolTest.java:102)
    at ru.ispras.retrascopengine.test.printer.vcd.VcdBenchTest.run_Test(VcdBenchTest.java:54)
    at ru.ispras.retrascopengine.test.printer.vcd.VcdVerilogTestSuite.fifo_mod(VcdVerilogTestSuite.java:54)
```

#### #14 - 09/10/2020 11:09 PM - Sergey Smolov

Ошибка [#10462](#) исправлена, теперь все тесты для vcd printer проходят. Просьба проверить стиль кодирования и исправить warnings, которые выдает IntelliJ IDEA (у меня их 27).

#### #15 - 09/14/2020 03:41 PM - Maxim Chudnov

- Status changed from Open to Resolved

#### #16 - 09/14/2020 03:58 PM - Sergey Smolov

- Status changed from Resolved to Verified

### Files

IMG_20200819_113930.jpg	3.22 MB	08/19/2020	Sergey Smolov
IMG_20200819_113930.jpg	3.22 MB	08/19/2020	Sergey Smolov