

## MicroTESK - Bug #4281

### [translator] Support for aliases in memory (aka mem) definitions.

07/01/2013 10:29 AM - Andrei Tatarnikov

<b>Status:</b>	Closed	<b>Start date:</b>	07/01/2013
<b>Priority:</b>	High	<b>Due date:</b>	05/31/2014
<b>Assignee:</b>	Andrei Tatarnikov	<b>% Done:</b>	100%
<b>Category:</b>		<b>Estimated time:</b>	0.00 hour
<b>Target version:</b>	2.1	<b>Spent time:</b>	0.00 hour
<b>Detected in build:</b>	svn	<b>Published in build:</b>	141230
<b>Platform:</b>			

#### Description

Подобные конструкции сейчас не поддерживаются транслятором Sim-nML:

```
mem tmp_signed_half_word [1, int(16)]
mem tmp_signed_half_word_A0 [1, int(8)] alias = tmp_signed_half_word[8]
```

Это одна из причин, по которой не удаётся оттранслировать спецификацию процессора MIPS.

#### History

##### #1 - 03/21/2014 09:44 AM - Alexander Kamkin

- Priority changed from Normal to High

Нужно сделать до семинара с МЦСТ.

##### #2 - 04/03/2014 07:43 AM - Alexander Kamkin

- Due date set to 05/31/2014

- Target version set to 2.1

##### #3 - 11/10/2014 07:22 PM - Andrei Tatarnikov

- Status changed from New to Open

##### #4 - 11/28/2014 11:32 AM - Andrei Tatarnikov

- Status changed from Open to Resolved

- % Done changed from 0 to 100

Implemented in r2835 (see also r2836 - size check).

The following structure is supported:

```
mem <NAME>[<N>, <TYPE>] alias = <LOCATION>
```

Memory <NAME> that consists of <N> elements is mapped on location <LOCATION>. Location size must be equal sizeof(<TYPE>) \* <N>.

##### #5 - 01/12/2015 11:04 AM - Andrei Tatarnikov

- Status changed from Resolved to Closed

- Published in build set to 141230