

Verilog Translator - Bug #4110

AST_DECLARATION: adding delay for net declarations

04/15/2013 10:32 AM - Alexander Kamkin

Status:	New	Start date:	04/15/2013
Priority:	Normal	Due date:	
Assignee:	Alexander Kamkin	% Done:	0%
Category:		Estimated time:	0.00 hour
Target version:	0.1	Published in build:	
Detected in build:	svn		
Platform:			
Description			
Extend AST_DECLARATION with AST_DELAY whose value is null for all declarations except net declarations.			

History

#1 - 04/15/2013 01:45 PM - Alexander Kamkin

net_declaration should contain continuous assignment, other declarations - procedural assignments.

#2 - 04/29/2014 01:57 PM - Alexander Kamkin

- Target version set to 0.1