

## Verilog Translator - Bug #4107

### Support for pulse control in specparam assignment

04/15/2013 10:26 AM - Alexander Kamkin

|                           |                  |                            |            |
|---------------------------|------------------|----------------------------|------------|
| <b>Status:</b>            | Closed           | <b>Start date:</b>         | 04/15/2013 |
| <b>Priority:</b>          | Normal           | <b>Due date:</b>           |            |
| <b>Assignee:</b>          | Alexander Kamkin | <b>% Done:</b>             | 0%         |
| <b>Category:</b>          |                  | <b>Estimated time:</b>     | 0.00 hour  |
| <b>Target version:</b>    | 0.1              | <b>Published in build:</b> |            |
| <b>Detected in build:</b> | svn              |                            |            |
| <b>Platform:</b>          |                  |                            |            |
| <b>Description</b>        |                  |                            |            |
| Grammar + AST             |                  |                            |            |

#### History

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**#1 - 04/29/2014 01:57 PM - Alexander Kamkin**

- Target version set to 0.1

**#2 - 08/31/2018 12:30 PM - Alexander Kamkin**

- Status changed from New to Resolved

**#3 - 08/31/2018 02:17 PM - Alexander Kamkin**

- Status changed from Resolved to Closed