

## Verilog Translator - Task #4106

### Support for library and config declaration

04/15/2013 10:26 AM - Alexander Kamkin

<b>Status:</b>	New	<b>Start date:</b>	04/15/2013
<b>Priority:</b>	Low	<b>Due date:</b>	
<b>Assignee:</b>	Alexander Kamkin	<b>% Done:</b>	0%
<b>Category:</b>		<b>Estimated time:</b>	0.00 hour
<b>Target version:</b>	0.3	<b>Published in build:</b>	
<b>Detected in build:</b>	svn		
<b>Description</b>			
Grammar + AST			

#### History

**#1 - 04/16/2013 08:03 PM - Alexander Kamkin**

- Subject changed from Support for config declaration to Support for library and config declaration

- Priority changed from Normal to Low

**#2 - 04/16/2013 08:04 PM - Alexander Kamkin**

Специфическая вещь, связанная с синтезом.

**#3 - 04/29/2014 01:57 PM - Alexander Kamkin**

- Target version set to 0.1

**#4 - 04/29/2014 01:58 PM - Alexander Kamkin**

- Target version changed from 0.1 to 0.3