

## MicroTESK - Task #10193

### Cache instances configuration

04/01/2020 10:23 AM - Alexander Kamkin

<b>Status:</b>	New	<b>Start date:</b>	04/01/2020
<b>Priority:</b>	Normal	<b>Due date:</b>	
<b>Assignee:</b>	Artem Kotsynyak	<b>% Done:</b>	0%
<b>Category:</b>	MMU Plugin	<b>Estimated time:</b>	0.00 hour
<b>Target version:</b>	2.5	<b>Spent time:</b>	0.00 hour
<b>Detected in build:</b>	git	<b>Published in build:</b>	
<b>Description</b>			
<p>MMU specifications look like they are written for a single core. That's OK and, I think, it should be so. However, it is unclear how to instantiate the caches and connects them with the cores, the main memory and each other in multi-core settings. There should be a kind of configuration.</p> <p>The modeling library provides the following mechanisms.</p> <p>CacheUnit's constructor has the next parameter, which is a reference to the next-level cache instance or the main memory (null if the next attribute is not specified):</p> <pre>public CacheUnit(..., final Buffer&lt;? extends Struct&lt;?&gt;, A&gt; next)</pre> <p>Also, CacheUnit implements the addNeighbor method that links the cache instance with the same-level ones:</p> <pre>public void addNeighbor(final CacheUnit&lt;?, A&gt; other)</pre>			