# Issues

| # | Project | Tracker | Status | Priority | Subject | Author | Assignee | Target version |
|---|---------|---------|--------|----------|---------|--------|----------|----------------|
| 7759 | TestBase | Bug | Closed | High | Avoid using lambda functions | Andrei Tatarnikov | Alexander Dyumaev | |
| 5312 | TestBase | Task | Closed | High | TestBase.executeQuery should return some status description | Andrei Tatarnikov | Artem Kotsynyak | 0.0 |
| 8939 | MicroTESK | Feature | Closed | High | [autogen] New option 'base-template-path' must be supported. | Andrei Tatarnikov | Alexander  Protsenko | 2.4 |
| 8481 | MicroTESK | Task | New | Normal | Need a way to specify the termination address for the test program | Andrei Tatarnikov | Alexander Kamkin | 2.6 |
| 8455 | MicroTESK | Bug | Rejected | Normal | Test Document | Andrei Tatarnikov | Andrei Tatarnikov | 2.4 |
| 8061 | MicroTESK | Bug | Rejected | Normal | Position of all blocks, calls, data etc. in test templates must be tracked to provide better debugging | Andrei Tatarnikov | Andrei Tatarnikov | 2.4 |
| 8060 | MicroTESK | Bug | New | Normal | Attributes image and syntax and static instances | Andrei Tatarnikov | Artem Kotsynyak | 2.6 |
| 7690 | MicroTESK | Bug | Closed | Urgent | Exception handler is not handled if it is not the first element in the list of exception describe in test templates | Andrei Tatarnikov | Andrei Tatarnikov | 2.4 |
| 7678 | MicroTESK | Task | New | Low | Generation of LLVM configuration files from nML specifications | Andrei Tatarnikov | Alexander Kamkin | 2.6 |
| 7628 | MicroTESK | Bug | Closed | Immediate | Methods 'dist' and 'range' are not supported in data sections | Andrei Tatarnikov | Andrei Tatarnikov | 2.3 |
| 7603 | MicroTESK | Bug | New | Normal | List of plug-ins must be stored in etc/settings.xml | Andrei Tatarnikov | Alexander Kamkin | 2.6 |
| 7534 | MicroTESK | Task | Closed | High | Configuration option to manage reservation of explicitly specified registers | Andrei Tatarnikov | Andrei Tatarnikov | 2.3 |
| 7463 | MicroTESK | Bug | Closed | Urgent | Correct processing of block structures and code in the root of pre, post and run methods | Andrei Tatarnikov | Andrei Tatarnikov | 2.3 |
| 7461 | MicroTESK | Bug | Closed | Immediate | Block-level prologue and epilogue must stay prologue and epilogue when sequences are mixed | Andrei Tatarnikov | Andrei Tatarnikov | 2.3 |
| 7418 | MicroTESK | Bug | Closed | High | Reset the state of the model before starting generating a new test program fle | Andrei Tatarnikov | Alexander Kamkin | 2.5 |
| 7385 | MicroTESK | Bug | Closed | Immediate | The 'prepare' function does not work with randomly selected registers | Andrei Tatarnikov | Andrei Tatarnikov | 2.3 |
| 7300 | MicroTESK | Task | Closed | Urgent | More timing metrics in generation statistics | Andrei Tatarnikov | Andrei Tatarnikov | 2.3 |
| 7298 | MicroTESK | Task | Closed | Urgent | Command-line option to disable simulation on the reference model | Andrei Tatarnikov | Andrei Tatarnikov | 2.3 |
| 7269 | MicroTESK | Bug | Rejected | High | Returning from an exception in the last instruction in a test case | Andrei Tatarnikov | Andrei Tatarnikov | 2.3 |
| 7251 | MicroTESK | Bug | Rejected | High | A warning or error is needed when a value is truncated | Andrei Tatarnikov | Artem Kotsynyak | 2.5 |
| 7012 | MicroTESK | Task | Closed | High | Implement means of managing the use of registers in a test program | Andrei Tatarnikov | Andrei Tatarnikov | 2.3 |
| 6973 | MicroTESK | Task | Closed | High | Support for atomic sequences that will never be interrupted/reordered by a compositor/permutator | Andrei Tatarnikov | Andrei Tatarnikov | 2.3 |
| 6911 | MicroTESK | Bug | New | Normal | The "get_address_of" method must work with all label types. | Andrei Tatarnikov | Alexander Kamkin | 2.6 |
| 6910 | MicroTESK | Bug | Closed | Immediate | The is_global flag for data sections | Andrei Tatarnikov | Andrei Tatarnikov | 2.3 |
| 6455 | MicroTESK | Bug | Closed | Urgent | Implement block-level prologue and epilogue | Andrei Tatarnikov | Andrei Tatarnikov | 2.3 |
| 6380 | MicroTESK | Bug | New | Normal | Possibility to specify initial values for registers | Andrei Tatarnikov | Artem Kotsynyak | 2.6 |
| 6194 | MicroTESK | Bug | Rejected | Normal | Automated extraction of addressable unit size | Andrei Tatarnikov | Andrei Tatarnikov | |
| 5990 | MicroTESK | Bug | New | Normal | Memory state must be taken into account when generating test data | Andrei Tatarnikov | Alexander Kamkin | 2.6 |

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| 5987 | MicroTESK | Bug | Closed | High | Handling situations when TestBase fails to generate test data (e.g. constraint is UNSAT) | Andrei Tatarnikov | Andrei Tatarnikov | 2.2 |
| 5716 | MicroTESK | Task | Closed | High | Implement the possibility to process template code multiple times (e.g. atomic (:repeat => N) {}) | Andrei Tatarnikov | Andrei Tatarnikov | 2.3 |
| 5708 | MicroTESK | Bug | Closed | Immediate | MicroTESK build fails: SsaAssembler - symbol Changes is not defined | Andrei Tatarnikov | Artem Kotsynyak | 2.1 |
| 5678 | MicroTESK | Task | Closed | Urgent | Support for named branches in ISA specifications | Andrei Tatarnikov | Andrei Tatarnikov | 2.1 |
| 5676 | MicroTESK | Task | Closed | Urgent | [generator] Random generation - weighting / biasing distribution | Andrei Tatarnikov | | |
| 5674 | MicroTESK | Task | Rejected | Normal | Description of test data generation mechanisms (test situation processing, constraint solving) | Andrei Tatarnikov | Alexander Kamkin | 2.2 |
| 5673 | MicroTESK | Task | Closed | Urgent | Memory scalability for large memory ranges (address space for 48 and 64 bit addresses) | Andrei Tatarnikov | Andrei Tatarnikov | 2.2 |
| 5672 | MicroTESK | Task | Closed | Urgent | Support for UNPREDICTED and UNDEFINED | Andrei Tatarnikov | Artem Kotsynyak | 2.2 |
| 5671 | MicroTESK | Bug | Rejected | Urgent | Robustness of test template processing logic must be improved | Andrei Tatarnikov | Andrei Tatarnikov | 2.2 |
| 5670 | MicroTESK | Task | Closed | Normal | Examples of test templates using memory-related test situations (hit, miss) | Andrei Tatarnikov | Alexander Kamkin | 2.3 |
| 5668 | MicroTESK | Bug | Closed | Immediate | Issues with large memory addresses (48 and 64 bits) must be fixed | Andrei Tatarnikov | Andrei Tatarnikov | 2.2 |
| 5667 | MicroTESK | Task | Closed | Urgent | Support splitting generated test programs into several source files | Andrei Tatarnikov | Andrei Tatarnikov | 2.3 |
| 5666 | MicroTESK | Task | Closed | Urgent | Estimate generation speed | Andrei Tatarnikov | Alexander Kamkin | 2.1 |
| 5659 | MicroTESK | Bug | Rejected | High | Function 'trace' should accept addressing modes as arguments | Andrei Tatarnikov | Andrei Tatarnikov | 2.2 |
| 5658 | MicroTESK | Task | Closed | Normal | Need a single entry point for the translator and test generator subsystem | Andrei Tatarnikov | Andrei Tatarnikov | 2.2 |
| 5657 | MicroTESK | Task | Closed | High | A command line option for random seed | Andrei Tatarnikov | Andrei Tatarnikov | 2.3 |
| 5654 | MicroTESK | Task | Closed | Urgent | [template] User-defined seed for random generation | Andrei Tatarnikov | Andrei Tatarnikov | |
| 5650 | MicroTESK | Bug | Rejected | High | Exception when no test situation is specified. | Andrei Tatarnikov | Andrei Tatarnikov | 2.2 |
| 5639 | MicroTESK | Task | Closed | High | Code review notes (package ru.ispras.unitesk.processor.test.mmu) | Andrei Tatarnikov | Alexander  Protsenko | 2.2 |
| 5542 | MicroTESK | Bug | Closed | High | Creating instances of modes and ops: validity checks for arguments are needed | Andrei Tatarnikov | Andrei Tatarnikov | 2.2 |
| 5514 | MicroTESK | Bug | Closed | High | The implementation of 'coerce' needs a review | Andrei Tatarnikov | Andrei Tatarnikov | 2.2 |
| 5403 | MicroTESK | Task | Closed | Normal | Test sequence generation logic (blocks, combinators, compositors) needs a review | Andrei Tatarnikov | Alexander Kamkin | 2.2 |
| 5350 | MicroTESK | Task | Closed | High | Labels in the text of instruction calls | Andrei Tatarnikov | Andrei Tatarnikov | 2.2 |
| 5344 | MicroTESK | Task | Closed | Urgent | Metamodel should provide information on argument types and whether they are in or out arguments | Andrei Tatarnikov | Andrei Tatarnikov | 2.2 |
| 5192 | MicroTESK | Task | New | Normal | Assert constructions to check the model state during and after simulation | Andrei Tatarnikov | Alexander Kamkin | 2.6 |
| 4996 | MicroTESK | Bug | Closed | Normal | [translator] It should be possible to define the "instruction" operation (root) as an OR rule | Andrei Tatarnikov | Andrei Tatarnikov | 2.1 |
| 4900 | MicroTESK | Task | Rejected | High | Printing information on the model should be possible | Andrei Tatarnikov | Andrei Tatarnikov | 2.2 |

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| 4283 | MicroTESK | Task | Closed | Normal | [translator] Support for custom attributes in operations and addressing modes. | Andrei Tatarnikov | Andrei Tatarnikov | 2.1 |
| 4281 | MicroTESK | Bug | Closed | High | [translator] Support for aliases in memory (aka mem) definitions. | Andrei Tatarnikov | Andrei Tatarnikov | 2.1 |
| 4161 | MicroTESK | Bug | Rejected | Normal | Observing the design state via addressing modes | Andrei Tatarnikov | Andrei Tatarnikov | 2.2 |
| 4116 | MicroTESK | Task | Closed | Normal | [model] Lazy memory allocation in memory models | Andrei Tatarnikov | Andrei Tatarnikov | 2.1 |
| 4109 | MicroTESK | Bug | Closed | Urgent | [template] Update needed: Test templates do not work after changes in Java. | Andrei Tatarnikov | Andrei Tatarnikov | |
| 4104 | MicroTESK | Task | Closed | Normal | [translator] Reset VAR values after an instruction call | Andrei Tatarnikov | Andrei Tatarnikov | |
| 4061 | MicroTESK | Task | New | Normal | Support for endianness | Andrei Tatarnikov | Alexander Kamkin | 2.6 |
| 4015 | MicroTESK | Bug | Closed | Normal | [translator] Shift and Rotate operations: negative distance | Andrei Tatarnikov | Andrei Tatarnikov | |
| 4014 | MicroTESK | Bug | Closed | Normal | Concatenation in left hand side expressions | Andrei Tatarnikov | Andrei Tatarnikov | 2.2 |
| 3996 | MicroTESK | Bug | Closed | Normal | [translator] Support for long types (64-bit) in expressions | Andrei Tatarnikov | Andrei Tatarnikov | |
| 3995 | MicroTESK | Bug | Closed | Normal | [translator] Support for bitfields using non-constant expressions | Andrei Tatarnikov | Andrei Tatarnikov | |
| 3988 | MicroTESK | Task | Rejected | Normal | Support for forward definitions in the nML translator | Andrei Tatarnikov | Andrei Tatarnikov | 2.2 |
| 5819 | Fortress | Task | Closed | High | Calculator for bit vectors | Andrei Tatarnikov | Artem Kotsynyak | 0.4 |
| 5775 | Fortress | Bug | Closed | Normal | BitVector.longValue returns incorrect values | Andrei Tatarnikov | Andrei Tatarnikov | 0.4 |
| 5665 | Fortress | Task | Closed | Urgent | Support for CVC4 | Andrei Tatarnikov | Artem Kotsynyak | 0.4 |
| 5463 | Fortress | Bug | Closed | High | [S-expr] JavaDoc comments are needed | Andrei Tatarnikov | Artem Kotsynyak | 0.3 |
| 5460 | Fortress | Bug | Closed | High | [project] Get rid of warnings | Andrei Tatarnikov | Artem Kotsynyak | 0.3 |
| 5217 | Fortress | Bug | Closed | Normal | [expression] NodeBinding: hashCode and equals must be implemented | Andrei Tatarnikov | Artem Kotsynyak | 0.3 |
| 5177 | Fortress | Task | Closed | Normal | [solver] Implement function templates | Andrei Tatarnikov | Andrei Tatarnikov | 0.3 |
| 5176 | Fortress | Task | Closed | Normal | [expression] Possibility to calculate the expression type | Andrei Tatarnikov | Andrei Tatarnikov | 0.3 |
| 4693 | Fortress | Task | Closed | High | [data] Реализации операций над битовыми векторами (BitVector) | Andrei Tatarnikov | Andrei Tatarnikov | 0.3 |