

## Issues

#	Project	Tracker	Status	Priority	Subject	Author	Assignee	Target version
1259	Aspectrace	Task	Closed	Normal	Шаблоны velocity находятся в одном каталоге с jar	Alexander Kamkin	Anastasia Tugaenko	
2053	C++TESK Development Environment	Bug	Closed	Normal	Часто при запуске mcs не устанавливается связь с сервером	Alexander Kamkin	Alexander Kamkin	
1687	MicroTESK	Task	Rejected	Normal	Хранение ограничений во внешней памяти	Alexander Kamkin		
280	CTESK	Task	Closed	Normal	Функция отключения проверки постусловий	Alexander Kamkin	Alexey Demakov	2.8
3671	MicroTESK	Task	Closed	Normal	Учесть инструкции переходов в шаблонах	Alexander Kamkin	Andrei Tatarnikov	2.1
1413	Aspectrace	Bug	Closed	Normal	Утечка памяти в функции printNode	Alexander Kamkin	Николай Пакулин	
1272	C++TESK Testing ToolKit	Task	Closed	Normal	Установка модуля подсветки синтаксиса VIM	Alexander Kamkin	Alexander Kamkin	
2058	C++TESK Development Environment	Task	Closed	Normal	Установка имени файла по умолчанию при запуске graphviz	Alexander Kamkin	Alexander Kamkin	
1707	C++TESK Testing ToolKit	Task	Closed	Normal	Унификация опций скриптов	Alexander Kamkin	Alexander Kamkin	
3958	MicroTESK	Task	Closed	High	Удалить старый, неиспользуемый код	Alexander Kamkin	Andrei Tatarnikov	
3533	Verilog Translator	Task	Closed	Normal	Убрать отдельное поддерево AST_PARAMETERS дерева AST_MODULE	Alexander Kamkin	Alexander Kamkin	
2051	C++TESK Development Environment	Task	Closed	Normal	Убрать окна сообщений, в которых есть только одна кнопка ОК	Alexander Kamkin	Alexander Kamkin	
4964	Verilog Translator	Task	Closed	Normal	Типизация переменных	Alexander Kamkin	Alexander Kamkin	0.1
1525	C++TESK Testing ToolKit	Task	Closed	Normal	Тесты для проверки билда	Alexander Kamkin	Alexander Kamkin	
4219	Fortress	Feature	Closed	Normal	Схлопывание константных выражений	Alexander Kamkin	Sergey Smolov	0.2
4704	Retrascope	Task	Closed	Normal	Структура пакетов проекта	Alexander Kamkin	Alexander Kamkin	
3981	Retrascope	Task	Closed	Normal	Структура пакетов проекта	Alexander Kamkin	Alexander Kamkin	
4963	Verilog Translator	Task	Closed	Normal	Структура данных для представления значений параметров	Alexander Kamkin	Alexander Kamkin	0.1
3779	Retrascope	Task	Rejected	Normal	Статья про конфликты в процессорах	Alexander Kamkin	Alexander Chudnovskiy	
1500	C++TESK Testing ToolKit	Task	Closed	Normal	Список рассылки C++TESK Testing ToolKit	Alexander Kamkin	Alexander Kamkin	
1283	C++TESK Testing ToolKit	Task	Closed	Normal	Список рассылки	Alexander Kamkin	Alexander Kamkin	

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4082	Verilog Translator	Task	Closed	Normal	Сохранение атрибутов выражения	Alexander Kamkin	Alexander Kamkin	
4980	Retrascope IDE	Task	Closed	Normal	Сортировка целей и инструментов в ToolLauncher	Alexander Kamkin	Alexander Kamkin	
4917	Retrascope	Task	Closed	Normal	Создания инструмента поиска конфликтов для пар EFSM	Alexander Kamkin	Mikhail Lebedev	1.0
1270	C++TESK Testing ToolKit	Task	Closed	Normal	Совместное использование с SystemVerilog	Alexander Kamkin	Alexander Kamkin	1.0
1184	C++TESK Testing ToolKit	Task	Closed	High	Скрипт установки полного набора инструментов	Alexander Kamkin	Alexander Kamkin	
1186	Veritool	Task	New	Normal	Скрипт установки Icarus Verilog	Alexander Kamkin	Alexander Kamkin	
2145	MicroTESK	Task	Closed	Normal	Скрипт сборки инструмента	Alexander Kamkin	Andrei Tatarnikov	
3566	MicroTESK	Task	Closed	Normal	Синтаксис описания подсистемы управления памятью	Alexander Kamkin	Andrei Tatarnikov	2.3
3954	Retrascope	Task	Closed	Normal	Симулятор EFSM-модели	Alexander Kamkin	Alexander Kamkin	
3592	Verilog Translator	Task	Rejected	Normal	Сделать все узлы модели наследниками VerilogNode	Alexander Kamkin	Alexander Kamkin	
5167	Verilog Translator	Task	Closed	Normal	Связывание экземпляров модулей с декларациями	Alexander Kamkin	Alexander Kamkin	0.1
5168	Verilog Translator	Task	Closed	Normal	Связывание вызовов функций с декларациями	Alexander Kamkin	Alexander Kamkin	0.1
5169	Verilog Translator	Task	Closed	Normal	Связывание вызовов задач с декларациями	Alexander Kamkin	Alexander Kamkin	0.1
4062	Verilog Translator	Task	Closed	Normal	Сборка проекта с помощью ant	Alexander Kamkin	Alexander Kamkin	
2132	C++TESK Development Environment	Task	Closed	Normal	Сборка проекта	Alexander Kamkin	Alexander Kamkin	
643	Veritool	Task	Closed	Normal	Репозиторий тестов	Alexander Kamkin	Alexander Kamkin	
4886	Retrascope IDE	Task	Closed	Normal	Реализовать навигатор по модели HDL-описания	Alexander Kamkin	Alexander Protsenko	
4962	Retrascope	Task	New	Normal	Реализация утилиты Veritool с помощью средств Retrascope	Alexander Kamkin	Alexander Kamkin	2.0
4891	Retrascope	Task	New	Low	Реализация генератора тестов по набору блок-схем	Alexander Kamkin	Alexander Kamkin	2.0
4881	Retrascope	Task	New	Low	Реализация алгоритма построения блок-схем по внутреннему представлению	Alexander Kamkin	Sergey Smolov	2.0
2136	C++TESK Development Environment	Task	Closed	Normal	Распределенный запуск теста	Alexander Kamkin	Alexander Kamkin	
4882	Retrascope	Task	New	Low	Разработка принтеров блок-схем в графические форматы	Alexander Kamkin	Alexander Protsenko	2.0
4877	Retrascope	Task	Closed	Normal	Разработка конвертера тестовой последовательности в VCD-формат	Alexander Kamkin	Maxim Chudnov	1.1

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4890	Retrascope	Task	New	Low	Разработка и реализация метода RTL-в-TLM абстракции блок-схем	Alexander Kamkin	Alexander Kamkin	2.0
4887	Retrascope	Task	New	Low	Разработка внутреннего представления тестовой системы	Alexander Kamkin	Sergey Smolov	2.0
4874	Retrascope	Task	Closed	Normal	Разработка внутреннего представления для решающих диаграмм	Alexander Kamkin	Alexander Kamkin	1.0
2133	C++TESK Development Environment	Task	Closed	Normal	Разобраться с разработкой плагинов для Eclipse IDE	Alexander Kamkin	Alexander Kamkin	
2148	MicroTESK	Task	Closed	Normal	Разделение солверов на два класса	Alexander Kamkin	Andrei Tatarnikov	
3278	MicroTESK	Task	Closed	Normal	Работающий прототип для miniMIPS	Alexander Kamkin	Andrei Tatarnikov	2.2
4151	MicroTESK	Task	Closed	Normal	Путаница в параметрах шаблонов комбинаторов и композиторов	Alexander Kamkin	Alexander Kamkin	2.2
4880	Retrascope	Task	New	Low	Проектирование внутреннего представления для блок-схем	Alexander Kamkin	Sergey Smolov	2.0
1583	MicroTESK	Task	Closed	Normal	Продумать внутреннее представление для ограничений	Alexander Kamkin	Andrei Tatarnikov	
5023	Retrascope	Task	Closed	Normal	Провести сравнение методов генерации функциональных тестов	Alexander Kamkin	Sergey Smolov	
2188	C++TESK Testing ToolKit	Task	Closed	Normal	Проверка совместимости инструмента с поддерживаемыми тестовыми системами	Alexander Kamkin	Alexander Kamkin	1.0
4097	Verilog Translator	Task	Open	Normal	Проверка зацикливания include-файлов	Alexander Kamkin	Alexander Kamkin	0.1
2143	MicroTESK	Task	Closed	Normal	Пробелы вместо табуляций	Alexander Kamkin	Andrei Tatarnikov	
1544	C++TESK Testing ToolKit	Task	Closed	Normal	При установке srpteskhw копируется hw/utlis/win32, но не копируется hw/utlis	Alexander Kamkin	Alexander Kamkin	
1222	C++TESK Testing ToolKit	Bug	Closed	Normal	При сборке теряется каталог hw/veritool	Alexander Kamkin	Mikhail Chupilko	
5408	Retrascope	Task	Closed	Normal	Принтер CFG в SMV	Alexander Kamkin	Mikhail Lebedev	1.0
2049	C++TESK Development Environment	Bug	Closed	Normal	При закрытии окна MCC server configuration открывается окно браузера	Alexander Kamkin	Alexander Kamkin	
4965	Verilog Translator	Bug	Closed	Normal	Привязывание переменных к их декларациям	Alexander Kamkin	Alexander Kamkin	0.1
5041	Retrascope IDE	Task	Closed	Normal	Привести в порядок сгенерированный код	Alexander Kamkin	Alexander Kamkin	
3477	Verilog Translator	Task	Closed	Normal	Препроцессор Verilog-кода	Alexander Kamkin	Alexander Kamkin	
12201	Track Finding Tools for NICA MPD	Task	Closed	Normal	Предобработка данных, выгруженных из ACTS	Alexander Kamkin		
5121	Verilog Translator	Bug	Closed	Normal	Похоже, что x- и z-маски всегда нулевые	Alexander Kamkin	Alexander Kamkin	0.1
4875	Retrascope	Task	Closed	Normal	Построение решающей диаграммы по системе условий	Alexander Kamkin	Alexander Kamkin	1.0
4949	Retrascope IDE	Task	Closed	Normal	Построение окна настроек инструмента по описанию его параметров	Alexander Kamkin	Alexander Kamkin	

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5164	Verilog Translator	Bug	Closed	Normal	После директивы `endif` в той же строчке могут идти лексемы	Alexander Kamkin	Alexander Kamkin	0.1
2130	Veritool	Task	New	Normal	Получение списка внутренних сигналов	Alexander Kamkin	Mikhail Chupilko	
5205	Verilog Translator	Task	Closed	Normal	Подстановка тела функции в место вызова	Alexander Kamkin	Alexander Kamkin	0.1
1268	C++TESK Testing ToolKit	Task	Closed	Normal	Подсветка синтаксиса в VIM	Alexander Kamkin	Alexander Kamkin	
3499	Fortress	Task	Closed	High	Поддержка пользовательских операций	Alexander Kamkin	Andrei Tatarnikov	0.1
4050	Fortress	Bug	Closed	Normal	Поддержка вызовов функций	Alexander Kamkin	Alexander Kamkin	0.1
3516	Verilog Translator	Task	Closed	Normal	Поддержка в препроцессоре условной компиляции	Alexander Kamkin	Alexander Kamkin	
3514	Verilog Translator	Task	Rejected	Low	Поддержка в препроцессоре define с параметрами	Alexander Kamkin	Alexander Kamkin	0.1
1181	C++TESK Testing ToolKit	Task	Closed	Normal	Поддержка Windows	Alexander Kamkin	Mikhail Chupilko	1.0
606	Veritool	Task	Closed	Normal	Поддержка C++ библиотеке sync	Alexander Kamkin	Alexander Kamkin	
1518	C++TESK Testing ToolKit	Task	Closed	Normal	Подвисание инсталлятора в конце работы	Alexander Kamkin	Alexander Kamkin	
1044	Veritool	Task	Feedback	Normal	Перенос репозитория тестов	Alexander Kamkin	Alexander Kamkin	
5228	Verilog Translator	Task	Closed	Normal	Перенести Expression.VariableGatherer в Fortress	Alexander Kamkin	Alexander Kamkin	0.1
2048	C++TESK Development Environment	Task	Closed	Normal	Перенаправление stdout в консоль Eclipse IDE	Alexander Kamkin	Alexander Kamkin	
3591	Verilog Translator	Bug	Closed	Normal	Переименовать AST_ITEM_GENERATE	Alexander Kamkin	Alexander Kamkin	
155	CTESK	Bug	Rejected	Normal	Переименование функции clone()	Alexander Kamkin	Alexey Demakov	
2055	C++TESK Development Environment	Task	Closed	Normal	Переименование полей в окне MCC	Alexander Kamkin	Alexander Kamkin	
825	Veritool	Task	Closed	Normal	Переименование опции ICARUS_VERILOG	Alexander Kamkin	Alexander Kamkin	
402	Veritool	Task	Closed	Normal	Передача параметров теста	Alexander Kamkin	Alexander Kamkin	
298	Aspectrace	Bug	Closed	Normal	Падение при невозможности открыть файл трассы	Alexander Kamkin	Alexander Kamkin	
3830	MicroTESK	Bug	Closed	Normal	Ошибки компиляции	Alexander Kamkin	Andrei Tatarnikov	
3822	MicroTESK	Bug	Closed	Normal	Ошибки компиляции	Alexander Kamkin	Taya Sergeeva	
206	CTESK	Bug	Closed	High	Ошибка в генерируемой функции call	Alexander Kamkin	Alexey Demakov	2.8
420	Veritool	Task	Closed	Normal	Отображение номера такта во временной диаграмме	Alexander Kamkin	Alexander Kamkin	

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387	Veritool	Task	New	Normal	Отладочный вывод значений входов и выходов	Alexander Kamkin	Alexander Kamkin	
5104	Verilog Translator	Task	Closed	Normal	Отладка сопоставления ссылок и деклараций	Alexander Kamkin	Alexander Kamkin	0.1
1257	Aspectrace	Task	Closed	Normal	Отключение построения отчета по аспекту coverage	Alexander Kamkin	Anastasia Tugaenko	
4044	Verilog Translator	Task	Closed	Normal	Отдельный класс для представления пути (List<String> -> Path)	Alexander Kamkin	Alexander Kamkin	
1604	MicroTESK	Task	Rejected	Normal	Определить типы данных аналогичные Z3	Alexander Kamkin		
3628	C++TESK Development Environment	Task	Open	Normal	Описание XML-схемы для прототипирования тестовых систем	Alexander Kamkin	Alexander Protsenko	
2144	MicroTESK	Task	Closed	Normal	Окружение солвера ограничений	Alexander Kamkin	Andrei Tatarnikov	
3482	Verilog Translator	Bug	Closed	Normal	Объединить в грамматике model_item и module_or_generate_item	Alexander Kamkin	Alexander Kamkin	
3484	Verilog Translator	Task	Closed	Normal	Объединить FUNCTION_DECLARATION и TASK_DECLARATION	Alexander Kamkin	Alexander Kamkin	
5150	Retrascope	Task	New	Normal	Обработка функций - реализация макроподстановок	Alexander Kamkin	Sergey Smolov	1.2
1499	C++TESK Testing ToolKit	Task	Closed	Normal	Обработка ошибок установки	Alexander Kamkin	Alexander Kamkin	
1700	C++TESK Testing ToolKit	Bug	Closed	Normal	Обработка ошибок в генераторе отчетов	Alexander Kamkin	Alexander Kamkin	
5206	Verilog Translator	Task	Closed	Normal	Обработка конструкций generate	Alexander Kamkin	Alexander Kamkin	0.1
5160	Verilog Translator	Task	Closed	Normal	Обработка комментариев в define	Alexander Kamkin	Alexander Kamkin	0.1
1423	C++TESK Testing ToolKit	Task	Closed	Normal	Обновление набора инструментов	Alexander Kamkin	Alexander Kamkin	
3781	Veritool	Task	Resolved	Normal	Нумерация тактов	Alexander Kamkin	Alexander Kamkin	
3764	Fortress	Task	Closed	High	Нужны методы equals() и hashCode() для формул	Alexander Kamkin	Andrei Tatarnikov	0.1
384	Veritool	Bug	Closed	Normal	Не хватает #include <vsim.h>. Иногда это приводит к ошибкам компиляции.	Alexander Kamkin	Alexander Kamkin	
383	Veritool	Bug	Closed	Normal	Не хватает #include "vpi_media.h"	Alexander Kamkin	Alexander Kamkin	
5473	Retrascope	Task	Closed	Normal	Непонятные места в описании Веронского метода	Alexander Kamkin	Sergey Smolov	1.0
204	CTESK	Bug	Closed	High	Не поддерживается совместная работа функций next() и call()	Alexander Kamkin	Alexey Demakov	2.8
5377	Fortress	Feature	Closed	Normal	Не поддерживается работа с несколькими CalculatorEngine	Alexander Kamkin	Andrei Tatarnikov	0.4
2047	C++TESK Development Environment	Bug	Closed	Normal	Не отображается картинка для кнопки start MCC with parameters	Alexander Kamkin	Alexander Kamkin	

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5028	Verilog Translator	Bug	Closed	Normal	Не обрабатывается конструкция repeat	Alexander Kamkin	Alexander Kamkin	0.1
3490	Verilog Translator	Bug	Closed	Normal	Некорректная обработка деклараций портов с размерностью перед идентификатором	Alexander Kamkin	Alexander Kamkin	
1177	CTESK	Bug	Rejected	Normal	Не запускается транслятор на 64-битном Linux	Alexander Kamkin	Alexander Kamkin	
491	CTESK	Bug	Closed	Normal	Неглубокое копирование строк	Alexander Kamkin	Alexey Demakov	2.8
411	Veritool	Task	Closed	Normal	Небольшой рефакторинг testbench.v	Alexander Kamkin	Alexander Kamkin	
3478	Verilog Translator	Task	Closed	Normal	Написать обходчик AST'a	Alexander Kamkin	Alexander Kamkin	
1584	MicroTESK	Task	Rejected	Normal	Написать мепперы для внутреннего представления ограничений	Alexander Kamkin		
5042	Retrascope IDE	Task	Closed	Normal	Написать комментарии к имеющемуся коду	Alexander Kamkin	Alexander Kamkin	
3481	Verilog Translator	Task	Closed	Normal	Найти место в AST для KW_VECTORED и KW_SCALARED	Alexander Kamkin	Alexander Kamkin	
288	CTESK	Task	Closed	Normal	Модификация функции getTTFileName()	Alexander Kamkin	Alexey Demakov	
4223	Fortress	Task	Closed	Normal	Метод DataFactory.createDataType() с одним параметром	Alexander Kamkin	Alexander Kamkin	0.2
2161	Veritool	Task	New	Normal	Массив вместо структуры для int128-int512	Alexander Kamkin	Mikhail Chupilko	
4675	Fortress	Task	New	Normal	Манипуляция с функциями алгебры логики	Alexander Kamkin	Alexander Kamkin	0.4
5166	Verilog Translator	Task	Closed	Normal	Логирования сообщение транслятора	Alexander Kamkin	Alexander Kamkin	0.1
1793	C++TESK Development Environment	Task	Closed	Normal	Краткое ТЗ на разработку плагина	Alexander Kamkin	Alexander Kamkin	
1245	C++TESK Testing ToolKit	Task	Closed	Normal	Косметические изменения в инсталляторе	Alexander Kamkin	Alexander Kamkin	
5348	TestBase	Task	New	Normal	Корректная реализация генератора IntSubExceptionGenerator	Alexander Kamkin	Alexander Kamkin	0.0
1813	CTESK	Feature	New	High	Конфликт имен ATL с пользовательскими	Alexander Kamkin	Alexey Demakov	
12202	Track Finding Tools for NICA MPD	Task	Closed	Normal	Конструирование признаков треков-кандидатов для последующей кластеризации	Alexander Kamkin		
5163	Verilog Translator	Bug	Closed	Normal	Конец файла после директивы препроцессора	Alexander Kamkin	Alexander Kamkin	0.1
386	Veritool	Task	Closed	Normal	Комментарии о разрядности входов и выходов	Alexander Kamkin	Alexander Kamkin	
3953	Retrascope	Task	Closed	Normal	Комментарии javadoc ко всем public методам	Alexander Kamkin	Alexander Kamkin	
289	Aspectrace	Task	New	Normal	Кодировка по умолчанию	Alexander Kamkin	Sergey Groshev	
2118	C++TESK Development Environment	Task	Closed	Normal	Ключевые слова CPPTESK_{DECLARE ADD}_FIELD_ARRAY	Alexander Kamkin	Alexander Kamkin	

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2109	C++TESK Development Environment	Task	Closed	Normal	Ключевые слова CPPTESK_CALL_STIMULUS_OF и CPPTESK_CALL_PROCESS_OF	Alexander Kamkin	Alexander Kamkin	
2098	C++TESK Development Environment	Task	Closed	Normal	Ключевое слово CPPTESK_ADD_INCOMPARABLE_FIELD	Alexander Kamkin	Alexander Kamkin	
12203	Track Finding Tools for NICA MPD	Task	Closed	Normal	Кластеризация треков-кандидатов по заданным признакам	Alexander Kamkin		
4246	Fortress	Task	Closed	Normal	Калькулятор константных выражений	Alexander Kamkin	Andrei Tatarnikov	0.2
1422	C++TESK Testing ToolKit	Task	Closed	Normal	Как узнать номер версии билда после установки?	Alexander Kamkin	Alexander Kamkin	
1603	MicroTESK	Task	Rejected	Normal	Как определять подтипы?	Alexander Kamkin		
203	CTESK	Task	Closed	Normal	Итерация значений итерационных переменных	Alexander Kamkin	Alexey Demakov	2.8
5566	Verilog Translator	Task	Closed	Normal	Использование стандартного принтера выражений	Alexander Kamkin	Alexander Kamkin	0.1
2059	C++TESK Development Environment	Task	New	Low	Использование разных цветов для выделения ключевых слов C++ и C++TESK	Alexander Kamkin	Sergey Smolov	
5086	Verilog Translator	Bug	Closed	Normal	Исключение при вычислении константных выражений	Alexander Kamkin	Alexander Kamkin	0.1
1185	Veritool	Task	New	Normal	Инструментирование Verilog-кода и отслеживание покрытия по коду	Alexander Kamkin	Sergey Smolov	
1183	C++TESK Testing ToolKit	Task	Closed	High	Инструкция по установке	Alexander Kamkin	Alexander Kamkin	
2595	C++TESK Testing ToolKit	Task	Closed	Normal	Инсталляция плагина	Alexander Kamkin	Alexander Kamkin	1.0
792	C++TESK Testing ToolKit	Task	Closed	High	Инсталлятор C++TESK	Alexander Kamkin	Alexander Kamkin	
2056	C++TESK Development Environment	Task	Closed	Normal	Иконки для файлов *.utt2 и *.gv	Alexander Kamkin	Alexander Kamkin	
1581	MicroTESK	Task	Rejected	Normal	Изучить язык nML/Sim-nML	Alexander Kamkin		
1121	C++TESK Testing ToolKit	Task	Closed	Normal	Изменить поставку исходников CTESK в cpptesk-toolkit-src-*.tar.gz	Alexander Kamkin	Mikhail Chupilko	1.0
3747	MicroTESK	Task	Closed	High	Заяка на DATE 2013	Alexander Kamkin	Andrei Tatarnikov	
2135	C++TESK Development Environment	Task	Closed	Normal	Запуск теста на локальной машине	Alexander Kamkin	Alexander Kamkin	

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2054	C++TESK Development Environment	Task	Closed	Normal	Заполнение окна MCC параметрами по умолчанию	Alexander Kamkin	Alexander Kamkin	
3602	Retrascope	Task	Closed	Normal	Замечания к коду прообраза ASM Library	Alexander Kamkin	Mikhail Lebedev	
1586	Veritool	Task	Closed	High	Задokumentировать внутреннее представление кода	Alexander Kamkin	Dmitry Vorobyev	
2050	C++TESK Development Environment	Bug	Closed	Normal	Заголовок "MCC server configuration" не помещается в окне	Alexander Kamkin	Alexander Kamkin	
4098	Verilog Translator	Bug	Closed	Normal	Если указать два файла при запуске парсера -> NullPointerException	Alexander Kamkin	Alexander Kamkin	
1246	Aspectrace	Task	Closed	Normal	Если в трассе нет покрытия, отображается null	Alexander Kamkin	Anastasia Tugaenko	
1446	Veritool	Task	Closed	Normal	Единая библиотека libveritool.a	Alexander Kamkin	Alexander Kamkin	
1182	C++TESK Testing ToolKit	Task	Closed	Normal	Документ по опциям обходчиков	Alexander Kamkin	Alexander Kamkin	
4561	Verilog Translator	Bug	Closed	Normal	Добавить узел LValue	Alexander Kamkin	Alexander Kamkin	
3500	Fortress	Task	Closed	Normal	Добавить поддержку операций Verilog	Alexander Kamkin	Alexander Kamkin	0.1
3515	Verilog Translator	Task	Closed	Normal	Добавить опцию для задания путей поиска include-файлов	Alexander Kamkin	Alexander Kamkin	
1582	MicroTESK	Task	Closed	Normal	Добавить документацию по nML/Sim-nML	Alexander Kamkin	Alexander Kamkin	
1585	MicroTESK	Task	Rejected	Normal	Добавить в транслятор с Sim-nML извлечение ограничений	Alexander Kamkin		
1699	C++TESK Testing ToolKit	Task	Closed	Normal	Добавить в примеры cptestshw/examples/cache_coherence	Alexander Kamkin	Alexander Kamkin	
5087	Verilog Translator	Task	Closed	Normal	Добавить возможность отсечения обхода дочерних поддеревьев	Alexander Kamkin	Alexander Kamkin	0.1
5174	Verilog Translator	Task	Closed	Normal	Добавить в калькулятор поддержку тернарной операции	Alexander Kamkin	Alexander Kamkin	0.1
4043	Verilog Translator	Task	Closed	Normal	Добавить в атрибут name в базовый узел	Alexander Kamkin	Alexander Kamkin	
388	Veritool	Task	Closed	Normal	Добавить extern "C" в vsim.h и sync.h.	Alexander Kamkin	Alexander Kamkin	
497	CTESK	Task	Closed	Normal	Добавить assertion в init_Int2PtrHashMap	Alexander Kamkin	Alexey Demakov	2.8
4960	Retrascope IDE	Task	Closed	Normal	Двухшаговый визард для запуска инструментов	Alexander Kamkin	Alexander Kamkin	
760	Veritool	Task	Closed	Normal	Группировка сигналов в массивы	Alexander Kamkin	Alexander Kamkin	
1825	C++TESK Development Environment	Task	New	Normal	Графическое построение сериализаторов и десериализаторов	Alexander Kamkin		



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2052	C++TESK Development Environment	Task	Closed	Normal	Генерация отчета по двойному щелчку мыши	Alexander Kamkin	Alexander Kamkin	
3670	MicroTESK	Task	Closed	High	Генерация кода для модели MMU	Alexander Kamkin	Andrei Tatarnikov	2.3
4889	Retrascope	Task	New	Low	Генерация заготовки эталонной модели по набору блок-схем (flowchart)	Alexander Kamkin	Sergey Smolov	2.0
2163	C++TESK Development Environment	Task	New	Normal	Генерация заготовки тестовой системы по информации об интерфейсе	Alexander Kamkin		
2138	Veritool	Task	New	Normal	Генерация C++ кода вместо C	Alexander Kamkin	Mikhail Chupilko	
4685	C++TESK Testing ToolKit	Bug	Closed	Urgent	Генератор случайный чисел не очень случайный	Alexander Kamkin	Alexander Kamkin	1.0
292	Aspectrace	Task	New	Normal	Генератор отчетов для аспекта FSM	Alexander Kamkin	Sergey Groshev	
2383	Aspectrace	Task	New	Normal	Генератор не должен падать, когда в трассе встречается элемент покрытия, не принадлежащий покрытию	Alexander Kamkin	Anastasia Tugaenko	
4888	Retrascope	Task	New	Low	Генератор заготовки тестовой системы по ее описанию	Alexander Kamkin	Sergey Smolov	2.0
5253	Retrascope	Bug	Closed	Normal	Где-то в планировщике путей есть ошибка	Alexander Kamkin	Alexander Kamkin	
4416	Retrascope	Task	Closed	Normal	Где находится класс для базового блока?	Alexander Kamkin	Alexander Kamkin	
5302	TestBase	Task	Closed	Immediate	Где комит?	Alexander Kamkin	Artem Kotsynyak	
4085	Verilog Translator	Task	Closed	Normal	Вычисление чисел (битовых векторов) и их представление	Alexander Kamkin	Andrei Tatarnikov	
4128	Verilog Translator	Task	Closed	Normal	Вычисление константных выражений	Alexander Kamkin	Alexander Kamkin	0.1
168	CTESK	Bug	Closed	Normal	Выход за границу стека в функции find_nonfinished_state()	Alexander Kamkin		
3360	MicroTESK	Task	Closed	Normal	Выделить подсистему для представления и разрешения ограничений в отдельный подпроект	Alexander Kamkin	Andrei Tatarnikov	
2147	MicroTESK	Task	Closed	Normal	Выделение результата работы солвера в отдельный класс	Alexander Kamkin	Andrei Tatarnikov	
229	CTESK	Task	Closed	Normal	Выделение предусловия стимула в отдельную функцию при трансляции	Alexander Kamkin	Alexey Demakov	2.8
209	CTESK	Feature	Closed	High	Выделение предусловия реакции в отдельную функцию при трансляции	Alexander Kamkin	Alexey Demakov	2.8
3813	Veritool	Bug	Resolved	Normal	В структуре inputs_t нет reset	Alexander Kamkin	Alexander Kamkin	
6506	Castle	Task	Feedback	Normal	Вопросы по представлению массивов	Alexander Kamkin	Alexander Kamkin	0.1
6505	Castle	Task	Feedback	Normal	Вопросы по MetalInfo	Alexander Kamkin	Alexander Kamkin	0.1
3522	Fortress	Task	Closed	Normal	Возможность подключения к узлам дерева операций произвольных объектов	Alexander Kamkin	Alexander Kamkin	0.1
230	CTESK	Task	Closed	Normal	Возможность отключения проверки предусловия стимулов	Alexander Kamkin	Alexey Demakov	2.8
208	CTESK	Feature	Closed	High	Возможность отключения проверки предусловия реакций	Alexander Kamkin	Alexey Demakov	2.8
4013	Retrascope	Task	Closed	Normal	Внутреннее представление HDL-описание	Alexander Kamkin	Alexander Kamkin	

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1540	C++TESK Testing ToolKit	Task	Closed	Normal	Включение в поставку примера account	Alexander Kamkin	Alexander Kamkin	
1174	C++TESK Testing ToolKit	Task	Closed	Normal	Включение в поставку генератора отчетов	Alexander Kamkin	Mikhail Chupilko	
1175	C++TESK Testing ToolKit	Task	Closed	Normal	Включение в поставку Veritool и Icarus Verilog	Alexander Kamkin	Alexander Kamkin	
12228	Track Finding Tools for NICA MPD	Task	Closed	Normal	Визуализация хитов и треков	Alexander Kamkin		
1267	C++TESK Development Environment	Task	New	Normal	Визарды	Alexander Kamkin		
1523	C++TESK Testing ToolKit	Task	Closed	Normal	Билд 1.0.9	Alexander Kamkin	Alexander Kamkin	
1457	C++TESK Testing ToolKit	Task	Closed	Normal	Билд 1.0.8	Alexander Kamkin	Alexander Kamkin	
1416	C++TESK Testing ToolKit	Task	Closed	Normal	Билд 1.0.7	Alexander Kamkin	Alexander Kamkin	
1244	C++TESK Testing ToolKit	Task	Closed	Normal	Билд 1.0.4	Alexander Kamkin	Mikhail Chupilko	
2187	C++TESK Testing ToolKit	Task	Closed	Normal	Билд 1.0.18	Alexander Kamkin	Alexander Kamkin	
2081	C++TESK Testing ToolKit	Task	Closed	Normal	Билд 1.0.16	Alexander Kamkin	Alexander Kamkin	1.0
2013	C++TESK Testing ToolKit	Task	Closed	Normal	Билд 1.0.15	Alexander Kamkin	Alexander Kamkin	
1701	C++TESK Testing ToolKit	Task	Closed	Normal	Билд 1.0.11	Alexander Kamkin	Alexander Kamkin	
1674	C++TESK Testing ToolKit	Task	Closed	Normal	Билд 1.0.10	Alexander Kamkin	Alexander Kamkin	
279	CTESK	Task	New	Normal	Библиотека рандомизации	Alexander Kamkin	Alexey Demakov	
290	Aspectrace	Task	New	Normal	Аспект FSM: трассировка переходов	Alexander Kamkin	Alexey Demakov	
1532	C++TESK Testing ToolKit	Task	Closed	Normal	Архив с инструментом без исходников	Alexander Kamkin	Alexander Kamkin	
4892	Retrascope	Task	New	Low	Анализ набора блок-схем на взаимные блокировки	Alexander Kamkin	Mikhail Lebedev	2.0
796	Veritool	Task	Closed	Normal	Альтернативы VCD	Alexander Kamkin	Alexander Kamkin	
5027	Retrascope	Task	New	Normal	Абстракция EFSM-моделей	Alexander Kamkin	Sergey Smolov	2.0
6325	MicroTESK	Bug	Closed	High	xxx.mmu 19:11 ERROR (SEMANTIC): "Unkown type name 'N'. java.lang.NullPointerException	Alexander Kamkin	Andrei Tatarnikov	2.3

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2744	Retrascope	Task	Closed	Normal	Works on guarded actions in University of Kaiserslautern	Alexander Kamkin	Alexander Kamkin	
3738	MicroTESK	Task	Closed	Normal	Wiki documentation of the template description language	Alexander Kamkin	Andrei Tatarnikov	2.2
7685	MicroTESK	Bug	Rejected	Urgent	warning(24): template error: context [/outputFile /parser /genericParser /region__ /genericParser__members /region__ /genericParser__members] 32:3 no such property or can't access:	Alexander Kamkin	Andrei Tatarnikov	2.4
4915	Retrascope	Task	Closed	Normal	[vhdl][translator] Разработка обертки вокруг zamiaCAD-транслятора	Alexander Kamkin	Alexander Kamkin	0.1
4979	Retrascope	Bug	Closed	Normal	[vhdl][parser] Исключение при запуске VhdlParser	Alexander Kamkin	Alexander Kamkin	0.1
5483	Retrascope	Task	Closed	Normal	[vhdl][parser][cfg] Обработка условий на события при построении CFG	Alexander Kamkin	Sergey Smolov	0.1
5075	Retrascope	Bug	Closed	Normal	[vhdl][parser][cfg] System variable PYTHONPATH is not specified, python will not work	Alexander Kamkin	Sergey Smolov	0.1
10506	Veritool	Bug	Resolved	High	Veritool does not support escaped identifiers	Alexander Kamkin	Mikhail Chupilko	
4872	Retrascope	Task	Closed	Normal	[verilog][translator] Проработать интерфейс backend'a транслятора с Verilog	Alexander Kamkin	Alexander Kamkin	0.1
4907	Retrascope	Task	Closed	High	[verilog][translator] Добавить в проект Verilog-транслятор	Alexander Kamkin	Alexander Kamkin	0.1
5165	Verilog Translator	Bug	Closed	Normal	Verilog static checker raises NullPointerException on test 005	Alexander Kamkin	Alexander Kamkin	0.1
8739	Verilog Translator	Bug	Closed	Normal	VerilogStaticChecker causes an exception when handling variables with parameter-defined length	Alexander Kamkin	Alexander Kamkin	0.1
4898	Retrascope	Task	Closed	High	[verilog][printer][smv] Перенос кода SMV-принтера в проект Retrascope	Alexander Kamkin	Павел Шестаков	1.0
4897	Retrascope	Task	Rejected	Normal	[verilog][printer][smv] Разработка принтера моделей в SMV-формат	Alexander Kamkin	Павел Шестаков	1.0
5024	Retrascope	Task	Rejected	Normal	[verilog][printer][smv] Небрежно оформлен код	Alexander Kamkin	Павел Шестаков	0.1
5025	Retrascope	Task	Rejected	Normal	[verilog][printer][smv] В проект должны быть добавлены Verilog-описания, на которых проводилось тестирование	Alexander Kamkin	Павел Шестаков	0.1
5062	Retrascope	Task	Closed	Normal	[verilog][parser][cfg] Проверка того, что выражение в списке чувствительности является переменной	Alexander Kamkin	Mikhail Chupilko	0.1
4873	Retrascope	Task	Closed	Normal	[verilog][parser][cfg] Построение внутреннего представления по AST	Alexander Kamkin	Mikhail Chupilko	0.1
5040	Retrascope	Task	Closed	Normal	[verilog][parser][cfg] Отсутствуют тесты для Verilog-парсера	Alexander Kamkin	Mikhail Chupilko	0.1
5060	Retrascope	Task	Closed	Normal	[verilog][parser][cfg] Обработка конструкций case	Alexander Kamkin	Mikhail Chupilko	0.1
5061	Retrascope	Task	Closed	Normal	[verilog][parser][cfg] Обработка конструкции непрерывного присваивания assign	Alexander Kamkin	Mikhail Chupilko	0.1
5039	Retrascope	Task	Closed	High	[verilog][parser][cfg] Неправильное оформление кода	Alexander Kamkin	Mikhail Chupilko	0.1
5112	Retrascope	Bug	Closed	Normal	[verilog][parser][cfg] Исключение при обработке описаний с несколькими модулями	Alexander Kamkin	Mikhail Chupilko	0.1
5073	Retrascope	Bug	Closed	Normal	[verilog][parser][cfg] Запуск парсера приводит IllegalStateException	Alexander Kamkin	Alexander Kamkin	0.1
5207	Retrascope	Task	Closed	Normal	[verilog][parser][cfg] Дублирование кода и другие замечания к коду	Alexander Kamkin	Mikhail Chupilko	0.1
5114	Retrascope IDE	Task	Closed	Normal	[verilog][parser][cfg] Вывод пометок на дугах из SwitchNode	Alexander Kamkin	Alexander Protsenko	
9229	Verilog Translator	Bug	Closed	Normal	VerilogTestCases.runTest_03_05_01_3: incorrect token under disabled if-def	Alexander Kamkin	Alexander Kamkin	0.1

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6160	MicroTESK	Bug	Closed	High	VAddrNotEqualPAddrNotEqualDTLBTAGNotReplacedJTLBTAGNotEqualL1TAGReplaced MEMIndexNotEqual is UNSAT	Alexander Kamkin	Alexander Kamkin	2.2
7644	MicroTESK	Task	Closed	Normal	Utility for Tarmac trace comparison	Alexander Kamkin	Sergey Smolov	2.4
6379	MicroTESK	Bug	New	Normal	Using if-then-else in syntax causes an error	Alexander Kamkin	Alexander Kamkin	2.6
8725	Verilog Translator	Task	Closed	Normal	Using Fortress's BitVector in VerilogLiteral	Alexander Kamkin	Alexander Kamkin	0.1
8740	Verilog Translator	Bug	Closed	Normal	Use VerilogGenerateProcessor in VerilogElaborator	Alexander Kamkin	Alexander Kamkin	0.1
6223	MicroTESK	Task	New	Low	User-defined template-oriented extensions (plugins)	Alexander Kamkin	Alexander Kamkin	2.6
4072	Fortress	Bug	Closed	Normal	Unsupported major.minor version 51.0	Alexander Kamkin	Alexander Kamkin	0.1
5211	Fortress	Task	Open	High	Unit-тесты для трансформеров и калькулятора	Alexander Kamkin	Artem Kotsynyak	0.4
5037	Verilog Translator	Task	Rejected	Normal	Unification of If(Statement Generate) и Case(Statement Generate)	Alexander Kamkin	Alexander Kamkin	0.1
4126	Verilog Translator	Task	New	Normal	Unification of AST_EDGE and AST_TABLE_EDGE	Alexander Kamkin	Alexander Kamkin	0.1
9314	MicroTESK	Bug	Closed	Normal	Undesired behavior of situation('random', :size => 32, :sign_extend => true)	Alexander Kamkin	Alexander Kamkin	2.4
8697	MicroTESK	Bug	Closed	Normal	Type mismatch in an mmu file causes NullPointerException	Alexander Kamkin	Andrei Tatarnikov	2.4
10177	Fortress	Bug	Feedback	Normal	TreeVisitor's SKIP status does not work as expected	Alexander Kamkin	Artem Kotsynyak	0.4
4648	MicroTESK	Bug	Closed	Normal	[translator] Функция форматной печати (syntax и image) требует обязательный параметр после форматной строки	Alexander Kamkin	Andrei Tatarnikov	
4658	MicroTESK	Bug	Closed	Normal	[translator] условный оператор -> org.antlr.runtime.tree.RewriteEmptyStreamException:	Alexander Kamkin	Andrei Tatarnikov	
4657	MicroTESK	Bug	Closed	Normal	[translator] Тип в качестве индекса -> java.lang.NullPointerException	Alexander Kamkin	Andrei Tatarnikov	
4647	MicroTESK	Bug	Closed	Normal	[translator] Сообщение об ошибке SYNTESIZE	Alexander Kamkin	Andrei Tatarnikov	
3570	MicroTESK	Task	Closed	High	[translator] Сборка транслятора	Alexander Kamkin	Andrei Tatarnikov	
4645	MicroTESK	Task	Rejected	Normal	[translator] Префикс Instruction излишний	Alexander Kamkin	Andrei Tatarnikov	
3518	MicroTESK	Task	Closed	Normal	[translator] Поддержка директивы include	Alexander Kamkin	Alexander Kamkin	
4095	MicroTESK	Task	Closed	Normal	[translator] Поддержка let PC = "..."	Alexander Kamkin	Andrei Tatarnikov	
4649	MicroTESK	Bug	Closed	Normal	[translator] Падение транслятора	Alexander Kamkin	Andrei Tatarnikov	
4656	MicroTESK	Task	Closed	High	[translator] Возможность инстанцирования операций в других операциях	Alexander Kamkin	Andrei Tatarnikov	2.1
4659	MicroTESK	Task	Closed	Normal	[translator] Ветки elif в условном операторе	Alexander Kamkin	Andrei Tatarnikov	
3277	MicroTESK	Task	Closed	Normal	[translator] Апробация Sim-nML транслятора на примерах MIPS и ARM	Alexander Kamkin	Andrei Tatarnikov	
4056	MicroTESK	Task	Closed	Normal	[translator] Syntax sugaring for X<N..N>	Alexander Kamkin	Andrei Tatarnikov	
5458	MicroTESK	Task	Closed	High	[translator] Support for '.' operator in grammar	Alexander Kamkin	Andrei Tatarnikov	2.1
6064	MicroTESK	Task	New	Low	Translator support for exporting symbols in marks	Alexander Kamkin	Artem Kotsynyak	2.6

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4653	MicroTESK	Bug	Closed	Normal	[translator] Shared.java:30: error: cannot find symbol	Alexander Kamkin	Andrei Tatarnikov	
4652	MicroTESK	Bug	Closed	Normal	[translator] ru.ispras.microtesk.model.elbrus.mode does not exist	Alexander Kamkin	Andrei Tatarnikov	
4655	MicroTESK	Bug	Closed	Normal	[translator] org.antlr.runtime.tree.RewriteEmptyStreamException: rule typeExpr	Alexander Kamkin	Andrei Tatarnikov	
4651	MicroTESK	Bug	Closed	Normal	[translator] NullPointerException на пустом файле	Alexander Kamkin	Alexander Kamkin	
4628	MicroTESK	Task	Closed	Normal	[translator, model] Предопределенная функция trace	Alexander Kamkin	Andrei Tatarnikov	2.1
4832	MicroTESK	Task	Closed	Normal	[translator][model] VLIW architectures support	Alexander Kamkin	Andrei Tatarnikov	2.1
4779	MicroTESK	Bug	Rejected	Normal	[translator, model] Support for aliases	Alexander Kamkin	Andrei Tatarnikov	2.0
4811	MicroTESK	Task	Closed	Normal	[translator, model] Predefined function for raising exceptions	Alexander Kamkin	Andrei Tatarnikov	2.1
5454	MicroTESK	Task	Closed	Normal	[translator] Extensible symbol table	Alexander Kamkin	Andrei Tatarnikov	2.1
4827	MicroTESK	Bug	Closed	Normal	[translator] Error if a mode's name starts with a lower-case letter	Alexander Kamkin	Andrei Tatarnikov	2.0
10044	MicroTESK	Bug	Closed	Normal	Translator crashes with NPE if no instruction is defined	Alexander Kamkin	Artem Kotsynyak	2.5
4676	Fortress	Task	Closed	Normal	[transform] Реализация однократных подстановок	Alexander Kamkin	Andrei Tatarnikov	0.3
4677	Fortress	Task	Closed	Normal	[transform] Канонизация элементарных логических формул	Alexander Kamkin	Artem Kotsynyak	0.3
5213	Fortress	Task	Closed	High	[transformer] Отсутствуют комментарии к трансформерам	Alexander Kamkin	Artem Kotsynyak	0.3
5209	Fortress	Task	Closed	Normal	[transformer] Осуществление подстановок для узлов типа BINDING	Alexander Kamkin	Artem Kotsynyak	0.3
5208	Fortress	Task	Closed	Normal	[transformer] Не учитываются узлы типа BINDING при упрощении выражения и др.	Alexander Kamkin	Artem Kotsynyak	0.3
6119	MicroTESK	Task	Closed	Normal	Tracking addresses in indirect branches (label propagation, address arithmetic, etc.)	Alexander Kamkin	Andrei Tatarnikov	2.2
4805	TestBase	Task	New	Normal	Top-level test situations of instructions	Alexander Kamkin	Andrei Tatarnikov	0.0
5044	Retrascope IDE	Task	Closed	Normal	[tool][launcher] Добавление результатов работы инструментов в проект	Alexander Kamkin	Alexander Kamkin	
5100	Retrascope IDE	Task	Closed	Normal	[tool][launcher] Добавить в окно ProgressBar	Alexander Kamkin	Alexander Kamkin	
4961	Retrascope IDE	Task	New	Normal	[tool][configurator] Конструктор инструментальных цепочек	Alexander Kamkin	Alexander Kamkin	0.1
5057	Retrascope IDE	Bug	New	Normal	[tool][configurator] Для низкоуровневого конфигуратора Arguments не нужно выдавать --arguments	Alexander Kamkin	Alexander Kamkin	0.1
5043	Retrascope IDE	Bug	Closed	Normal	[tool][configurator] Возможность удаления добавленных файлов в FilesConfigurator	Alexander Kamkin	Alexander Kamkin	
5056	Retrascope IDE	Task	Closed	Normal	[tool][configurator] Возможность выбора файлов через диалоговое окно в TextConfigurator	Alexander Kamkin	Alexander Kamkin	
2057	C++TESK Development Environment	Task	Closed	Normal	Toogle C++TESK Highlighting -> Enable C++TESK Highlighting   Disable C++TESK Highlighting	Alexander Kamkin	Alexander Kamkin	
4071	Fortress	Task	Closed	Normal	Thoughts/questions on EVerilogOperation	Alexander Kamkin	Alexander Kamkin	0.1
6088	MicroTESK	Bug	Closed	High	The use of deprecated methods	Alexander Kamkin	Artem Kotsynyak	2.2
4016	MicroTESK	Task	Rejected	Normal	The nML Language Reference	Alexander Kamkin	Andrei Tatarnikov	
6328	MicroTESK	Bug	Closed	Normal	"The image attribute is not defined for the Op primitive."	Alexander Kamkin	Andrei Tatarnikov	2.3
10187	MicroTESK	Bug	Closed	Normal	testWriteThroughAllocationInclusive: lw core=2, address=d058: deadbeef != a8c7e6ae	Alexander Kamkin	Alexander Kamkin	2.5

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5384	MicroTESK	Task	Closed	Urgent	Test templates examples for the miniMIPS ISA	Alexander Kamkin	Andrei Tatarnikov	2.1
5811	TestBase	Task	New	Normal	Test data generators for basic arithmetic SIMD instructions	Alexander Kamkin	Alexander Kamkin	0.0
4252	MicroTESK	Task	Closed	Urgent	Test coverage extraction	Alexander Kamkin	Artem Kotsynyak	2.2
6117	MicroTESK	Task	Closed	Normal	Test case-level prologue and epilogue	Alexander Kamkin	Andrei Tatarnikov	2.2
2523	MicroTESK	Task	Closed	Immediate	[template] Разработка Ruby-библиотеки для описания шаблонов тестовых программ	Alexander Kamkin	Andrei Tatarnikov	
3987	MicroTESK	Task	Closed	Normal	[template] Примеры шаблонов для ARM	Alexander Kamkin	Andrei Tatarnikov	2.0
4094	MicroTESK	Task	Closed	High	[template] Отладка шаблона Euclide	Alexander Kamkin	Andrei Tatarnikov	2.0
2522	MicroTESK	Task	Closed	High	[template] Интеграция Ruby-шаблонов с солверами	Alexander Kamkin	Andrei Tatarnikov	2.0
4820	MicroTESK	Bug	Closed	Normal	[template] Test program header	Alexander Kamkin	Andrei Tatarnikov	2.0
5386	MicroTESK	Bug	Closed	High	[template] Templates are not launched in Windows!	Alexander Kamkin	Andrei Tatarnikov	2.1
5411	MicroTESK	Task	Closed	High	[template] Template constructs for data declaration	Alexander Kamkin	Andrei Tatarnikov	2.1
4816	MicroTESK	Task	Closed	Normal	[template] Rename lib/ruby/mtruby	Alexander Kamkin	Andrei Tatarnikov	2.0
6789	MicroTESK	Bug	Closed	Urgent	Template processor ignores empty blocks	Alexander Kamkin	Andrei Tatarnikov	2.3
4792	MicroTESK	Task	Closed	Normal	[template] Newline doesn't work	Alexander Kamkin	Andrei Tatarnikov	2.1
10122	MicroTESK	Task	New	Normal	Template libraries for standard features	Alexander Kamkin	Alexander Kamkin	2.6
4775	MicroTESK	Bug	Closed	Urgent	[template] generate.sh fails	Alexander Kamkin	Andrei Tatarnikov	2.0
6037	MicroTESK	Task	Closed	Normal	Template functions for setting up indentToken and separatorToken	Alexander Kamkin	Andrei Tatarnikov	2.2
4809	MicroTESK	Task	Closed	Normal	[template] Facilities for defining/using labels in test templates	Alexander Kamkin	Andrei Tatarnikov	2.1
4791	MicroTESK	Bug	Closed	Normal	[template] Exception if Z3 is not installed	Alexander Kamkin	Andrei Tatarnikov	2.0
4815	MicroTESK	Bug	Closed	Normal	[template] Directory \$MICROTESK_HOME/tools should be used instead of `pwd`/tools	Alexander Kamkin	Andrei Tatarnikov	2.0
4818	MicroTESK	Task	Closed	Normal	[template] Configuration files	Alexander Kamkin	Andrei Tatarnikov	2.1
4819	MicroTESK	Task	Closed	Normal	[template] Auxiliary test template functions	Alexander Kamkin	Andrei Tatarnikov	2.1
5271	Retrascope IDE	Task	New	Normal	[task][configurator] Деактивация кнопки ОК	Alexander Kamkin	Alexander Kamkin	0.1
5252	Retrascope IDE	Task	Closed	Normal	[target][configurator] Группировка целей по префиксу	Alexander Kamkin	Alexander Kamkin	
5038	Verilog Translator	Task	Closed	Normal	Taking into account X- and Z-don't cares in CaseStatementItem.getExpression().	Alexander Kamkin	Alexander Kamkin	0.1
4383	Verilog Translator	Task	Closed	Normal	Tag DECLARATION + enum { INPUT, OUTPUT, etc.}	Alexander Kamkin	Alexander Kamkin	
4065	Fortress	Task	Closed	Normal	TABs should be replaced to 4 spaces	Alexander Kamkin	Alexander Kamkin	0.1
3489	Verilog Translator	Bug	Closed	Normal	(SYSTEM_)TASK_ENABLE -> (SYSTEM_)TASK_ENABLE_STATEMENT	Alexander Kamkin	Alexander Kamkin	
9910	MicroTESK	Bug	Closed	Normal	Symbolic executor fails to construct CFG	Alexander Kamkin	Artem Kotsynyak	2.5
3593	Verilog Translator	Task	Closed	Low	Support of UDP declaration and instantiation	Alexander Kamkin	Alexander Kamkin	

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4117	Fortress	Task	Closed	High	Support of replication operation	Alexander Kamkin	Alexander Kamkin	0.1
3594	Verilog Translator	Bug	Closed	Low	Support of drive strength	Alexander Kamkin	Alexander Kamkin	
10168	MicroTESK	Task	Closed	Normal	Support for Write-Back policy in cache specification	Alexander Kamkin	Alexander Kamkin	2.5
6539	MicroTESK	Task	Closed	Normal	Support for variants in preparators and comparators	Alexander Kamkin	Andrei Tatarnikov	2.3
6377	MicroTESK	Task	Rejected	High	Support for user-defined attributes	Alexander Kamkin	Alexander Kamkin	2.3
6167	MicroTESK	Task	Closed	Normal	Support for the 'width' attribute of addresses	Alexander Kamkin	Artem Kotsynyak	2.2
5753	MicroTESK	Task	Closed	Urgent	Support for the repetition operation	Alexander Kamkin	Andrei Tatarnikov	2.2
4075	Fortress	Task	Closed	Normal	Support for the case equality/inequality (=== and !==) operators	Alexander Kamkin	Alexander Kamkin	0.1
4123	Verilog Translator	Task	Closed	Low	Support for system timing check	Alexander Kamkin	Alexander Kamkin	0.3
6205	MicroTESK	Task	Closed	Normal	Support for STRUCT in MMU specification language	Alexander Kamkin	Artem Kotsynyak	2.2
6030	MicroTESK	Task	Rejected	Normal	Support for specifying implementation-defined behavior	Alexander Kamkin	Andrei Tatarnikov	2.2
4108	Verilog Translator	Task	Closed	Normal	Support for specify constructs	Alexander Kamkin	Alexander Kamkin	
6313	MicroTESK	Task	Closed	Normal	Support for shifts and bitwise operations in MMU specs	Alexander Kamkin	Andrei Tatarnikov	2.3
4107	Verilog Translator	Bug	Closed	Normal	Support for pulse control in specparam assignment	Alexander Kamkin	Alexander Kamkin	0.1
4124	Verilog Translator	Bug	Closed	Normal	Support for port attributes in a module's signature	Alexander Kamkin	Alexander Kamkin	0.1
6189	MicroTESK	Task	Closed	Normal	Support for page table declaration	Alexander Kamkin	Andrei Tatarnikov	2.2
5942	MicroTESK	Task	Closed	Normal	Support for memory segments specification	Alexander Kamkin	Alexander Kamkin	2.3
6207	MicroTESK	Task	Closed	Normal	Support for MARK in MMU specification language	Alexander Kamkin	Artem Kotsynyak	2.2
5752	MicroTESK	Task	Rejected	Normal	Support for macro	Alexander Kamkin	Andrei Tatarnikov	2.2
4106	Verilog Translator	Task	New	Low	Support for library and config declaration	Alexander Kamkin	Alexander Kamkin	0.3
6203	MicroTESK	Task	Closed	Normal	Support for LET in MMU specification language	Alexander Kamkin	Artem Kotsynyak	2.2
6461	MicroTESK	Task	New	Normal	Support for inline comments	Alexander Kamkin	Alexander Kamkin	2.6
4105	Verilog Translator	Task	Closed	Normal	Support for gate (primitive) instantiation	Alexander Kamkin	Alexander Kamkin	
6318	MicroTESK	Task	Closed	Normal	Support for functions in MMU language	Alexander Kamkin	Artem Kotsynyak	2.3
7395	MicroTESK	Task	Closed	Urgent	Support for external code and global labels	Alexander Kamkin	Andrei Tatarnikov	2.3
6152	MicroTESK	Task	Closed	Normal	Support for disjunctive constraints in MMU specifications	Alexander Kamkin	Alexander Kamkin	2.3
6089	MicroTESK	Task	Closed	Normal	Support for data streams in test templates	Alexander Kamkin	Andrei Tatarnikov	2.2
6115	MicroTESK	Task	Closed	Normal	Support for buffer preparators	Alexander Kamkin	Andrei Tatarnikov	2.3

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7726	MicroTESK	Task	Closed	High	Support for binary masks in preparators	Alexander Kamkin	Andrei Tatarnikov	2.4
6121	MicroTESK	Task	Closed	Normal	Support for basic preprocessor constructs	Alexander Kamkin	Alexander Kamkin	2.2
10178	MicroTESK	Task	Closed	Normal	Support for basic cache coherence protocols in MMU specifications	Alexander Kamkin	Alexander Kamkin	2.5
6187	MicroTESK	Task	Closed	Normal	Support for address object declaration	Alexander Kamkin	Alexander Kamkin	2.2
6066	MicroTESK	Task	Closed	Urgent	Support for adding user-defined Java code	Alexander Kamkin	Andrei Tatarnikov	2.2
10106	MicroTESK	Task	New	Normal	Support %b as format's specifier	Alexander Kamkin	Alexander Kamkin	2.6
6035	MicroTESK	Bug	Closed	Normal	Superfluous initialization code	Alexander Kamkin	Andrei Tatarnikov	2.2
6124	Fortress	Task	Closed	Normal	Suggestion on InvariantChecks.check* methods	Alexander Kamkin	Andrei Tatarnikov	0.4
6087	MicroTESK	Task	Closed	High	Suggestion for the Solver interface	Alexander Kamkin	Artem Kotsyniyak	2.2
7646	MicroTESK	Task	Closed	Normal	Structures in nML	Alexander Kamkin	Andrei Tatarnikov	2.4
4084	Fortress	Task	Rejected	Normal	String data type required	Alexander Kamkin	Alexander Kamkin	0.1
4076	Fortress	Bug	Closed	Normal	Strange hashCode methods	Alexander Kamkin	Alexander Kamkin	0.1
412	Veritool	Task	Closed	Normal	start_testbench() и stop_testbench()	Alexander Kamkin	Alexander Kamkin	
1665	Veritool	Task	Feedback	Normal	start_sцен должен быть в библиотеке репозитория тестов	Alexander Kamkin	Alexander Kamkin	
6161	MicroTESK	Task	Rejected	Normal	Standard (abstract sequence-based) MMU template representation	Alexander Kamkin	Alexander Kamkin	2.2
5325	TestBase	Task	New	Normal	Sqrt hard-to-round iterator for the nearest rounding needs to be reviewed	Alexander Kamkin	Alexander Kamkin	0.0
7706	TestBase	Task	Closed	Normal	SQLite-based storage backend	Alexander Kamkin	Alexander Dyumaev	
4032	MicroTESK	Task	Rejected	Normal	Specification of the miniMIPS pipelining	Alexander Kamkin	Artem Kotsyniyak	
6148	MicroTESK	Task	Closed	Normal	Specification of 'no-touch' and 'share' zones of memory	Alexander Kamkin	Alexander Kamkin	2.2
9846	Aspectrace	Task	Resolved	Normal	Source code formatting and improvement	Alexander Kamkin	Ivan Grigorov	
9978	MicroTESK for RISC-V	Task	New	Normal	Some templates use rand(-2147483648, 2147483647) for randomizing words	Alexander Kamkin	Alexander Kamkin	0.1
5220	Fortress	Task	Closed	Normal	[solver] Переименовать SolverUtils	Alexander Kamkin	Andrei Tatarnikov	0.3
5216	Fortress	Task	Closed	Normal	[solver] Вспомогательные методы работы с логическими формулами	Alexander Kamkin	Andrei Tatarnikov	0.3
5155	Fortress	Task	Closed	Urgent	[solver][constraint] Поддержка конструкции связывания переменных	Alexander Kamkin	Artem Kotsyniyak	0.3
3661	C++TESK Testing Toolkit	Task	Closed	Normal	Soft correctness criteria	Alexander Kamkin	Alexander Kamkin	1.0
4086	Retrascope	Task	Rejected	Normal	[smv][example] Разобраться с инструментом SMV	Alexander Kamkin	Mikhail Lebedev	1.0
5747	MicroTESK	Task	Closed	Normal	Simple infinite loop detection	Alexander Kamkin	Andrei Tatarnikov	2.2
8691	MicroTESK	Task	New	Normal	Sign- and zero-extend functions in preparators	Alexander Kamkin	Alexander Kamkin	2.6
5311	MicroTESK	Task	New	Normal	Shortcuts for getting modes from templates	Alexander Kamkin	Alexander Kamkin	2.6
291	Aspectrace	Task	Closed	High	Shell-скрипт для запуска генератора	Alexander Kamkin	Alexander Kamkin	



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4408	Verilog Translator	Task	Closed	Normal	Scope.YES & Scope.NO	Alexander Kamkin	Alexander Kamkin	0.1
9261	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_19_03_01_1: line 1:4 mismatched input ')' expecting LPAREN	Alexander Kamkin	Alexander Kamkin	0.1
9255	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_14_06_04_2_3: no viable alternative	Alexander Kamkin	Alexander Kamkin	0.1
9259	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_12_04_02_4: Task declaration has not been found	Alexander Kamkin	Alexander Kamkin	0.1
9258	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_12_04_02_1 STANDARD_OUT	Alexander Kamkin	Alexander Kamkin	0.1
9257	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_12_04_01_5: IllegalArgumentException	Alexander Kamkin	Alexander Kamkin	0.1
9256	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_12_02_00_1: NullPointerException	Alexander Kamkin	Alexander Kamkin	0.1
9260	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_10_02_02_1_2: StackOverflowError	Alexander Kamkin	Alexander Kamkin	0.1
9262	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_09_06_00_1: Descriptor has not been found	Alexander Kamkin	Alexander Kamkin	0.1
9254	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_07_08_00_1: no viable alternative	Alexander Kamkin	Alexander Kamkin	0.1
9253	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_07_01_02_1: no viable alternative	Alexander Kamkin	Alexander Kamkin	0.1
9252	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_05_01_05_2: Cannot convert a real to a bitvector	Alexander Kamkin	Alexander Kamkin	0.1
4409	Fortress	Bug	Closed	Normal	ru/ispras/solver/core/syntax/EStandardOperation.java:212: inconvertible types	Alexander Kamkin	Alexander Kamkin	0.2
6438	MicroTESK	Task	Closed	Normal	Reusing blocks in test templates	Alexander Kamkin	Andrei Tatarnikov	2.3
8672	MicroTESK	Task	Closed	Normal	Renaming nontaken to nonexecuted in test templates	Alexander Kamkin	Andrei Tatarnikov	2.4
4074	Verilog Translator	Task	Closed	Normal	Rename STAR2	Alexander Kamkin	Alexander Kamkin	
4073	Verilog Translator	Task	Closed	Normal	Rename SL, SR, SL3 and SR3	Alexander Kamkin	Alexander Kamkin	
4068	Verilog Translator	Task	Closed	Normal	Rename LT_, LE, GT and GE	Alexander Kamkin	Alexander Kamkin	
4391	Verilog Translator	Task	Closed	Normal	Rename FOR_GENERATE to LOOP_GENERATE	Alexander Kamkin	Alexander Kamkin	
5980	MicroTESK	Task	Closed	High	Referring to normal branches (paths with no exceptions, unpredicted, etc.)	Alexander Kamkin	Artem Kotsynyak	2.2
4814	MicroTESK	Task	Rejected	Normal	Refactoring of L-expressions and memory model	Alexander Kamkin	Andrei Tatarnikov	2.2
7681	MicroTESK	Bug	Closed	Urgent	README file is incorrect	Alexander Kamkin	Andrei Tatarnikov	2.4

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5999	MicroTESK	Task	Closed	High	Randomization of operands of instructions with unspecified situations	Alexander Kamkin	Andrei Tatarnikov	2.2
4067	Fortress	Bug	Closed	Normal	Questions on ADD and MINUS	Alexander Kamkin	Alexander Kamkin	0.1
4070	Fortress	Bug	Closed	Normal	Question on the ternary conditional operation (cond ? lhs : rhs)	Alexander Kamkin	Alexander Kamkin	0.1
4682	Fortress	Task	Closed	Normal	[project] Отделить тесты от исходников	Alexander Kamkin	Andrei Tatarnikov	0.3
4644	MicroTESK	Task	Rejected	Normal	[project] Зачем нужна папка microtesk в trunk	Alexander Kamkin	Andrei Tatarnikov	
6070	MicroTESK	Task	Rejected	Normal	Processing of complex situations	Alexander Kamkin	Artem Kotsynyak	2.2
6072	MicroTESK	Task	Rejected	Low	Processing inline situations	Alexander Kamkin	Artem Kotsynyak	2.3
10299	Fortress	Bug	Closed	Normal	Probably, a bug in BitVector.isAll{Set,Reset}	Alexander Kamkin	Sergey Smolov	0.4
5933	MicroTESK	Task	Closed	Normal	Printing test generation statistics	Alexander Kamkin	Andrei Tatarnikov	2.2
10095	MicroTESK for RISC-V	Bug	Closed	Normal	print_imm12 seems to be redundant	Alexander Kamkin	Alexander Protsenko	0.1
5300	Java SoftFloat	Task	Closed	Normal	Porting SoftFloat to Java	Alexander Kamkin	Alexander Protsenko	
8695	MicroTESK	Bug	Closed	Normal	Parsing an mmu-file with no mmu declaration causes IllegalArgumentException	Alexander Kamkin	Artem Kotsynyak	2.5
8696	MicroTESK	Bug	New	Normal	Parsing an mmu-file with empty read and write definitions causes IllegalArgumentException	Alexander Kamkin	Artem Kotsynyak	2.6
8694	MicroTESK	Bug	Closed	Normal	Parsing an empty *.mmu file causes NullPointerException	Alexander Kamkin	Artem Kotsynyak	2.5
5149	Retrascope	Task	Closed	Normal	[parser][cfg] Слияние процессов непрерывного присваивания	Alexander Kamkin	Sergey Smolov	0.1
3487	Verilog Translator	Bug	Closed	Normal	par_block   seq_block -> block_statement	Alexander Kamkin	Alexander Kamkin	
6214	MicroTESK	Task	Closed	High	Overriding configuration options from templates	Alexander Kamkin	Andrei Tatarnikov	2.2
5409	Fortress	Task	Closed	Normal	Orthogonalizer refactoring etc.	Alexander Kamkin	Alexander Kamkin	0.3
6113	MicroTESK	Bug	Rejected	Normal	org.jruby.exceptions.RaiseException: (ArgumentError) wrong number of arguments	Alexander Kamkin	Andrei Tatarnikov	2.2
5978	MicroTESK	Task	Closed	Normal	Options related to splitting instruction stream into parts	Alexander Kamkin	Andrei Tatarnikov	2.2
5996	MicroTESK	Task	Closed	Normal	Options for turning on/off comments	Alexander Kamkin	Andrei Tatarnikov	2.2
6024	MicroTESK	Task	Closed	Urgent	Option for specifying arch directories	Alexander Kamkin	Andrei Tatarnikov	2.2
7729	MicroTESK	Task	New	Normal	On-demand memory access path exploration (coverage extraction)	Alexander Kamkin	Alexander Kamkin	2.6
6378	MicroTESK	Bug	Rejected	Normal	No way to jump from epilogue to a label defined in prologue	Alexander Kamkin	Andrei Tatarnikov	
8683	MicroTESK for MIPS	Bug	Resolved	Normal	Nonoptimal preparator generation	Alexander Kamkin	Alexander Kamkin	0.0
6333	MicroTESK	Bug	Rejected	Normal	NmlTreeWalker.g: node from line 0:0 no viable alternative at input	Alexander Kamkin	Andrei Tatarnikov	2.3
7679	MicroTESK	Bug	Closed	Normal	New section in ChangeLog required	Alexander Kamkin	Andrei Tatarnikov	2.4
4212	Fortress	Task	Closed	Normal	New build	Alexander Kamkin	Alexander Kamkin	0.1
3742	C++TESK Testing ToolKit	Task	Closed	Normal	New build	Alexander Kamkin	Alexander Kamkin	

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6052	MicroTESK	Task	Closed	Normal	Multiple preparators per mode (index range, value mask, etc.)	Alexander Kamkin	Andrei Tatarnikov	2.2
6372	MicroTESK	Task	Closed	Normal	Mode's attribute number	Alexander Kamkin	Andrei Tatarnikov	2.3
4825	MicroTESK	Task	Rejected	Normal	[model] Modeling a branch delay slot	Alexander Kamkin	Alexander Kamkin	2.0
4824	MicroTESK	Task	Closed	Normal	[model] Floating-point arithmetic support	Alexander Kamkin	Andrei Tatarnikov	2.1
4093	MicroTESK	Task	Rejected	High	[mmu] Функции доступа к памяти	Alexander Kamkin	Taya Sergeeva	2.3
4055	MicroTESK	Task	Closed	Normal	[mmu] Определение политики вытеснения PLRU	Alexander Kamkin	Taya Sergeeva	
4054	MicroTESK	Task	Rejected	Normal	[mmu] Идентификатор политики вытеснения	Alexander Kamkin	Taya Sergeeva	2.3
4254	MicroTESK	Task	Closed	High	[mmu] Замечания по коду пакета MMU	Alexander Kamkin	Taya Sergeeva	
6277	MicroTESK	Bug	Closed	Normal	MMU translator does not support X<...>:Y	Alexander Kamkin	Andrei Tatarnikov	2.3
4249	MicroTESK	Bug	Closed	Normal	[mmu] TAB -> spaces	Alexander Kamkin	Taya Sergeeva	
6294	MicroTESK	Task	Closed	Normal	MmuSubsystem.Builder.setSettings(GeneratorSettings)	Alexander Kamkin	Andrei Tatarnikov	2.3
6332	MicroTESK	Bug	Closed	Normal	mismatched input '-' expecting SEMI"	Alexander Kamkin	Andrei Tatarnikov	2.3
4255	MicroTESK	Task	Closed	Normal	[mips] Добавить в проект модель MIPS	Alexander Kamkin	Andrei Tatarnikov	
4031	MicroTESK	Task	Rejected	Normal	[mips] Specification of miniMIPS's instruction set	Alexander Kamkin	Taya Sergeeva	
4822	MicroTESK	Bug	Closed	Normal	[mips][model] tmp_signed_word & tmp_signed_word1	Alexander Kamkin	Andrei Tatarnikov	2.0
5383	MicroTESK	Task	Closed	Normal	[minimips][model] Specification of the miniMIPS ISA	Alexander Kamkin	Platon Bibik	2.1
6001	MicroTESK	Task	Closed	Normal	MicroTESK should report an error if no preparator is specified	Alexander Kamkin	Andrei Tatarnikov	2.2
9336	MicroTESK	Bug	Closed	Normal	MicroTESK is not able to disassemble euclid binary (RISC-V)	Alexander Kamkin	Alexander Kamkin	2.4
6234	MicroTESK	Bug	Closed	Normal	MicroTESK does not simulate checking code	Alexander Kamkin	Andrei Tatarnikov	2.2
6085	MicroTESK	Bug	Closed	Immediate	MicroTESK 2.2.6 news + closed bugs	Alexander Kamkin	Andrei Tatarnikov	2.2
5725	TestBase	Task	New	Normal	Meta-information provider required	Alexander Kamkin	Artem Kotsynyak	0.0
4385	Verilog Translator	Task	Closed	Normal	Merging FUNCTION and TASK tags	Alexander Kamkin	Alexander Kamkin	
5092	Verilog Translator	Task	Closed	Normal	Merging declaration parts for the same variable	Alexander Kamkin	Alexander Kamkin	0.1
297	CTESK	Bug	Closed	Normal	Memory leak in tracer	Alexander Kamkin	Alexey Demakov	2.8
4096	MicroTESK	Task	Closed	Normal	Memory access handler	Alexander Kamkin	Andrei Tatarnikov	2.3
6028	MicroTESK	Task	Closed	Normal	Mark explicitly specified registers (addressing modes) as 'used'	Alexander Kamkin	Andrei Tatarnikov	2.2
4804	TestBase	Task	New	Normal	Manual description of test situations in Java and XML	Alexander Kamkin	Andrei Tatarnikov	0.0
4778	TestBase	Task	Open	Urgent	Low-level interface for adding test situations (named constraints)	Alexander Kamkin	Andrei Tatarnikov	0.0
4956	C++TESK Testing ToolKit	Bug	New	Normal	[log] Сообщение при удалении реакции по тайм-ауту	Alexander Kamkin	Mikhail Chupilko	1.0
6308	MicroTESK	Task	Closed	High	Link between ISA specs and MMU specs	Alexander Kamkin	Andrei Tatarnikov	2.3

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5036	MicroTESK	Task	Closed	Normal	Lazy construction of test situation constraints	Alexander Kamkin	Artem Kotsynyak	2.3
4932	C++TESK Testing ToolKit	Task	Resolved	Normal	[lang][log] Переопределение вывода полей сообщений	Alexander Kamkin	Alexander Kamkin	1.0
4955	C++TESK Testing ToolKit	Task	New	Normal	[lang][core] Способы работы с типами данных uint128_t, uint256_t и т.д.	Alexander Kamkin	Alexander Kamkin	1.0
1828	C++TESK Testing ToolKit	Task	New	Normal	[lang][core] Implementation of temporal sequences	Alexander Kamkin	Alexander Kamkin	1.1
6112	MicroTESK	Bug	Closed	Urgent	Labels are not unique	Alexander Kamkin	Andrei Tatarnikov	2.2
6111	MicroTESK	Bug	Closed	Normal	Labels are not printed	Alexander Kamkin	Andrei Tatarnikov	2.2
6330	MicroTESK	Bug	Closed	Normal	java.util.NoSuchElementException: can't look backwards more than one token in this stream	Alexander Kamkin	Andrei Tatarnikov	2.3
6462	MicroTESK	Bug	Closed	Urgent	java.lang.UnsupportedOperationException: isHit is unsupported for mapped buffers.	Alexander Kamkin	Andrei Tatarnikov	2.3
6329	MicroTESK	Bug	New	Normal	java.lang.NullPointerException at ru.ispras.microtesk.mmu.translator.MmuTreeWalkerBase.newAttributeRef(MmuTreeWalkerBase.java:940)	Alexander Kamkin	Alexander Kamkin	2.6
6478	MicroTESK	Bug	Closed	Normal	java.lang.IllegalStateException: Multiple hits in a cache set	Alexander Kamkin	Andrei Tatarnikov	2.3
6376	MicroTESK	Bug	Closed	High	java.lang.AssertionError: Operation.image - default implementation. Should never be called!	Alexander Kamkin	Andrei Tatarnikov	2.3
10179	MicroTESK	Bug	Closed	High	java.lang.AssertionError: lw core=0, address=1c: deadbeef != d3d5e5f9	Alexander Kamkin	Alexander Kamkin	2.5
7649	MicroTESK	Task	Closed	Urgent	Iterator-based implementation of MMU path enumeration	Alexander Kamkin	Artem Kotsynyak	2.4
5732	MicroTESK	Task	Rejected	Normal	ISA reference manual generation	Alexander Kamkin	Alexander Kamkin	2.2
5877	Fortress	Task	Closed	Normal	InvariantChecks: checkTrue and checkFalse methods	Alexander Kamkin	Andrei Tatarnikov	0.4
10507	Veritool	Task	New	Normal	Introduce versions and close the resolved issues	Alexander Kamkin	Mikhail Chupilko	
6171	MicroTESK	Task	Closed	Normal	Introduce address attributes	Alexander Kamkin	Artem Kotsynyak	2.2
6054	MicroTESK	Task	Closed	High	Interface method for checking whether an instruction is capable of throwing an exception	Alexander Kamkin	Andrei Tatarnikov	2.2
6306	MicroTESK	Task	Closed	High	Integration with JSFloat	Alexander Kamkin	Andrei Tatarnikov	2.3

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