

## Issues

#	Project	Tracker	Status	Priority	Subject	Author	Assignee	Target version
3779	Retrascope	Task	Rejected	Normal	Статья про конфликты в процессорах	Alexander Kamkin	Alexander Chudnovskiy	
7706	TestBase	Task	Closed	Normal	SQLite-based storage backend	Alexander Kamkin	Alexander Dyumaev	
1210	Veritool	Task	Closed	Normal	\$applyAction() + \$checkAction = too much	Alexander Kamkin	Alexander Kamkin	
3095	C++TESK Testing ToolKit	Task	Closed	Normal	20-ый билд перед отпуском	Alexander Kamkin	Alexander Kamkin	
4392	Verilog Translator	Task	Closed	Normal	Add return value for on<Node>(Begin End) methods	Alexander Kamkin	Alexander Kamkin	0.1
4670	C++TESK Testing ToolKit	Task	Closed	Normal	All resolved issues should be closed	Alexander Kamkin	Alexander Kamkin	
5955	MicroTESK	Task	Closed	Normal	Architecture-dependent configuration	Alexander Kamkin	Alexander Kamkin	2.2
3541	Verilog Translator	Bug	Closed	Normal	AST_ASSIGNMENT_STATEMENT -> AST_ASSIGN_STATEMENT	Alexander Kamkin	Alexander Kamkin	
3474	Verilog Translator	Task	Closed	Normal	AST_ASSIGNMENT для параметров	Alexander Kamkin	Alexander Kamkin	
3483	Verilog Translator	Task	Closed	Normal	AST_BODY_GENERATE -> AST_GENERATE_BLOCK	Alexander Kamkin	Alexander Kamkin	
4110	Verilog Translator	Bug	New	Normal	AST_DECLARATION: adding delay for net declarations	Alexander Kamkin	Alexander Kamkin	0.1
3485	Verilog Translator	Task	Closed	Normal	AST_DISCRETE_ASSIGNMENT -> AST_ASSIGNMENT_STATEMENT	Alexander Kamkin	Alexander Kamkin	
3486	Verilog Translator	Task	Closed	Normal	AST_EVENT_TRIGGER -> AST_EVENT_TRIGGER_STATEMENT	Alexander Kamkin	Alexander Kamkin	
4111	Verilog Translator	Task	Closed	Normal	AST_INSTANTIATION: += AST_STRENGTH	Alexander Kamkin	Alexander Kamkin	
3542	Verilog Translator	Task	Closed	Normal	AST_MODULE -> AST_MODULE_TYPE, AST_*_DECLARATION -> AST_*	Alexander Kamkin	Alexander Kamkin	
3480	Verilog Translator	Task	Closed	Normal	AST_PORT_CONNECTION += AST_ATTRIBUTES	Alexander Kamkin	Alexander Kamkin	
3537	Verilog Translator	Task	Closed	Normal	AST_PROCESS -> AST_ACTIVITY	Alexander Kamkin	Alexander Kamkin	
3459	Verilog Translator	Task	Closed	Normal	AST_TYPE structure refactoring	Alexander Kamkin	Alexander Kamkin	
1777	C++TESK Testing ToolKit	Task	Closed	Normal	Attach the user documents to 'Documents'	Alexander Kamkin	Alexander Kamkin	
4808	MicroTESK	Task	Rejected	Normal	Automated extraction of initialization code	Alexander Kamkin	Alexander Kamkin	

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4947	Retrascope	Task	Closed	Normal	[basis][engine] Описатель параметров командной строки	Alexander Kamkin	Alexander Kamkin	0.1
4923	Retrascope	Task	Closed	Normal	[basis][log] Подсистема логирования	Alexander Kamkin	Alexander Kamkin	0.1
4876	Retrascope	Task	Closed	Normal	[basis][testgen] Разработка представления тестовой последовательности	Alexander Kamkin	Alexander Kamkin	0.1
4985	Retrascope	Bug	Closed	Normal	[basis] the 'test' target is unreachable	Alexander Kamkin	Alexander Kamkin	0.1
4906	Retrascope	Task	Closed	High	[basis] Интерфейсы базовых сущностей Retrascope	Alexander Kamkin	Alexander Kamkin	0.1
4995	Retrascope	Bug	Closed	Normal	[basis] Ошибочный порядок применения инструментов	Alexander Kamkin	Alexander Kamkin	0.1
5088	Retrascope	Task	Closed	Normal	[basis] Поддержка параметров командной строки с предопределенным набором значений	Alexander Kamkin	Alexander Kamkin	0.1
4066	Fortress	Bug	Closed	Normal	Bitwise XOR operaton is not supported	Alexander Kamkin	Alexander Kamkin	0.1
4335	Verilog Translator	Bug	Closed	Normal	bootstrap class path not set in conjunction with source 1.6	Alexander Kamkin	Alexander Kamkin	
9390	MicroTESK	Bug	Closed	Normal	Bug in code allocation	Alexander Kamkin	Alexander Kamkin	2.4
6289	MicroTESK	Bug	Closed	Normal	Bug in the MMU solver	Alexander Kamkin	Alexander Kamkin	2.3
4333	Fortress	Task	Closed	Normal	Build 0.2.2 with support of calculators	Alexander Kamkin	Alexander Kamkin	0.2
2580	C++TESK Testing ToolKit	Task	Closed	Normal	Build 1.0.19	Alexander Kamkin	Alexander Kamkin	1.0
1822	C++TESK Testing ToolKit	Task	Closed	Normal	Build 13	Alexander Kamkin	Alexander Kamkin	
4132	MicroTESK	Task	Closed	Normal	[build] A target for launching junit tests	Alexander Kamkin	Alexander Kamkin	
5346	MicroTESK	Bug	Closed	Normal	[build] Files LICENSE and NOTICE should be added	Alexander Kamkin	Alexander Kamkin	2.1
4803	MicroTESK	Bug	Closed	Normal	[build] run 'ant -f setup.xml' to get latest BuildBase build script	Alexander Kamkin	Alexander Kamkin	2.0
10181	MicroTESK	Bug	Closed	High	CacheUnitTestCase causes StackOverflowError	Alexander Kamkin	Alexander Kamkin	2.5
10188	MicroTESK	Bug	Closed	Normal	CacheUnitTestCase fails for exclusive caches	Alexander Kamkin	Alexander Kamkin	2.5
8726	Verilog Translator	Bug	Closed	Normal	Cannot find the module declaration inside the module	Alexander Kamkin	Alexander Kamkin	0.1
4393	Verilog Translator	Task	Closed	Normal	CaseGenerateltem and Case	Alexander Kamkin	Alexander Kamkin	
4919	Retrascope	Task	Closed	Normal	[cfg][engine] Базовый инструмент для CfgModel	Alexander Kamkin	Alexander Kamkin	0.1
5085	Retrascope IDE	Bug	Closed	Normal	[cfg][visualizator][zest] Не отображается код в узлах графа	Alexander Kamkin	Alexander Kamkin	
5276	Retrascope IDE	Task	New	Normal	[cfg][visualizator][zest] Отладить размещение графа с циклами	Alexander Kamkin	Alexander Kamkin	0.1
5273	Retrascope IDE	Task	New	Normal	[cfg][visualizator][zest] Получение принтера выражений из конфигурации	Alexander Kamkin	Alexander Kamkin	0.1
5093	Retrascope IDE	Task	New	High	[cfg][visualizator][zest] Следует открывать граф в области редактора	Alexander Kamkin	Alexander Kamkin	0.1
5272	Retrascope IDE	Task	New	Normal	[cfg][visualizator][zest] Установка прогресса при построении графа	Alexander Kamkin	Alexander Kamkin	0.1
6159	MicroTESK	Bug	Closed	High	Checker produces the incorrect constraint for a memory access structure	Alexander Kamkin	Alexander Kamkin	2.2

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3982	Retrascope	Task	Closed	High	[code] Рекомендации по коду	Alexander Kamkin	Alexander Kamkin	0.1
4810	MicroTESK	Task	Closed	Normal	Combinatorial test program generation based on test templates with branch instructions	Alexander Kamkin	Alexander Kamkin	2.2
10136	MicroTESK	Bug	New	Normal	Comment for self checks differs from the others	Alexander Kamkin	Alexander Kamkin	2.6
3488	Verilog Translator	Bug	Closed	Normal	CONDITIONAL_STATEMENT -> IF_STATEMENT	Alexander Kamkin	Alexander Kamkin	
4081	Fortress	Task	Closed	Normal	constraint-solver-api-0.1.jar -> constraint-solver-api-0.1.1-beta.jar	Alexander Kamkin	Alexander Kamkin	0.1
8693	MicroTESK	Task	Closed	Normal	Construct sizeof(type)	Alexander Kamkin	Alexander Kamkin	2.5
3582	C++TESK Testing ToolKit	Task	New	Normal	[core] C++TESK as Explicit-State Model Checker	Alexander Kamkin	Alexander Kamkin	1.1
1778	C++TESK Testing ToolKit	Task	Closed	Normal	C++TESK whitepaper in English	Alexander Kamkin	Alexander Kamkin	
4083	Fortress	Task	Closed	Normal	DataFactory.createObject required	Alexander Kamkin	Alexander Kamkin	0.1
4069	Fortress	Task	Closed	Normal	Decomposition of the BVSHL into BVLSHL and BVASHL	Alexander Kamkin	Alexander Kamkin	0.1
8747	Verilog Translator	Bug	Closed	Normal	defparam uses paths, not identifiers as it is implied in the grammar	Alexander Kamkin	Alexander Kamkin	0.1
1776	C++TESK Testing ToolKit	Task	Closed	Normal	Describe the system requirements in wiki	Alexander Kamkin	Alexander Kamkin	
2743	Retrascope	Task	Closed	Normal	Description of the AIF and AIF+ formats	Alexander Kamkin	Alexander Kamkin	
2669	C++TESK Testing ToolKit	Bug	New	Normal	[docs] Add figures to the project description & whitepaper	Alexander Kamkin	Alexander Kamkin	1.0
2213	C++TESK Testing ToolKit	Task	New	Normal	[docs] Английскую документацию делать в wiki	Alexander Kamkin	Alexander Kamkin	1.0
2212	C++TESK Testing ToolKit	Task	New	Normal	[docs] Задokumentировать адаптеры по умолчанию	Alexander Kamkin	Alexander Kamkin	1.0
1838	C++TESK Testing ToolKit	Task	New	Normal	[docs] Задokumentировать интерфейсы с ядром C++TESK	Alexander Kamkin	Alexander Kamkin	1.1
10124	MicroTESK	Bug	New	High	Double preparation of the same register	Alexander Kamkin	Alexander Kamkin	2.6
3985	Retrascope	Task	Closed	Normal	[efsm][model] EFSM walker and visitor	Alexander Kamkin	Alexander Kamkin	0.1
8755	Verilog Translator	Bug	Closed	Normal	Elaborator does not apply port connections	Alexander Kamkin	Alexander Kamkin	0.1
4129	MicroTESK	Task	Closed	Normal	[engines] Interfaces of test generation engine + implementation of basic engines	Alexander Kamkin	Alexander Kamkin	2.0
10107	MicroTESK	Task	New	Normal	Entry point specification in templates	Alexander Kamkin	Alexander Kamkin	2.6
8803	Verilog Translator	Bug	Closed	Normal	Error in parsing the specify construct	Alexander Kamkin	Alexander Kamkin	0.1
8833	Verilog Translator	Bug	Closed	Normal	Error when building AST for if-generate constructs without else branches	Alexander Kamkin	Alexander Kamkin	0.1

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8796	Verilog Translator	Bug	Closed	Normal	Error when handling incorrect tokens under ifdef	Alexander Kamkin	Alexander Kamkin	0.1
8793	Verilog Translator	Bug	Closed	Normal	Error when using attributes	Alexander Kamkin	Alexander Kamkin	0.1
8791	Verilog Translator	Bug	Closed	Normal	Error when using escaped identifiers	Alexander Kamkin	Alexander Kamkin	0.1
8797	Verilog Translator	Bug	New	Normal	Error when using multiple includes	Alexander Kamkin	Alexander Kamkin	0.1
8789	Verilog Translator	Bug	Closed	Normal	Error when using special symbols in strings	Alexander Kamkin	Alexander Kamkin	0.1
4146	Verilog Translator	Task	Closed	Normal	EVerilogOperation -> ECustomOperation	Alexander Kamkin	Alexander Kamkin	
4812	MicroTESK	Task	Closed	Normal	Exception handler definition	Alexander Kamkin	Alexander Kamkin	2.4
8678	MicroTESK for MIPS	Bug	Resolved	Normal	Exception in exception handler	Alexander Kamkin	Alexander Kamkin	0.0
8804	Verilog Translator	Bug	Closed	Normal	Exception when dividing 4-bit vector by 32-bit one	Alexander Kamkin	Alexander Kamkin	0.1
8675	MicroTESK for MIPS	Bug	Resolved	Normal	Executing tlwbi causes CU0	Alexander Kamkin	Alexander Kamkin	0.0
4908	Retrascope	Task	Closed	High	[ехес] Система запуска инструментов	Alexander Kamkin	Alexander Kamkin	0.1
4394	Verilog Translator	Task	Closed	Normal	EXPRESSION and EXTENDED_EXPRESSION	Alexander Kamkin	Alexander Kamkin	
3475	Verilog Translator	Task	Closed	Normal	expression (COMA expression)* -> parameter_list	Alexander Kamkin	Alexander Kamkin	
8818	Verilog Translator	Feature	Closed	Normal	Extending grammar to allow else if	Alexander Kamkin	Alexander Kamkin	0.1
2764	Retrascope	Task	Closed	Normal	Extracting clocks from HDL description	Alexander Kamkin	Alexander Kamkin	
5326	TestBase	Task	Closed	Normal	[floating] The FpResult class can be generalized to Result<T>	Alexander Kamkin	Alexander Kamkin	
4884	Retrascope IDE	Task	New	Normal	[flowchart][visualizator][zest] Визуализация блок-схем	Alexander Kamkin	Alexander Kamkin	0.1
6104	MicroTESK	Task	Closed	Normal	getDelaySlotSize method wanted	Alexander Kamkin	Alexander Kamkin	2.2
6396	MicroTESK	Bug	New	Normal	Handling TAG_REPLACED dependencies	Alexander Kamkin	Alexander Kamkin	2.6
6440	MicroTESK	Task	Closed	Normal	How to reuse a preparator?	Alexander Kamkin	Alexander Kamkin	2.3
5352	TestBase	Task	New	Normal	Implementation of FpMulHardToNRound	Alexander Kamkin	Alexander Kamkin	0.0
4127	Verilog Translator	Task	Closed	Normal	Implementation of replication operation	Alexander Kamkin	Alexander Kamkin	
5351	TestBase	Task	New	Normal	Implementation of ResultAdapter for FP.MUL	Alexander Kamkin	Alexander Kamkin	0.0

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1826	C++TESK Testing ToolKit	Task	Closed	Normal	Installation instruction in English	Alexander Kamkin	Alexander Kamkin	
9338	MicroTESK	Task	Closed	Normal	Instruction decoding hints	Alexander Kamkin	Alexander Kamkin	2.4
4171	Veritool	Bug	Resolved	Normal	Int128-512: Возможно ошибка	Alexander Kamkin	Alexander Kamkin	
4170	Veritool	Bug	Resolved	Normal	Int128-512: Замечания к коду	Alexander Kamkin	Alexander Kamkin	
5732	MicroTESK	Task	Rejected	Normal	ISA reference manual generation	Alexander Kamkin	Alexander Kamkin	2.2
10179	MicroTESK	Bug	Closed	High	java.lang.AssertionError: lw core=0, address=1c: deadbeef != d3d5e5f9	Alexander Kamkin	Alexander Kamkin	2.5
6329	MicroTESK	Bug	New	Normal	java.lang.NullPointerException at ru.ispras.microtesk.mmu.translator.MmuTreeWalkerBase.newAttributeRef(MmuTreeWalkerBase.java:940)	Alexander Kamkin	Alexander Kamkin	2.6
1828	C++TESK Testing ToolKit	Task	New	Normal	[lang][core] Implementation of temporal sequences	Alexander Kamkin	Alexander Kamkin	1.1
4955	C++TESK Testing ToolKit	Task	New	Normal	[lang][core] Способы работы с типами данных uint128_t, uint256_t и т.д.	Alexander Kamkin	Alexander Kamkin	1.0
4932	C++TESK Testing ToolKit	Task	Resolved	Normal	[lang][log] Переопределение вывода полей сообщений	Alexander Kamkin	Alexander Kamkin	1.0
5092	Verilog Translator	Task	Closed	Normal	Merging declaration parts for the same variable	Alexander Kamkin	Alexander Kamkin	0.1
4385	Verilog Translator	Task	Closed	Normal	Merging FUNCTION and TASK tags	Alexander Kamkin	Alexander Kamkin	
9336	MicroTESK	Bug	Closed	Normal	MicroTESK is not able to disassemble euclid binary (RISC-V)	Alexander Kamkin	Alexander Kamkin	2.4
4825	MicroTESK	Task	Rejected	Normal	[model] Modeling a branch delay slot	Alexander Kamkin	Alexander Kamkin	2.0
4212	Fortress	Task	Closed	Normal	New build	Alexander Kamkin	Alexander Kamkin	0.1
3742	C++TESK Testing ToolKit	Task	Closed	Normal	New build	Alexander Kamkin	Alexander Kamkin	
8683	MicroTESK for MIPS	Bug	Resolved	Normal	Nonoptimal preparator generation	Alexander Kamkin	Alexander Kamkin	0.0
7729	MicroTESK	Task	New	Normal	On-demand memory access path exploration (coverage extraction)	Alexander Kamkin	Alexander Kamkin	2.6
5409	Fortress	Task	Closed	Normal	Orthogonalizer refactoring etc.	Alexander Kamkin	Alexander Kamkin	0.3
3487	Verilog Translator	Bug	Closed	Normal	par_block   seq_block -> block_statement	Alexander Kamkin	Alexander Kamkin	
4070	Fortress	Bug	Closed	Normal	Question on the ternary conditional operation (cond ? lhs : rhs)	Alexander Kamkin	Alexander Kamkin	0.1
4067	Fortress	Bug	Closed	Normal	Questions on ADD and MINUS	Alexander Kamkin	Alexander Kamkin	0.1
4391	Verilog Translator	Task	Closed	Normal	Rename FOR_GENERATE to LOOP_GENERATE	Alexander Kamkin	Alexander Kamkin	
4068	Verilog Translator	Task	Closed	Normal	Rename LT_, LE, GT and GE	Alexander Kamkin	Alexander Kamkin	

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4073	Verilog Translator	Task	Closed	Normal	Rename SL, SR, SL3 and SR3	Alexander Kamkin	Alexander Kamkin	
4074	Verilog Translator	Task	Closed	Normal	Rename STAR2	Alexander Kamkin	Alexander Kamkin	
4409	Fortress	Bug	Closed	Normal	ru/ispras/solver/core/syntax/EStandardOperation.java:212: inconvertible types	Alexander Kamkin	Alexander Kamkin	0.2
9252	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_05_01_05_2: Cannot convert a real to a bitvector	Alexander Kamkin	Alexander Kamkin	0.1
9253	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_07_01_02_1: no viable alternative	Alexander Kamkin	Alexander Kamkin	0.1
9254	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_07_08_00_1: no viable alternative	Alexander Kamkin	Alexander Kamkin	0.1
9262	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_09_06_00_1: Descriptor has not been found	Alexander Kamkin	Alexander Kamkin	0.1
9260	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_10_02_02_1_2: StackOverflowError	Alexander Kamkin	Alexander Kamkin	0.1
9256	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_12_02_00_1: NullPointerException	Alexander Kamkin	Alexander Kamkin	0.1
9257	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_12_04_01_5: IllegalArgumentException	Alexander Kamkin	Alexander Kamkin	0.1
9258	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_12_04_02_1 STANDARD_OUT	Alexander Kamkin	Alexander Kamkin	0.1
9259	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_12_04_02_4: Task declaration has not been found	Alexander Kamkin	Alexander Kamkin	0.1
9255	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_14_06_04_2_3: no viable alternative	Alexander Kamkin	Alexander Kamkin	0.1
9261	Verilog Translator	Bug	Closed	Normal	ru.ispras.verilog.parser.VerilogleeeTestCase.runTest_19_03_01_1: line 1:4 mismatched input ')' expecting LPAREN	Alexander Kamkin	Alexander Kamkin	0.1
4408	Verilog Translator	Task	Closed	Normal	Scope.YES & Scope.NO	Alexander Kamkin	Alexander Kamkin	0.1
291	Aspectrace	Task	Closed	High	Shell-скрипт для запуска генератора	Alexander Kamkin	Alexander Kamkin	
5311	MicroTESK	Task	New	Normal	Shortcuts for getting modes from templates	Alexander Kamkin	Alexander Kamkin	2.6
8691	MicroTESK	Task	New	Normal	Sign- and zero-extend functions in preparators	Alexander Kamkin	Alexander Kamkin	2.6
3661	C++ TESK Testing Toolkit	Task	Closed	Normal	Soft correctness criteria	Alexander Kamkin	Alexander Kamkin	1.0
9978	MicroTESK for RISC-V	Task	New	Normal	Some templates use rand(-2147483648, 2147483647) for randomizing words	Alexander Kamkin	Alexander Kamkin	0.1
6148	MicroTESK	Task	Closed	Normal	Specification of 'no-touch' and 'share' zones of memory	Alexander Kamkin	Alexander Kamkin	2.2
5325	TestBase	Task	New	Normal	Sqrt hard-to-round iterator for the nearest rounding needs to be reviewed	Alexander Kamkin	Alexander Kamkin	0.0

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6161	MicroTESK	Task	Rejected	Normal	Standard (abstract sequence-based) MMU template representation	Alexander Kamkin	Alexander Kamkin	2.2
1665	Veritool	Task	Feedback	Normal	start_scep должен быть в библиотеке репозитория тестов	Alexander Kamkin	Alexander Kamkin	
412	Veritool	Task	Closed	Normal	start_testbench() и stop_testbench()	Alexander Kamkin	Alexander Kamkin	
4076	Fortress	Bug	Closed	Normal	Strange hashCode methods	Alexander Kamkin	Alexander Kamkin	0.1
4084	Fortress	Task	Rejected	Normal	String data type required	Alexander Kamkin	Alexander Kamkin	0.1
10106	MicroTESK	Task	New	Normal	Support %b as format's specifier	Alexander Kamkin	Alexander Kamkin	2.6
6187	MicroTESK	Task	Closed	Normal	Support for address object declaration	Alexander Kamkin	Alexander Kamkin	2.2
10178	MicroTESK	Task	Closed	Normal	Support for basic cache coherence protocols in MMU specifications	Alexander Kamkin	Alexander Kamkin	2.5
6121	MicroTESK	Task	Closed	Normal	Support for basic preprocessor constructs	Alexander Kamkin	Alexander Kamkin	2.2
6152	MicroTESK	Task	Closed	Normal	Support for disjunctive constraints in MMU specifications	Alexander Kamkin	Alexander Kamkin	2.3
4105	Verilog Translator	Task	Closed	Normal	Support for gate (primitive) instantiation	Alexander Kamkin	Alexander Kamkin	
6461	MicroTESK	Task	New	Normal	Support for inline comments	Alexander Kamkin	Alexander Kamkin	2.6
4106	Verilog Translator	Task	New	Low	Support for library and config declaration	Alexander Kamkin	Alexander Kamkin	0.3
5942	MicroTESK	Task	Closed	Normal	Support for memory segments specification	Alexander Kamkin	Alexander Kamkin	2.3
4124	Verilog Translator	Bug	Closed	Normal	Support for port attributes in a module's signature	Alexander Kamkin	Alexander Kamkin	0.1
4107	Verilog Translator	Bug	Closed	Normal	Support for pulse control in specparam assignment	Alexander Kamkin	Alexander Kamkin	0.1
4108	Verilog Translator	Task	Closed	Normal	Support for specify constructs	Alexander Kamkin	Alexander Kamkin	
4123	Verilog Translator	Task	Closed	Low	Support for system timing check	Alexander Kamkin	Alexander Kamkin	0.3
4075	Fortress	Task	Closed	Normal	Support for the case equality/inequality (=== and !==) operators	Alexander Kamkin	Alexander Kamkin	0.1
6377	MicroTESK	Task	Rejected	High	Support for user-defined attributes	Alexander Kamkin	Alexander Kamkin	2.3
10168	MicroTESK	Task	Closed	Normal	Support for Write-Back policy in cache specification	Alexander Kamkin	Alexander Kamkin	2.5
3594	Verilog Translator	Bug	Closed	Low	Support of drive strength	Alexander Kamkin	Alexander Kamkin	
4117	Fortress	Task	Closed	High	Support of replication operation	Alexander Kamkin	Alexander Kamkin	0.1
3593	Verilog Translator	Task	Closed	Low	Support of UDP declaration and instantiation	Alexander Kamkin	Alexander Kamkin	
3489	Verilog Translator	Bug	Closed	Normal	(SYSTEM_)TASK_ENABLE -> (SYSTEM_)TASK_ENABLE_STATEMENT	Alexander Kamkin	Alexander Kamkin	
4065	Fortress	Task	Closed	Normal	TABs should be replaced to 4 spaces	Alexander Kamkin	Alexander Kamkin	0.1

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4383	Verilog Translator	Task	Closed	Normal	Tag DECLARATION + enum { INPUT, OUTPUT, etc.}	Alexander Kamkin	Alexander Kamkin	
5038	Verilog Translator	Task	Closed	Normal	Taking into account X- and Z-don't cares in CaseStatementItem.getExpression().	Alexander Kamkin	Alexander Kamkin	0.1
5252	Retrascope IDE	Task	Closed	Normal	[target][configurator] Группировка целей по префиксу	Alexander Kamkin	Alexander Kamkin	
5271	Retrascope IDE	Task	New	Normal	[task][configurator] Деактивация кнопки ОК	Alexander Kamkin	Alexander Kamkin	0.1
10122	MicroTESK	Task	New	Normal	Template libraries for standard features	Alexander Kamkin	Alexander Kamkin	2.6
5811	TestBase	Task	New	Normal	Test data generators for basic arithmetic SIMD instructions	Alexander Kamkin	Alexander Kamkin	0.0
10187	MicroTESK	Bug	Closed	Normal	testWriteThroughAllocationInclusive: lw core=2, address=d058: deadbeef != a8c7e6ae	Alexander Kamkin	Alexander Kamkin	2.5
4071	Fortress	Task	Closed	Normal	Thoughts/questions on EVerilogOperation	Alexander Kamkin	Alexander Kamkin	0.1
2057	C++TESK Development Environment	Task	Closed	Normal	Toogle C++TESK Highlighting -> Enable C++TESK Highlighting   Disable C++TESK Highlighting	Alexander Kamkin	Alexander Kamkin	
5056	Retrascope IDE	Task	Closed	Normal	[tool][configurator] Возможность выбора файлов через диалоговое окно в TextConfigurator	Alexander Kamkin	Alexander Kamkin	
5043	Retrascope IDE	Bug	Closed	Normal	[tool][configurator] Возможность удаления добавленных файлов в FilesConfigurator	Alexander Kamkin	Alexander Kamkin	
5057	Retrascope IDE	Bug	New	Normal	[tool][configurator] Для низкоуровневого конфигуратора Arguments не нужно выдавать --arguments	Alexander Kamkin	Alexander Kamkin	0.1
4961	Retrascope IDE	Task	New	Normal	[tool][configurator] Конструктор инструментальных цепочек	Alexander Kamkin	Alexander Kamkin	0.1
5100	Retrascope IDE	Task	Closed	Normal	[tool][launcher] Добавить в окно ProgressBar	Alexander Kamkin	Alexander Kamkin	
5044	Retrascope IDE	Task	Closed	Normal	[tool][launcher] Добавление результатов работы инструментов в проект	Alexander Kamkin	Alexander Kamkin	
4651	MicroTESK	Bug	Closed	Normal	[translator] NullPointerException на пустом файле	Alexander Kamkin	Alexander Kamkin	
3518	MicroTESK	Task	Closed	Normal	[translator] Поддержка директивы include	Alexander Kamkin	Alexander Kamkin	
9314	MicroTESK	Bug	Closed	Normal	Undesired behavior of situation('random', :size => 32, :sign_extend => true)	Alexander Kamkin	Alexander Kamkin	2.4
4126	Verilog Translator	Task	New	Normal	Unification of AST_EDGE and AST_TABLE_EDGE	Alexander Kamkin	Alexander Kamkin	0.1
5037	Verilog Translator	Task	Rejected	Normal	Unification of If(Statement Generate) и Case(Statement Generate)	Alexander Kamkin	Alexander Kamkin	0.1
4072	Fortress	Bug	Closed	Normal	Unsupported major.minor version 51.0	Alexander Kamkin	Alexander Kamkin	0.1
6223	MicroTESK	Task	New	Low	User-defined template-oriented extensions (plugins)	Alexander Kamkin	Alexander Kamkin	2.6
8740	Verilog Translator	Bug	Closed	Normal	Use VerilogGenerateProcessor in VerilogElaborator	Alexander Kamkin	Alexander Kamkin	0.1
8725	Verilog Translator	Task	Closed	Normal	Using Fortress's BitVector in VerilogLiteral	Alexander Kamkin	Alexander Kamkin	0.1
6379	MicroTESK	Bug	New	Normal	Using if-then-else in syntax causes an error	Alexander Kamkin	Alexander Kamkin	2.6



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6160	MicroTESK	Bug	Closed	High	VAddrNotEqualPAddrNotEqualDTLBTagNotReplacedJTLBTagNotEqualL1TagReplaced MEMIndexNotEqual is UNSAT	Alexander Kamkin	Alexander Kamkin	2.2
9229	Verilog Translator	Bug	Closed	Normal	VerilogTestCases.runTest_03_05_01_3: incorrect token under disabled if-def	Alexander Kamkin	Alexander Kamkin	0.1
5073	Retrascope	Bug	Closed	Normal	[verilog][parser][cfg] Запуск парсера приводит IllegalStateException	Alexander Kamkin	Alexander Kamkin	0.1
8739	Verilog Translator	Bug	Closed	Normal	VerilogStaticChecker causes an exception when handling variables with parameter-defined length	Alexander Kamkin	Alexander Kamkin	0.1
5165	Verilog Translator	Bug	Closed	Normal	Verilog static checker raises NullPointerException on test 005	Alexander Kamkin	Alexander Kamkin	0.1
4907	Retrascope	Task	Closed	High	[verilog][translator] Добавить в проект Verilog-транслятор	Alexander Kamkin	Alexander Kamkin	0.1
4872	Retrascope	Task	Closed	Normal	[verilog][translator] Проработать интерфейс backend'a транслятора с Verilog	Alexander Kamkin	Alexander Kamkin	0.1
4979	Retrascope	Bug	Closed	Normal	[vhdl][parser] Исключение при запуске VhdlParser	Alexander Kamkin	Alexander Kamkin	0.1
4915	Retrascope	Task	Closed	Normal	[vhdl][translator] Разработка обертки вокруг zamiaCAD-транслятора	Alexander Kamkin	Alexander Kamkin	0.1
2744	Retrascope	Task	Closed	Normal	Works on guarded actions in University of Kaiserslautern	Alexander Kamkin	Alexander Kamkin	
796	Veritool	Task	Closed	Normal	Альтернативы VCD	Alexander Kamkin	Alexander Kamkin	
1532	C++TESK Testing ToolKit	Task	Closed	Normal	Архив с инструментом без исходников	Alexander Kamkin	Alexander Kamkin	
1674	C++TESK Testing ToolKit	Task	Closed	Normal	Билд 1.0.10	Alexander Kamkin	Alexander Kamkin	
1701	C++TESK Testing ToolKit	Task	Closed	Normal	Билд 1.0.11	Alexander Kamkin	Alexander Kamkin	
2013	C++TESK Testing ToolKit	Task	Closed	Normal	Билд 1.0.15	Alexander Kamkin	Alexander Kamkin	
2081	C++TESK Testing ToolKit	Task	Closed	Normal	Билд 1.0.16	Alexander Kamkin	Alexander Kamkin	1.0
2187	C++TESK Testing ToolKit	Task	Closed	Normal	Билд 1.0.18	Alexander Kamkin	Alexander Kamkin	
1416	C++TESK Testing ToolKit	Task	Closed	Normal	Билд 1.0.7	Alexander Kamkin	Alexander Kamkin	
1457	C++TESK Testing ToolKit	Task	Closed	Normal	Билд 1.0.8	Alexander Kamkin	Alexander Kamkin	
1523	C++TESK Testing ToolKit	Task	Closed	Normal	Билд 1.0.9	Alexander Kamkin	Alexander Kamkin	
1175	C++TESK Testing ToolKit	Task	Closed	Normal	Включение в поставку Veritool и Icarus Verilog	Alexander Kamkin	Alexander Kamkin	
1540	C++TESK Testing ToolKit	Task	Closed	Normal	Включение в поставку примера account	Alexander Kamkin	Alexander Kamkin	
4013	Retrascope	Task	Closed	Normal	Внутреннее представление HDL-описание	Alexander Kamkin	Alexander Kamkin	

#	Project	Tracker	Status	Priority	Subject	Author	Assignee	Target version
3522	Fortress	Task	Closed	Normal	Возможность подключения к узлам дерева операций произвольных объектов	Alexander Kamkin	Alexander Kamkin	0.1
6505	Castle	Task	Feedback	Normal	Вопросы по MetalInfo	Alexander Kamkin	Alexander Kamkin	0.1
6506	Castle	Task	Feedback	Normal	Вопросы по представлению массивов	Alexander Kamkin	Alexander Kamkin	0.1
3813	Veritool	Bug	Resolved	Normal	В структуре inputs_t нет reset	Alexander Kamkin	Alexander Kamkin	
4128	Verilog Translator	Task	Closed	Normal	Вычисление константных выражений	Alexander Kamkin	Alexander Kamkin	0.1
4416	Retrascope	Task	Closed	Normal	Где находится класс для базового блока?	Alexander Kamkin	Alexander Kamkin	
5253	Retrascope	Bug	Closed	Normal	Где-то в планировщике путей есть ошибка	Alexander Kamkin	Alexander Kamkin	
4685	C++TESK Testing ToolKit	Bug	Closed	Urgent	Генератор случайный чисел не очень случайный	Alexander Kamkin	Alexander Kamkin	1.0
2052	C++TESK Development Environment	Task	Closed	Normal	Генерация отчета по двойному щелчку мыши	Alexander Kamkin	Alexander Kamkin	
760	Veritool	Task	Closed	Normal	Группировка сигналов в массивы	Alexander Kamkin	Alexander Kamkin	
4960	Retrascope IDE	Task	Closed	Normal	Двухшаговый визард для запуска инструментов	Alexander Kamkin	Alexander Kamkin	
388	Veritool	Task	Closed	Normal	Добавить extern "C" в vsim.h и sync.h.	Alexander Kamkin	Alexander Kamkin	
4043	Verilog Translator	Task	Closed	Normal	Добавить в атрибут name в базовый узел	Alexander Kamkin	Alexander Kamkin	
5174	Verilog Translator	Task	Closed	Normal	Добавить в калькулятор поддержку тернарной операции	Alexander Kamkin	Alexander Kamkin	0.1
5087	Verilog Translator	Task	Closed	Normal	Добавить возможность отсечения обхода дочерних поддеревьев	Alexander Kamkin	Alexander Kamkin	0.1
1699	C++TESK Testing ToolKit	Task	Closed	Normal	Добавить в примеры cppteskhw/examples/cache_coherence	Alexander Kamkin	Alexander Kamkin	
1582	MicroTESK	Task	Closed	Normal	Добавить документацию по nML/Sim-nML	Alexander Kamkin	Alexander Kamkin	
3515	Verilog Translator	Task	Closed	Normal	Добавить опцию для задания путей поиска include-файлов	Alexander Kamkin	Alexander Kamkin	
3500	Fortress	Task	Closed	Normal	Добавить поддержку операций Verilog	Alexander Kamkin	Alexander Kamkin	0.1
4561	Verilog Translator	Bug	Closed	Normal	Добавить узел LValue	Alexander Kamkin	Alexander Kamkin	
1182	C++TESK Testing ToolKit	Task	Closed	Normal	Документ по опциям обходчиков	Alexander Kamkin	Alexander Kamkin	
1446	Veritool	Task	Closed	Normal	Единая библиотека libveritool.a	Alexander Kamkin	Alexander Kamkin	
4098	Verilog Translator	Bug	Closed	Normal	Если указать два файла при запуске парсера -> NullPointerException	Alexander Kamkin	Alexander Kamkin	

#	Project	Tracker	Status	Priority	Subject	Author	Assignee	Target version
2050	C++TESK Development Environment	Bug	Closed	Normal	Заголовок "MCC server configuration" не помещается в окне	Alexander Kamkin	Alexander Kamkin	
2054	C++TESK Development Environment	Task	Closed	Normal	Заполнение окна MCC параметрами по умолчанию	Alexander Kamkin	Alexander Kamkin	
2135	C++TESK Development Environment	Task	Closed	Normal	Запуск теста на локальной машине	Alexander Kamkin	Alexander Kamkin	
2056	C++TESK Development Environment	Task	Closed	Normal	Иконки для файлов *.utt2 и *.gv	Alexander Kamkin	Alexander Kamkin	
792	C++TESK Testing ToolKit	Task	Closed	High	Инсталлятор C++TESK	Alexander Kamkin	Alexander Kamkin	
2595	C++TESK Testing ToolKit	Task	Closed	Normal	Инсталляция плагина	Alexander Kamkin	Alexander Kamkin	1.0
1183	C++TESK Testing ToolKit	Task	Closed	High	Инструкция по установке	Alexander Kamkin	Alexander Kamkin	
5086	Verilog Translator	Bug	Closed	Normal	Исключение при вычислении константных выражений	Alexander Kamkin	Alexander Kamkin	0.1
5566	Verilog Translator	Task	Closed	Normal	Использование стандартного принтера выражений	Alexander Kamkin	Alexander Kamkin	0.1
1422	C++TESK Testing ToolKit	Task	Closed	Normal	Как узнать номер версии билда после установки?	Alexander Kamkin	Alexander Kamkin	
2098	C++TESK Development Environment	Task	Closed	Normal	Ключевое слово CPPTESK_ADD_INCOMPARABLE_FIELD	Alexander Kamkin	Alexander Kamkin	
2109	C++TESK Development Environment	Task	Closed	Normal	Ключевые слова CPPTESK_CALL_STIMULUS_OF и CPPTESK_CALL_PROCESS_OF	Alexander Kamkin	Alexander Kamkin	
2118	C++TESK Development Environment	Task	Closed	Normal	Ключевые слова CPPTESK_{DECLARE ADD}_FIELD_ARRAY	Alexander Kamkin	Alexander Kamkin	
3953	Retrascope	Task	Closed	Normal	Комментарии javadoc ко всем public методам	Alexander Kamkin	Alexander Kamkin	
386	Veritool	Task	Closed	Normal	Комментарии о разрядности входов и выходов	Alexander Kamkin	Alexander Kamkin	
5163	Verilog Translator	Bug	Closed	Normal	Конец файла после директивы препроцессора	Alexander Kamkin	Alexander Kamkin	0.1
5348	TestBase	Task	New	Normal	Корректная реализация генератора IntSubExceptionGenerator	Alexander Kamkin	Alexander Kamkin	0.0
1245	C++TESK Testing ToolKit	Task	Closed	Normal	Косметические изменения в инсталляторе	Alexander Kamkin	Alexander Kamkin	

#	Project	Tracker	Status	Priority	Subject	Author	Assignee	Target version
1793	C++TESK Development Environment	Task	Closed	Normal	Краткое ТЗ на разработку плагина	Alexander Kamkin	Alexander Kamkin	
5166	Verilog Translator	Task	Closed	Normal	Логирования сообщение транслятора	Alexander Kamkin	Alexander Kamkin	0.1
4675	Fortress	Task	New	Normal	Манипуляция с функциями алгебры логики	Alexander Kamkin	Alexander Kamkin	0.4
4223	Fortress	Task	Closed	Normal	Метод DataFactory.createDataType() с одним параметром	Alexander Kamkin	Alexander Kamkin	0.2
3481	Verilog Translator	Task	Closed	Normal	Найти место в AST для KW_VECTORED и KW_SCALARED	Alexander Kamkin	Alexander Kamkin	
5042	Retrascope IDE	Task	Closed	Normal	Написать комментарии к имеющемуся коду	Alexander Kamkin	Alexander Kamkin	
3478	Verilog Translator	Task	Closed	Normal	Написать обходчик AST'a	Alexander Kamkin	Alexander Kamkin	
411	Veritool	Task	Closed	Normal	Небольшой рефакторинг testbench.v	Alexander Kamkin	Alexander Kamkin	
1177	CTESK	Bug	Rejected	Normal	Не запускается транслятор на 64-битном Linux	Alexander Kamkin	Alexander Kamkin	
3490	Verilog Translator	Bug	Closed	Normal	Некорректная обработка деклараций портов с размерностью перед идентификатором	Alexander Kamkin	Alexander Kamkin	
5028	Verilog Translator	Bug	Closed	Normal	Не обрабатывается конструкция repeat	Alexander Kamkin	Alexander Kamkin	0.1
2047	C++TESK Development Environment	Bug	Closed	Normal	Не отображается картинка для кнопки start MCC with parameters	Alexander Kamkin	Alexander Kamkin	
383	Veritool	Bug	Closed	Normal	Не хватает #include "vpi_media.h"	Alexander Kamkin	Alexander Kamkin	
384	Veritool	Bug	Closed	Normal	Не хватает #include <vsim.h>. Иногда это приводит к ошибкам компиляции.	Alexander Kamkin	Alexander Kamkin	
3781	Veritool	Task	Resolved	Normal	Нумерация тактов	Alexander Kamkin	Alexander Kamkin	
1423	C++TESK Testing ToolKit	Task	Closed	Normal	Обновление набора инструментов	Alexander Kamkin	Alexander Kamkin	
5160	Verilog Translator	Task	Closed	Normal	Обработка комментариев в define	Alexander Kamkin	Alexander Kamkin	0.1
5206	Verilog Translator	Task	Closed	Normal	Обработка конструкций generate	Alexander Kamkin	Alexander Kamkin	0.1
1700	C++TESK Testing ToolKit	Bug	Closed	Normal	Обработка ошибок в генераторе отчетов	Alexander Kamkin	Alexander Kamkin	
1499	C++TESK Testing ToolKit	Task	Closed	Normal	Обработка ошибок установки	Alexander Kamkin	Alexander Kamkin	
3484	Verilog Translator	Task	Closed	Normal	Объединить FUNCTION_DECLARATION и TASK_DECLARATION	Alexander Kamkin	Alexander Kamkin	
3482	Verilog Translator	Bug	Closed	Normal	Объединить в грамматике model_item и module_or_generate_item	Alexander Kamkin	Alexander Kamkin	

#	Project	Tracker	Status	Priority	Subject	Author	Assignee	Target version
4044	Verilog Translator	Task	Closed	Normal	Отдельный класс для представления пути (List<String> -> Path)	Alexander Kamkin	Alexander Kamkin	
5104	Verilog Translator	Task	Closed	Normal	Отладка сопоставления ссылок и деклараций	Alexander Kamkin	Alexander Kamkin	0.1
387	Veritool	Task	New	Normal	Отладочный вывод значений входов и выходов	Alexander Kamkin	Alexander Kamkin	
420	Veritool	Task	Closed	Normal	Отображение номера такта во временной диаграмме	Alexander Kamkin	Alexander Kamkin	
298	Aspectrace	Bug	Closed	Normal	Падение при невозможности открыть файл трассы	Alexander Kamkin	Alexander Kamkin	
402	Veritool	Task	Closed	Normal	Передача параметров теста	Alexander Kamkin	Alexander Kamkin	
825	Veritool	Task	Closed	Normal	Переименование опции ICARUS_VERILOG	Alexander Kamkin	Alexander Kamkin	
2055	C++TESK Development Environment	Task	Closed	Normal	Переименование полей в окне MCC	Alexander Kamkin	Alexander Kamkin	
3591	Verilog Translator	Bug	Closed	Normal	Переименовать AST_ITEM_GENERATE	Alexander Kamkin	Alexander Kamkin	
2048	C++TESK Development Environment	Task	Closed	Normal	Перенаправление stdout в консоль Eclipse IDE	Alexander Kamkin	Alexander Kamkin	
5228	Verilog Translator	Task	Closed	Normal	Перенести Expression.VariableGatherer в Fortress	Alexander Kamkin	Alexander Kamkin	0.1
1044	Veritool	Task	Feedback	Normal	Перенос репозитория тестов	Alexander Kamkin	Alexander Kamkin	
1518	C++TESK Testing ToolKit	Task	Closed	Normal	Подвешивание инсталлятора в конце работы	Alexander Kamkin	Alexander Kamkin	
606	Veritool	Task	Closed	Normal	Поддержка C++ библиотеке sync	Alexander Kamkin	Alexander Kamkin	
3514	Verilog Translator	Task	Rejected	Low	Поддержка в препроцессоре define с параметрами	Alexander Kamkin	Alexander Kamkin	0.1
3516	Verilog Translator	Task	Closed	Normal	Поддержка в препроцессоре условной компиляции	Alexander Kamkin	Alexander Kamkin	
4050	Fortress	Bug	Closed	Normal	Поддержка вызовов функций	Alexander Kamkin	Alexander Kamkin	0.1
1268	C++TESK Testing ToolKit	Task	Closed	Normal	Подсветка синтаксиса в VIM	Alexander Kamkin	Alexander Kamkin	
5205	Verilog Translator	Task	Closed	Normal	Подстановка тела функции в место вызова	Alexander Kamkin	Alexander Kamkin	0.1
5164	Verilog Translator	Bug	Closed	Normal	После директивы `endif в той же строчке могут идти лексемы	Alexander Kamkin	Alexander Kamkin	0.1
4949	Retrascope IDE	Task	Closed	Normal	Построение окна настроек инструмента по описанию его параметров	Alexander Kamkin	Alexander Kamkin	
4875	Retrascope	Task	Closed	Normal	Построение решающей диаграммы по системе условий	Alexander Kamkin	Alexander Kamkin	1.0
5121	Verilog Translator	Bug	Closed	Normal	Похоже, что x- и z-маски всегда нулевые	Alexander Kamkin	Alexander Kamkin	0.1

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3477	Verilog Translator	Task	Closed	Normal	Препроцессор Verilog-кода	Alexander Kamkin	Alexander Kamkin	
5041	Retrascope IDE	Task	Closed	Normal	Привести в порядок сгенерированный код	Alexander Kamkin	Alexander Kamkin	
4965	Verilog Translator	Bug	Closed	Normal	Привязывание переменных к их декларациям	Alexander Kamkin	Alexander Kamkin	0.1
2049	C++TESK Development Environment	Bug	Closed	Normal	При закрытии окна MCC server configuration открывается окно браузера	Alexander Kamkin	Alexander Kamkin	
1544	C++TESK Testing ToolKit	Task	Closed	Normal	При установке cppteskhw копируется hw/utills/win32, но не копируется hw/utills	Alexander Kamkin	Alexander Kamkin	
4097	Verilog Translator	Task	Open	Normal	Проверка зацикливания include-файлов	Alexander Kamkin	Alexander Kamkin	0.1
2188	C++TESK Testing ToolKit	Task	Closed	Normal	Проверка совместимости инструмента с поддерживаемыми тестовыми системами	Alexander Kamkin	Alexander Kamkin	1.0
4151	MicroTESK	Task	Closed	Normal	Путаница в параметрах шаблонов комбинаторов и композиторов	Alexander Kamkin	Alexander Kamkin	2.2
2133	C++TESK Development Environment	Task	Closed	Normal	Разобраться с разработкой плагинов для Eclipse IDE	Alexander Kamkin	Alexander Kamkin	
4874	Retrascope	Task	Closed	Normal	Разработка внутреннего представления для решающих диаграмм	Alexander Kamkin	Alexander Kamkin	1.0
4890	Retrascope	Task	New	Low	Разработка и реализация метода RTL-в-TLM абстракции блок-схем	Alexander Kamkin	Alexander Kamkin	2.0
2136	C++TESK Development Environment	Task	Closed	Normal	Распределенный запуск теста	Alexander Kamkin	Alexander Kamkin	
4891	Retrascope	Task	New	Low	Реализация генератора тестов по набору блок-схем	Alexander Kamkin	Alexander Kamkin	2.0
4962	Retrascope	Task	New	Normal	Реализация утилиты Veritool с помощью средств Retrascope	Alexander Kamkin	Alexander Kamkin	2.0
643	Veritool	Task	Closed	Normal	Репозиторий тестов	Alexander Kamkin	Alexander Kamkin	
2132	C++TESK Development Environment	Task	Closed	Normal	Сборка проекта	Alexander Kamkin	Alexander Kamkin	
4062	Verilog Translator	Task	Closed	Normal	Сборка проекта с помощью ant	Alexander Kamkin	Alexander Kamkin	
5169	Verilog Translator	Task	Closed	Normal	Связывание вызовов задач с декларациями	Alexander Kamkin	Alexander Kamkin	0.1
5168	Verilog Translator	Task	Closed	Normal	Связывание вызовов функций с декларациями	Alexander Kamkin	Alexander Kamkin	0.1
5167	Verilog Translator	Task	Closed	Normal	Связывание экземпляров модулей с декларациями	Alexander Kamkin	Alexander Kamkin	0.1
3592	Verilog Translator	Task	Rejected	Normal	Сделать все узлы модели наследниками VerilogNode	Alexander Kamkin	Alexander Kamkin	

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3954	Retrascope	Task	Closed	Normal	Симулятор EFSM-модели	Alexander Kamkin	Alexander Kamkin	
1186	Veritool	Task	New	Normal	Скрипт установки Icarus Verilog	Alexander Kamkin	Alexander Kamkin	
1184	C++TESK Testing ToolKit	Task	Closed	High	Скрипт установки полного набора инструментов	Alexander Kamkin	Alexander Kamkin	
1270	C++TESK Testing ToolKit	Task	Closed	Normal	Совместное использование с SystemVerilog	Alexander Kamkin	Alexander Kamkin	1.0
4980	Retrascope IDE	Task	Closed	Normal	Сортировка целей и инструментов в ToolLauncher	Alexander Kamkin	Alexander Kamkin	
4082	Verilog Translator	Task	Closed	Normal	Сохранение атрибутов выражения	Alexander Kamkin	Alexander Kamkin	
1283	C++TESK Testing ToolKit	Task	Closed	Normal	Список рассылки	Alexander Kamkin	Alexander Kamkin	
1500	C++TESK Testing ToolKit	Task	Closed	Normal	Список рассылки C++TESK Testing ToolKit	Alexander Kamkin	Alexander Kamkin	
4963	Verilog Translator	Task	Closed	Normal	Структура данных для представления значений параметров	Alexander Kamkin	Alexander Kamkin	0.1
4704	Retrascope	Task	Closed	Normal	Структура пакетов проекта	Alexander Kamkin	Alexander Kamkin	
3981	Retrascope	Task	Closed	Normal	Структура пакетов проекта	Alexander Kamkin	Alexander Kamkin	
1525	C++TESK Testing ToolKit	Task	Closed	Normal	Тесты для проверки билда	Alexander Kamkin	Alexander Kamkin	
4964	Verilog Translator	Task	Closed	Normal	Типизация переменных	Alexander Kamkin	Alexander Kamkin	0.1
2051	C++TESK Development Environment	Task	Closed	Normal	Убрать окна сообщений, в которых есть только одна кнопка ОК	Alexander Kamkin	Alexander Kamkin	
3533	Verilog Translator	Task	Closed	Normal	Убрать отдельное поддереву AST_PARAMETERS дерева AST_MODULE	Alexander Kamkin	Alexander Kamkin	
1707	C++TESK Testing ToolKit	Task	Closed	Normal	Унификация опций скриптов	Alexander Kamkin	Alexander Kamkin	
2058	C++TESK Development Environment	Task	Closed	Normal	Установка имени файла по умолчанию при запуске graphviz	Alexander Kamkin	Alexander Kamkin	
1272	C++TESK Testing ToolKit	Task	Closed	Normal	Установка модуля подсветки синтаксиса VIM	Alexander Kamkin	Alexander Kamkin	
2053	C++TESK Development Environment	Bug	Closed	Normal	Часто при запуске тсс не устанавливается связь с сервером	Alexander Kamkin	Alexander Kamkin	
5067	Retrascope	Bug	Closed	Immediate	[build] Не нужно комитить некомпиллируемое!	Alexander Kamkin	Alexander Protsenko	0.1

#	Project	Tracker	Status	Priority	Subject	Author	Assignee	Target version
5274	Retrascope	Task	Closed	Normal	[cfg][printer][graphml] Получение принтера выражений из конфигурации	Alexander Kamkin	Alexander Protsenko	0.1
5082	Retrascope IDE	Bug	Closed	Normal	[cfg][visualizator][zest] В графе не рисуются стрелки	Alexander Kamkin	Alexander Protsenko	
5083	Retrascope IDE	Task	Closed	Normal	[cfg][visualizator][zest] Используется неудачный метод размещения	Alexander Kamkin	Alexander Protsenko	
5084	Retrascope IDE	Bug	Closed	Normal	[cfg][visualizator][zest] Используются неправильные фигуры для узлов графа	Alexander Kamkin	Alexander Protsenko	
5261	Retrascope IDE	Bug	Closed	Normal	[cfg][visualizator][zest] Ошибки в узлах ветвления	Alexander Kamkin	Alexander Protsenko	
5113	Retrascope IDE	Task	Closed	Immediate	[cfg][visualizator][zest] Создать задачи, о которых мы говорили 21.07.2014	Alexander Kamkin	Alexander Protsenko	
5769	MicroTESK	Task	Rejected	Normal	Code review	Alexander Kamkin	Alexander Protsenko	2.2
5068	Retrascope	Bug	Closed	Urgent	[comments] Комментарии к коду	Alexander Kamkin	Alexander Protsenko	0.1
4885	Retrascope	Task	Closed	Normal	[eclipse][verilog] Редактор описаний на языке Verilog	Alexander Kamkin	Alexander Protsenko	0.1
4879	Retrascope	Task	Closed	Normal	[efsm][printer] Разработка принтеров EFSM-моделей в графические форматы	Alexander Kamkin	Alexander Protsenko	0.1
4883	Retrascope IDE	Task	Closed	Normal	[efsm][visualizator][zest] Визуализация EFSM-моделей	Alexander Kamkin	Alexander Protsenko	0.1
5300	Java SoftFloat	Task	Closed	Normal	Porting SoftFloat to Java	Alexander Kamkin	Alexander Protsenko	
10095	MicroTESK for RISC-V	Bug	Closed	Normal	print_imm12 seems to be redundant	Alexander Kamkin	Alexander Protsenko	0.1
5114	Retrascope IDE	Task	Closed	Normal	[verilog][parser][cfg] Вывод пометок на дугах из SwitchNode	Alexander Kamkin	Alexander Protsenko	
3628	C++TESK Development Environment	Task	Open	Normal	Описание XML-схемы для прототипирования тестовых систем	Alexander Kamkin	Alexander Protsenko	
4882	Retrascope	Task	New	Low	Разработка принтеров блок-схем в графические форматы	Alexander Kamkin	Alexander Protsenko	2.0
4886	Retrascope IDE	Task	Closed	Normal	Реализовать навигатор по модели HDL-описания	Alexander Kamkin	Alexander Protsenko	
169	CTESK	Bug	Closed	Normal	CTESK не может создать трассы размера > 2Gb	Alexander Kamkin	Alexey Demakov	2.8
297	CTESK	Bug	Closed	Normal	Memory leak in tracer	Alexander Kamkin	Alexey Demakov	2.8
290	Aspectrace	Task	New	Normal	Аспект FSM: трассировка переходов	Alexander Kamkin	Alexey Demakov	
279	CTESK	Task	New	Normal	Библиотека рандомизации	Alexander Kamkin	Alexey Demakov	



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208	CTESK	Feature	Closed	High	Возможность отключения проверки предусловия реакций	Alexander Kamkin	Alexey Demakov	2.8
230	CTESK	Task	Closed	Normal	Возможность отключения проверки предусловия стимулов	Alexander Kamkin	Alexey Demakov	2.8
209	CTESK	Feature	Closed	High	Выделение предусловия реакции в отдельную функцию при трансляции	Alexander Kamkin	Alexey Demakov	2.8
229	CTESK	Task	Closed	Normal	Выделение предусловия стимула в отдельную функцию при трансляции	Alexander Kamkin	Alexey Demakov	2.8
497	CTESK	Task	Closed	Normal	Добавить assertion в init_Int2PtrHashMap	Alexander Kamkin	Alexey Demakov	2.8
203	CTESK	Task	Closed	Normal	Итерация значений итерационных переменных	Alexander Kamkin	Alexey Demakov	2.8
1813	CTESK	Feature	New	High	Конфликт имен ATL с пользовательскими	Alexander Kamkin	Alexey Demakov	
288	CTESK	Task	Closed	Normal	Модификация функции getTTFileName()	Alexander Kamkin	Alexey Demakov	
491	CTESK	Bug	Closed	Normal	Неглубокое копирование строк	Alexander Kamkin	Alexey Demakov	2.8
204	CTESK	Bug	Closed	High	Не поддерживается совместная работа функций next() и call()	Alexander Kamkin	Alexey Demakov	2.8
206	CTESK	Bug	Closed	High	Ошибка в генерируемой функции call	Alexander Kamkin	Alexey Demakov	2.8
155	CTESK	Bug	Rejected	Normal	Переименование функции clone()	Alexander Kamkin	Alexey Demakov	
280	CTESK	Task	Closed	Normal	Функция отключения проверки постусловий	Alexander Kamkin	Alexey Demakov	2.8
2520	Aspectrace	Bug	Rejected	Normal	Caused by: java.lang.NumberFormatException: For input string: "<font color="#FF0000">?</font></b>"	Alexander Kamkin	Anastasia Tugaenko	
2383	Aspectrace	Task	New	Normal	Генератор не должен падать, когда в трассе встречается элемент покрытия, не принадлежащий покрытию	Alexander Kamkin	Anastasia Tugaenko	
1246	Aspectrace	Task	Closed	Normal	Если в трассе нет покрытия, отображается null	Alexander Kamkin	Anastasia Tugaenko	
1257	Aspectrace	Task	Closed	Normal	Отключение построения отчета по аспекту coverage	Alexander Kamkin	Anastasia Tugaenko	
1259	Aspectrace	Task	Closed	Normal	Шаблоны velocity находятся в одном каталоге с jar	Alexander Kamkin	Anastasia Tugaenko	
6218	MicroTESK	Task	Closed	Normal	Add a reference to DataManager to EngineContext	Alexander Kamkin	Andrei Tatarnikov	2.2
8698	MicroTESK	Task	Closed	Normal	Adding makefiles to MicroTESK demos	Alexander Kamkin	Andrei Tatarnikov	2.4
8670	MicroTESK	Bug	Rejected	Normal	Address 0xffffffa0002380 is unallocated. Simulation is paused until it is allocated.	Alexander Kamkin	Andrei Tatarnikov	2.4
5986	MicroTESK	Task	Closed	High	Addresses in ConcreteCall and TestSequence	Alexander Kamkin	Andrei Tatarnikov	2.2
5435	MicroTESK	Task	Closed	Normal	An architecture validator engine is required	Alexander Kamkin	Andrei Tatarnikov	2.2
5345	MicroTESK	Task	Closed	Normal	[arch] Notes to the existing architecture models and templates	Alexander Kamkin	Andrei Tatarnikov	2.1
5924	MicroTESK	Task	Closed	Normal	Automatic marks insertion for exceptions	Alexander Kamkin	Andrei Tatarnikov	2.2
7641	MicroTESK	Task	Closed	Normal	Basic support for multiple-PE testing	Alexander Kamkin	Andrei Tatarnikov	2.4
5952	MicroTESK	Task	Closed	Normal	Biased distribution over test situations	Alexander Kamkin	Andrei Tatarnikov	2.3
8676	Fortress	Bug	Closed	Normal	BitVector's comparison operations return BitVector	Alexander Kamkin	Andrei Tatarnikov	0.4
6533	MicroTESK	Bug	Closed	Normal	Branch Engine: java.lang.IllegalStateException: Value is not set.	Alexander Kamkin	Andrei Tatarnikov	2.3

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6439	MicroTESK	Task	Closed	Immediate	Buffer preparator: entry(lo, hi)	Alexander Kamkin	Andrei Tatarnikov	2.3
5679	MicroTESK	Bug	Closed	High	Bug in test data generation	Alexander Kamkin	Andrei Tatarnikov	2.2
4131	MicroTESK	Task	Closed	Normal	[build] A target for generating javadoc documentation	Alexander Kamkin	Andrei Tatarnikov	
4862	MicroTESK	Bug	Closed	Immediate	[build] ChangeLog has not be updated	Alexander Kamkin	Andrei Tatarnikov	2.0
4099	MicroTESK	Bug	Closed	High	[build] Compilation error	Alexander Kamkin	Andrei Tatarnikov	
4774	MicroTESK	Bug	Closed	Urgent	[build] compile.sh (being executed in any directory except bin) fails	Alexander Kamkin	Andrei Tatarnikov	2.0
6524	MicroTESK	Bug	Closed	Normal	Build failed: cannot download CVC4	Alexander Kamkin	Andrei Tatarnikov	2.3
5035	Fortress	Task	Closed	High	[build] New build required	Alexander Kamkin	Andrei Tatarnikov	0.3
4777	MicroTESK	Task	Closed	Urgent	[build] README file is missing	Alexander Kamkin	Andrei Tatarnikov	2.0
4776	MicroTESK	Task	Closed	Urgent	[build] Strange structure of the MicroTESK home directory	Alexander Kamkin	Andrei Tatarnikov	2.0
3961	MicroTESK	Task	Closed	Normal	[build] Первый билд проекта	Alexander Kamkin	Andrei Tatarnikov	
3909	MicroTESK	Bug	Closed	Normal	[build] Проект не компилируется java 1.6	Alexander Kamkin	Andrei Tatarnikov	
3823	MicroTESK	Task	Closed	High	[build] Скрипт запуска инструмента	Alexander Kamkin	Andrei Tatarnikov	
4992	Fortress	Bug	Closed	Normal	[calculator] В StandardOperationsInt операции MOD и REM реализованы одинаково	Alexander Kamkin	Andrei Tatarnikov	0.3
4993	Fortress	Bug	Closed	Normal	[calculator] Для неассоциативных операций некорректно указывать BINARY_UNBOUND	Alexander Kamkin	Andrei Tatarnikov	0.3
4987	Fortress	Task	Closed	Normal	[calculator] Переименование класса Range	Alexander Kamkin	Andrei Tatarnikov	0.3
4989	Fortress	Task	Closed	High	[calculator] Поддержка операций произвольной арности	Alexander Kamkin	Andrei Tatarnikov	0.3
6103	MicroTESK	Task	Closed	Urgent	Call.getTargetLabel method wanted	Alexander Kamkin	Andrei Tatarnikov	2.2
6142	MicroTESK	Task	Closed	Normal	Call's method getBlockSize()	Alexander Kamkin	Andrei Tatarnikov	2.2
6140	MicroTESK	Task	Closed	Normal	Call's methods isLoad() and isStore()	Alexander Kamkin	Andrei Tatarnikov	2.2
5958	MicroTESK	Task	Closed	Normal	Checking addresses using memory region specs	Alexander Kamkin	Andrei Tatarnikov	2.2
6136	MicroTESK	Task	Closed	Normal	Checking UNPREDICATED behavior during simulation	Alexander Kamkin	Andrei Tatarnikov	2.2
5448	MicroTESK	Bug	Closed	Normal	[commit] Ко всем комитам должны быть комментарии	Alexander Kamkin	Andrei Tatarnikov	
6125	MicroTESK	Bug	Closed	Urgent	Concrete calls should be supplied with correct addresses	Alexander Kamkin	Andrei Tatarnikov	2.2
5915	MicroTESK	Task	Closed	Normal	Configuration file and command line options	Alexander Kamkin	Andrei Tatarnikov	2.2
5697	MicroTESK	Task	Closed	High	Controlling register dependencies in test templates	Alexander Kamkin	Andrei Tatarnikov	2.2
5641	MicroTESK	Bug	Closed	Normal	[cpu][template] A typo in example.rb comments	Alexander Kamkin	Andrei Tatarnikov	2.1
5698	MicroTESK	Task	Closed	High	Data file generation	Alexander Kamkin	Andrei Tatarnikov	2.2
5489	Fortress	Task	Closed	High	[data] The BitVector.valueOf(BigInteger value, int bitSize) method required	Alexander Kamkin	Andrei Tatarnikov	0.3
6068	MicroTESK	Task	Rejected	Normal	Description of complex situations	Alexander Kamkin	Andrei Tatarnikov	2.2
5301	MicroTESK	Task	Rejected	Normal	Description of the Java template interface	Alexander Kamkin	Andrei Tatarnikov	2.2
3993	MicroTESK	Task	Rejected	Normal	Description of the nML translator	Alexander Kamkin	Andrei Tatarnikov	

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4714	MicroTESK	Task	Closed	Normal	[docs] Installation Instruction	Alexander Kamkin	Andrei Tatarnikov	2.0
4228	MicroTESK	Task	Closed	High	[docs] Presentation for the Course	Alexander Kamkin	Andrei Tatarnikov	
4780	MicroTESK	Task	Closed	Urgent	[docs] The MicroTESK datasheet should be updated	Alexander Kamkin	Andrei Tatarnikov	2.0
4773	MicroTESK	Task	Closed	Normal	[docs] Изменить состав документации, попадаемой в билд	Alexander Kamkin	Andrei Tatarnikov	2.0
2146	MicroTESK	Task	Closed	Low	[docs] Комментарии javadoc ко всем public методам	Alexander Kamkin	Andrei Tatarnikov	
3476	Fortress	Task	Closed	Normal	[docs] Перенести из проекта MicroTESK описание	Alexander Kamkin	Andrei Tatarnikov	0.1
7655	MicroTESK	Task	Closed	Normal	Documentation describing the overall generation flow	Alexander Kamkin	Andrei Tatarnikov	2.4
6451	MicroTESK	Bug	Closed	Urgent	Error: Address 0x0 does not match any memory region	Alexander Kamkin	Andrei Tatarnikov	2.3
7680	MicroTESK	Bug	Closed	Urgent	Error: Failed to load the ru.ispras.microtesk.model.minimips.mmu.sim.Model	Alexander Kamkin	Andrei Tatarnikov	2.4
6351	MicroTESK	Bug	Closed	Normal	ERROR (SEMANTIC): "The image attribute is not defined for the Op primitive."	Alexander Kamkin	Andrei Tatarnikov	2.3
5691	MicroTESK	Bug	Closed	Urgent	Error: Your application used more memory than the safety cap of 1024M.	Alexander Kamkin	Andrei Tatarnikov	2.1
5221	Fortress	Task	Closed	Normal	[expression] More auxiliary methods dealing with expressions	Alexander Kamkin	Andrei Tatarnikov	0.3
5565	Fortress	Task	Closed	Normal	[expression] Setting traversal order in visitor	Alexander Kamkin	Andrei Tatarnikov	0.3
4681	Fortress	Bug	Closed	Normal	[expression] В обходчике не учитывается то обстоятельство, что выражение может быть значением или переменной	Alexander Kamkin	Andrei Tatarnikov	0.3
4982	Fortress	Task	Closed	Normal	[expression] Возможность досрочного завершения обхода	Alexander Kamkin	Andrei Tatarnikov	0.3
4672	Fortress	Bug	Closed	High	[expression] Исчезла поддержка некоторых операций	Alexander Kamkin	Andrei Tatarnikov	0.3
4983	Fortress	Task	Closed	Normal	[expression] Переименование Visitor и Walker	Alexander Kamkin	Andrei Tatarnikov	0.3
5210	Fortress	Task	Closed	Normal	[expression] Переименование некоторых сущностей	Alexander Kamkin	Andrei Tatarnikov	0.3
4692	Fortress	Task	Closed	Normal	[expression] Перенос идентификаторов специальных операций (FunctionOperation) в пакет expression	Alexander Kamkin	Andrei Tatarnikov	0.3
4522	Fortress	Task	Closed	High	[expression] Пользовательские принтеры выражений/ограничений	Alexander Kamkin	Andrei Tatarnikov	0.3
6226	MicroTESK	Task	Closed	High	Generating abstract representation of MMU	Alexander Kamkin	Andrei Tatarnikov	2.3
4806	MicroTESK	Bug	Closed	Normal	[generator] Manual specification of initialization code	Alexander Kamkin	Andrei Tatarnikov	2.1
6165	MicroTESK	Task	Rejected	Urgent	Guard specification for view devices	Alexander Kamkin	Andrei Tatarnikov	2.2
4646	MicroTESK	Task	Closed	Normal	Handling Java keywords	Alexander Kamkin	Andrei Tatarnikov	2.2
6862	MicroTESK	Bug	Closed	Urgent	If a block contains several sequences, only one is returned by the iterator	Alexander Kamkin	Andrei Tatarnikov	2.3
6326	MicroTESK	Bug	Closed	High	If an nml file contains no operations, translation fails with NPE	Alexander Kamkin	Andrei Tatarnikov	2.3
6788	MicroTESK	Bug	Closed	Urgent	Illegal compositor name -> java.lang.IllegalArgumentException	Alexander Kamkin	Andrei Tatarnikov	2.3
8674	MicroTESK	Bug	Closed	Normal	Incorrect memory preparator in miniMIPS demo	Alexander Kamkin	Andrei Tatarnikov	2.4
6452	MicroTESK	Bug	Closed	Urgent	Incorrect result of MMU translation	Alexander Kamkin	Andrei Tatarnikov	2.3
7645	MicroTESK	Task	Closed	Normal	Instruction decoder implementation	Alexander Kamkin	Andrei Tatarnikov	2.4
4866	MicroTESK	Task	Closed	High	Instruction interface methods for checking whether an instruction is branch	Alexander Kamkin	Andrei Tatarnikov	2.2

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6306	MicroTESK	Task	Closed	High	Integration with JSoftFloat	Alexander Kamkin	Andrei Tatarnikov	2.3
6054	MicroTESK	Task	Closed	High	Interface method for checking whether an instruction is capable of throwing an exception	Alexander Kamkin	Andrei Tatarnikov	2.2
5877	Fortress	Task	Closed	Normal	InvariantChecks: checkTrue and checkFalse methods	Alexander Kamkin	Andrei Tatarnikov	0.4
6376	MicroTESK	Bug	Closed	High	java.lang.AssertionError: Operation.image - default implementation. Should never be called!	Alexander Kamkin	Andrei Tatarnikov	2.3
6478	MicroTESK	Bug	Closed	Normal	java.lang.IllegalStateException: Multiple hits in a cache set	Alexander Kamkin	Andrei Tatarnikov	2.3
6462	MicroTESK	Bug	Closed	Urgent	java.lang.UnsupportedOperationException: isHit is unsupported for mapped buffers.	Alexander Kamkin	Andrei Tatarnikov	2.3
6330	MicroTESK	Bug	Closed	Normal	java.util.NoSuchElementException: can't look backwards more than one token in this stream	Alexander Kamkin	Andrei Tatarnikov	2.3
6111	MicroTESK	Bug	Closed	Normal	Labels are not printed	Alexander Kamkin	Andrei Tatarnikov	2.2
6112	MicroTESK	Bug	Closed	Urgent	Labels are not unique	Alexander Kamkin	Andrei Tatarnikov	2.2
6308	MicroTESK	Task	Closed	High	Link between ISA specs and MMU specs	Alexander Kamkin	Andrei Tatarnikov	2.3
4778	TestBase	Task	Open	Urgent	Low-level interface for adding test situations (named constraints)	Alexander Kamkin	Andrei Tatarnikov	0.0
4804	TestBase	Task	New	Normal	Manual description of test situations in Java and XML	Alexander Kamkin	Andrei Tatarnikov	0.0
6028	MicroTESK	Task	Closed	Normal	Mark explicitly specified registers (addressing modes) as 'used'	Alexander Kamkin	Andrei Tatarnikov	2.2
4096	MicroTESK	Task	Closed	Normal	Memory access handler	Alexander Kamkin	Andrei Tatarnikov	2.3
6085	MicroTESK	Bug	Closed	Immediate	MicroTESK 2.2.6 news + closed bugs	Alexander Kamkin	Andrei Tatarnikov	2.2
6234	MicroTESK	Bug	Closed	Normal	MicroTESK does not simulate checking code	Alexander Kamkin	Andrei Tatarnikov	2.2
6001	MicroTESK	Task	Closed	Normal	MicroTESK should report an error if no preparator is specified	Alexander Kamkin	Andrei Tatarnikov	2.2
4822	MicroTESK	Bug	Closed	Normal	[mips][model] tmp_signed_word & tmp_signed_word1	Alexander Kamkin	Andrei Tatarnikov	2.0
4255	MicroTESK	Task	Closed	Normal	[mips] Добавить в проект модель MIPS	Alexander Kamkin	Andrei Tatarnikov	
6332	MicroTESK	Bug	Closed	Normal	mismatched input '-' expecting SEMI"	Alexander Kamkin	Andrei Tatarnikov	2.3
6294	MicroTESK	Task	Closed	Normal	MmuSubsystem.Builder.setSettings(GeneratorSettings)	Alexander Kamkin	Andrei Tatarnikov	2.3
6277	MicroTESK	Bug	Closed	Normal	MMU translator does not support X<...>:Y	Alexander Kamkin	Andrei Tatarnikov	2.3
4824	MicroTESK	Task	Closed	Normal	[model] Floating-point arithmetic support	Alexander Kamkin	Andrei Tatarnikov	2.1
6372	MicroTESK	Task	Closed	Normal	Mode's attribute number	Alexander Kamkin	Andrei Tatarnikov	2.3
6052	MicroTESK	Task	Closed	Normal	Multiple preparators per mode (index range, value mask, etc.)	Alexander Kamkin	Andrei Tatarnikov	2.2
7679	MicroTESK	Bug	Closed	Normal	New section in ChangeLog required	Alexander Kamkin	Andrei Tatarnikov	2.4
6333	MicroTESK	Bug	Rejected	Normal	NmiTreeWalker.g: node from line 0:0 no viable alternative at input	Alexander Kamkin	Andrei Tatarnikov	2.3
6378	MicroTESK	Bug	Rejected	Normal	No way to jump from epilogue to a label defined in prologue	Alexander Kamkin	Andrei Tatarnikov	
6024	MicroTESK	Task	Closed	Urgent	Option for specifying arch directories	Alexander Kamkin	Andrei Tatarnikov	2.2
5996	MicroTESK	Task	Closed	Normal	Options for turning on/off comments	Alexander Kamkin	Andrei Tatarnikov	2.2
5978	MicroTESK	Task	Closed	Normal	Options related to splitting instruction stream into parts	Alexander Kamkin	Andrei Tatarnikov	2.2
6113	MicroTESK	Bug	Rejected	Normal	org.jruby.exceptions.RaiseException: (ArgumentError) wrong number of arguments	Alexander Kamkin	Andrei Tatarnikov	2.2

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6214	MicroTESK	Task	Closed	High	Overriding configuration options from templates	Alexander Kamkin	Andrei Tatarnikov	2.2
5933	MicroTESK	Task	Closed	Normal	Printing test generation statistics	Alexander Kamkin	Andrei Tatarnikov	2.2
4644	MicroTESK	Task	Rejected	Normal	[project] Зачем нужна папка microtesk в trunk	Alexander Kamkin	Andrei Tatarnikov	
4682	Fortress	Task	Closed	Normal	[project] Отделить тесты от исходников	Alexander Kamkin	Andrei Tatarnikov	0.3
5999	MicroTESK	Task	Closed	High	Randomization of operands of instructions with unspecified situations	Alexander Kamkin	Andrei Tatarnikov	2.2
7681	MicroTESK	Bug	Closed	Urgent	README file is incorrect	Alexander Kamkin	Andrei Tatarnikov	2.4
4814	MicroTESK	Task	Rejected	Normal	Refactoring of L-expressions and memory model	Alexander Kamkin	Andrei Tatarnikov	2.2
8672	MicroTESK	Task	Closed	Normal	Renaming nontaken to nonexecuted in test templates	Alexander Kamkin	Andrei Tatarnikov	2.4
6438	MicroTESK	Task	Closed	Normal	Reusing blocks in test templates	Alexander Kamkin	Andrei Tatarnikov	2.3
5747	MicroTESK	Task	Closed	Normal	Simple infinite loop detection	Alexander Kamkin	Andrei Tatarnikov	2.2
5216	Fortress	Task	Closed	Normal	[solver] Вспомогательные методы работы с логическими формулами	Alexander Kamkin	Andrei Tatarnikov	0.3
5220	Fortress	Task	Closed	Normal	[solver] Переименовать SolverUtils	Alexander Kamkin	Andrei Tatarnikov	0.3
7646	MicroTESK	Task	Closed	Normal	Structures in nML	Alexander Kamkin	Andrei Tatarnikov	2.4

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