

Issues

#	Project	Tracker	Status	Priority	Subject	Author	Assignee	Target version
10369	MicroTESK	Bug	New	Normal	BubbleSortTestCase: first ori instruction doesn't write null in a register	Ivan Grigorov		2.6
10029	MicroTESK	Bug	Closed	Normal	MIPS: adds presimulated instructions in a trace	Ivan Grigorov	Ivan Grigorov	2.5
10028	MicroTESK	Bug	Closed	Normal	MIPS: adds a sll instruction before every instruction in a trace	Ivan Grigorov	Ivan Grigorov	2.5