

## Issues

#	Project	Tracker	Status	Priority	Subject	Author	Assignee	Target version
10011	MicroTESK	Bug	New	High	Необходима возможность преобразования конструкции if-then-else в mir представление	Alexander Protsenko	Artem Kotsynyak	2.6
9472	MicroTESK	Developer Request	Open	High	Неочевидные преобразования типов при вычислении выражений	Alexander Protsenko	Artem Kotsynyak	2.6
10064	MicroTESK	Bug	New	Normal	Support FP instructions in MIR	Alexander Protsenko	Artem Kotsynyak	2.6
10061	MicroTESK	Bug	New	Normal	Buffers are now shared among all processing elements	Alexander Kamkin	Artem Kotsynyak	2.6
9720	MicroTESK	Task	New	Normal	Определение влияния параметров на инструкцию	Alexander Protsenko	Artem Kotsynyak	2.6
8696	MicroTESK	Bug	New	Normal	Parsing an mmu-file with empty read and write definitions causes IllegalArgumentException	Alexander Kamkin	Artem Kotsynyak	2.6
8060	MicroTESK	Bug	New	Normal	Attributes image and syntax and static instances	Andrei Tatarnikov	Artem Kotsynyak	2.6
7401	MicroTESK	Bug	New	Normal	int_to_float is not supported for constant expressions	Alexander Protsenko	Artem Kotsynyak	2.6
6380	MicroTESK	Bug	New	Normal	Possibility to specify initial values for registers	Andrei Tatarnikov	Artem Kotsynyak	2.6
8186	MicroTESK	Task	New	Normal	Full support for 'struct' in nml	Alexander Protsenko	Artem Kotsynyak	2.6
8587	MicroTESK	Feature	New	Normal	ISA subsets	Sergey Smolov	Artem Kotsynyak	2.6
10193	MicroTESK	Task	New	Normal	Cache instances configuration	Alexander Kamkin	Artem Kotsynyak	2.6
10832	MicroTESK	Bug	New	Normal	Parser allows bit vector extraction of array types	Alexander Protsenko	Artem Kotsynyak	2.6
11661	MicroTESK	Developer Request	New	Normal	Загрузка данных в память через mode	Alexander Protsenko	Artem Kotsynyak	2.6
11695	MicroTESK	Bug	New	Normal	error: cannot find symbol (зависимость mode от op)	Alexander Protsenko	Artem Kotsynyak	2.6
12027	MicroTESK	Task	New	Normal	SMT Solvers: CVC4 -> CVC5	Alexander Protsenko	Artem Kotsynyak	2.6
12223	MicroTESK	Bug	New	Normal	Конфликт инструкций с разными модификаторами (op, internal op) и с одним телом	Alexander Protsenko	Artem Kotsynyak	2.6
6065	MicroTESK	Task	New	Low	Handling marks with exported symbols in the coverage extractor	Alexander Kamkin	Artem Kotsynyak	2.6
6064	MicroTESK	Task	New	Low	Translator support for exporting symbols in marks	Alexander Kamkin	Artem Kotsynyak	2.6
4674	TestBase	Task	New	Normal	Тестовые ситуации	Sergey Smolov	Artem Kotsynyak	0.0
5725	TestBase	Task	New	Normal	Meta-information provider required	Alexander Kamkin	Artem Kotsynyak	0.0
4523	Fortress	Task	New	Immediate	Implementing a solver for simple constraints	Alexander Kamkin	Artem Kotsynyak	0.4
4713	Fortress	Task	New	High	SMT-LIB structures	Sergey Smolov	Artem Kotsynyak	0.4
5211	Fortress	Task	Open	High	Unit-тесты для трансформеров и калькулятора	Alexander Kamkin	Artem Kotsynyak	0.4
10177	Fortress	Bug	Feedback	Normal	TreeVisitor's SKIP status does not work as expected	Alexander Kamkin	Artem Kotsynyak	0.4
5451	Fortress	Feature	New	Normal	Support external rules in TransformerRule interface	Artem Kotsynyak	Artem Kotsynyak	0.4
11654	MicroTESK	Bug	New	Normal	Или правило для регистров может вызывать ошибку "error: incompatible types"	Alexander Protsenko	Artem Kotsynyak	

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10764	MicroTESK	Task	New	Normal	Поддержка алтернативного формата вывода SMT-LIB представления машинного кода	Pavel Putro	Artem Kotsynyak	
10566	MicroTESK	Bug	New	Normal	Отсутствует поддержка тернарного оператора в mir	Pavel Putro	Artem Kotsynyak	