

Issues

#	Project	Tracker	Status	Priority	Subject	Author	Assignee	Target version
5836	Local Support Project	Bug	Resolved	High	не собирается проект на сервере Jenkins	Sergey Smolov	Sergey Smolov	
12249	MicroTESK for PowerPC	Bug	New	Normal	qemu-system-ppc: failed to find romfile "efi-virtio.rom"	Sergey Smolov	Sergey Smolov	
10819	MicroTESK for PowerPC	Bug	New	Normal	Trace Matcher crashes on QEMU4V empty trace	Sergey Smolov	Sergey Smolov	
12243	QEMU4V	Bug	Resolved	Normal	ERROR: missing file .../qemu4v-src-0.3.6-beta-200430/ui/keycodemapdb/README	Alexander Protsenko	Sergey Smolov	0.3
12242	QEMU4V	Feature	Resolved	Normal	Добавлять в архив корневую директорию qemu4v-src-X	Alexander Protsenko	Sergey Smolov	0.3
12607	QEMU4V	Bug	New	Normal	missing log problem	Sergey Smolov	Sergey Smolov	
12282	QEMU4V	Task	New	Normal	migrate to QEMU 8.0.0	Sergey Smolov	Sergey Smolov	0.3
12058	QEMU4V	Task	Resolved	Normal	migrate to QEMU 7.2.0	Sergey Smolov	Sergey Smolov	0.3
11811	QEMU4V	Task	Resolved	Normal	migrate to QEMU 7.1.0	Sergey Smolov	Sergey Smolov	0.3
11516	QEMU4V	Task	Resolved	Normal	migrate to QEMU 7.0.0	Sergey Smolov	Sergey Smolov	0.3
11017	QEMU4V	Task	Resolved	Normal	migrate to QEMU 6.2.0	Sergey Smolov	Sergey Smolov	0.3
10888	QEMU4V	Task	Resolved	Normal	migrate to QEMU 6.1.0	Sergey Smolov	Sergey Smolov	0.3
10836	QEMU4V	Task	Resolved	Normal	migrate to QEMU 6.0.0	Sergey Smolov	Sergey Smolov	0.3
10611	QEMU4V	Task	Resolved	Normal	migrate to QEMU 5.2.0	Sergey Smolov	Sergey Smolov	0.3
10474	QEMU4V	Task	Resolved	Normal	migrate to QEMU 5.1.0	Sergey Smolov	Sergey Smolov	0.3
9986	QEMU4V	Task	New	Normal	check if QEMU4V features can be implemented as TCG plugin	Sergey Smolov	Sergey Smolov	0.3
5150	Retrascope	Task	New	Normal	Обработка функций - реализация макроподстановок	Alexander Kamkin	Sergey Smolov	1.2
5027	Retrascope	Task	New	Normal	Абстракция EFSM-моделей	Alexander Kamkin	Sergey Smolov	2.0
4889	Retrascope	Task	New	Low	Генерация заготовки эталонной модели по набору блок-схем (flowchart)	Alexander Kamkin	Sergey Smolov	2.0
4888	Retrascope	Task	New	Low	Генератор заготовки тестовой системы по ее описанию	Alexander Kamkin	Sergey Smolov	2.0
4887	Retrascope	Task	New	Low	Разработка внутреннего представления тестовой системы	Alexander Kamkin	Sergey Smolov	2.0
4881	Retrascope	Task	New	Low	Реализация алгоритма построения блок-схем по внутреннему представлению	Alexander Kamkin	Sergey Smolov	2.0
4880	Retrascope	Task	New	Low	Проектирование внутреннего представления для блок-схем	Alexander Kamkin	Sergey Smolov	2.0
10104	Retrascope	Bug	Open	Normal	Wiki documentation errata	Nikita Chertok	Sergey Smolov	1.1
9247	Retrascope	Feature	Open	High	CFG-to-C printer	Sergey Smolov	Sergey Smolov	1.2
10988	Retrascope	Task	New	Normal	lowercase debug messages	Sergey Smolov	Sergey Smolov	
10082	Retrascope	Bug	New	Normal	WARNING: Illegal reflective access by org.python.core.PySystemState	Sergey Smolov	Sergey Smolov	1.1
10058	Retrascope	Task	New	Normal	User documentation	Sergey Smolov	Sergey Smolov	1.2

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9488	Retrascope	Task	New	Normal	CFG-GADD transformer backend that makes assignments index and range-free	Sergey Smolov	Sergey Smolov	1.2
6449	Retrascope	Task	New	Low	testbench generator taking test sequences and mappings as inputs	Sergey Smolov	Sergey Smolov	2.0
6448	Retrascope	Task	New	Low	mapping description language + IR + parser	Sergey Smolov	Sergey Smolov	2.0
4521	Retrascope	Task	New	Low	Входной класс для генератора тестовой последовательности	Sergey Smolov	Sergey Smolov	2.0
4363	Retrascope	Task	New	Low	Критерий кластеризации входных сигналов, основанный на GA	Sergey Smolov	Sergey Smolov	2.0
10229	Retrascope Test Suite	Task	Resolved	Normal	Handle \$ND functions	Mikhail Lebedev	Sergey Smolov	
9670	Retrascope Test Suite	Task	New	High	add 'ar.v' module to the test suite when SVA support will be implemented	Sergey Smolov	Sergey Smolov	
11018	Retrascope Test Suite	Task	New	Normal	Verilog duplicate removing	Sergey Smolov	Sergey Smolov	
12246	Trace Matcher	Bug	Resolved	Normal	Ошибка при сборке проекта traceutils-src-0.1.9-beta-200116	Alexander Protsenko	Sergey Smolov	0.1
10100	Trace Matcher	Feature	Resolved	Normal	"--boot-size <num>" command line option	Sergey Smolov	Sergey Smolov	0.1
10099	Trace Matcher	Feature	Resolved	Normal	"--start-addr <hex value>" command line option	Sergey Smolov	Sergey Smolov	0.1
1185	Veritool	Task	New	Normal	Инструментирование Verilog-кода и отслеживание покрытия по коду	Alexander Kamkin	Sergey Smolov	
2059	C++TESK Development Environment	Task	New	Low	Использование разных цветов для выделения ключевых слов C++ и C++TESK	Alexander Kamkin	Sergey Smolov	