

## Issues

#	Project	Tracker	Status	Priority	Subject	Author	Assignee	Target version
10124	MicroTESK	Bug	New	High	Double preparation of the same register	Alexander Kamkin	Alexander Kamkin	2.6
9902	Verilog Translator	Bug	New	High	java.lang.IllegalArgumentException: Descriptor for '<var name>' has not been found	Sergey Smolov	Alexander Kamkin	0.1
9993	Verilog Translator	Bug	New	High	if two modules are passed to the tool and one includes another, the tool hangs	Sergey Smolov	Alexander Kamkin	0.1
10696	MicroTESK	Bug	New	Normal	Error: Failed to load custom data generators for plasma.	Alexander Protsenko	Alexander Kamkin	2.6
10121	MicroTESK	Bug	Open	Normal	technical output printing at 'compile.sh' script running with '--help' option	Sergey Smolov	Alexander Kamkin	2.6
10091	MicroTESK	Bug	New	Normal	[x86] ошибка при добавлении модели в src\main\etc\settings.xml	Alexander Protsenko	Alexander Kamkin	2.6
10070	MicroTESK	Bug	New	Normal	[minimips] Нужно больше информации об ошибке "NameError: undefined local ..."	Alexander Protsenko	Alexander Kamkin	2.6
10069	MicroTESK	Bug	New	Normal	cpu.nml Error: Internal error: context [/Isa] 1:8 attribute file isn't defined	Sergey Smolov	Alexander Kamkin	2.6
9869	MicroTESK	Bug	New	Normal	Генерация граничных значений для регистров инструкций	Alexander Protsenko	Alexander Kamkin	2.6
9473	MicroTESK	Bug	New	Normal	riscv.mmu 181:0 ERROR (SEMANTIC): "IF statements are not allowed in operations."	Alexander Protsenko	Alexander Kamkin	2.6
9471	MicroTESK	Bug	New	Normal	ERROR (SEMANTIC): The given bitfield expressions cannot be reduced to constant value	Alexander Protsenko	Alexander Kamkin	2.6
9441	MicroTESK	Bug	New	Normal	Unsupported operator BVADD	Alexander Protsenko	Alexander Kamkin	2.6
9440	MicroTESK	Bug	New	Normal	Bit field expressions must be statically calculated constants	Alexander Protsenko	Alexander Kamkin	2.6
9439	MicroTESK	Bug	New	Normal	Cannot perform OR with Condition [type=AND ...] and Condition [type=AND ...]	Alexander Protsenko	Alexander Kamkin	2.6
7603	MicroTESK	Bug	New	Normal	List of plug-ins must be stored in etc/settings.xml	Andrei Tatarnikov	Alexander Kamkin	2.6
7313	MicroTESK	Bug	New	Normal	Analysis for input/output parameters	Mikhail Chupilko	Alexander Kamkin	2.6
7229	MicroTESK	Bug	New	Normal	Unsupported assignment to mode	Mikhail Chupilko	Alexander Kamkin	2.6
6911	MicroTESK	Bug	New	Normal	The "get_address_of" method must work with all label types.	Andrei Tatarnikov	Alexander Kamkin	2.6
6396	MicroTESK	Bug	New	Normal	Handling TAG_REPLACED dependencies	Alexander Kamkin	Alexander Kamkin	2.6
6379	MicroTESK	Bug	New	Normal	Using if-then-else in syntax causes an error	Alexander Kamkin	Alexander Kamkin	2.6
6329	MicroTESK	Bug	New	Normal	java.lang.NullPointerException at ru.ispras.microtesk.mmu.translator.MmuTreeWalkerBase.newAttributeRef(MmuTreeWalkerBase.java:940)	Alexander Kamkin	Alexander Kamkin	2.6
5990	MicroTESK	Bug	New	Normal	Memory state must be taken into account when generating test data	Andrei Tatarnikov	Alexander Kamkin	2.6
5813	MicroTESK	Bug	New	Normal	Support of instance call in left part of assignment	Mikhail Chupilko	Alexander Kamkin	2.6
10136	MicroTESK	Bug	New	Normal	Comment for self checks differs from the others	Alexander Kamkin	Alexander Kamkin	2.6
10653	MicroTESK	Bug	New	Normal	MicroTESK mode: IsaPrimitive cannot be converted	Alexander Protsenko	Alexander Kamkin	2.6
10657	MicroTESK	Bug	New	Normal	Некорректная диагностика: вывод имени файла при указании строк для ошибки	Alexander Protsenko	Alexander Kamkin	2.6
10698	MicroTESK	Bug	New	Normal	ArgumentError: wrong number of arguments calling `target` (2 for 0)	Alexander Protsenko	Alexander Kamkin	2.6
10747	MicroTESK	Bug	New	Normal	Переопределение define_section_text (в соответствии с microtesk-2.5-user-guide-ru)	Alexander Protsenko	Alexander Kamkin	2.6
10513	Verilog Translator	Bug	New	Normal	macOS related line endings at Verilog modules	Sergey Smolov	Alexander Kamkin	0.1

#	Project	Tracker	Status	Priority	Subject	Author	Assignee	Target version
10215	Verilog Translator	Bug	New	Normal	ERROR: Starting points limit has been exhausted: 2255	Sergey Smolov	Alexander Kamkin	0.1
10512	Verilog Translator	Bug	New	Normal	ADDA162H90A_atop.v line 120:47 mismatched input ':' expecting RPAREN	Sergey Smolov	Alexander Kamkin	0.1
10510	Verilog Translator	Bug	New	Normal	ERROR: [Internal] Bit vector sizes do not match: 32 != 2.	Sergey Smolov	Alexander Kamkin	0.1
10509	Verilog Translator	Bug	New	Normal	ERROR: [Internal] 0 must be > 0	Sergey Smolov	Alexander Kamkin	0.1
10508	Verilog Translator	Bug	New	Normal	ERROR: [Internal] Java heap space	Sergey Smolov	Alexander Kamkin	0.1
10502	Verilog Translator	Bug	New	Normal	subbytes.v line 76:13 no viable alternative at input '['	Sergey Smolov	Alexander Kamkin	0.1
10505	Verilog Translator	Bug	New	Normal	ERROR: [Internal] 11 must be within range [0, 1)	Sergey Smolov	Alexander Kamkin	0.1
10096	MicroTESK for RISC-V	Bug	Resolved	Normal	The imm argument of addi has invalid type Type.CARD(12) while Type.INT(12) is expected	Alexander Protsenko	Alexander Kamkin	0.1
10098	MicroTESK for RISC-V	Bug	New	Normal	Some input files use unchecked or unsafe operations.	Alexander Protsenko	Alexander Kamkin	0.1
9388	MicroTESK for RISC-V	Bug	New	Normal	Mystical errors in model	Alexander Protsenko	Alexander Kamkin	0.1
10005	MicroTESK for MIPS	Bug	Resolved	Normal	org.jruby.exceptions.RaiseException: (NoMethodError) undefined method `newOrigin' for nil:NilClass	Alexander Protsenko	Alexander Kamkin	0.0
9383	MicroTESK for MIPS	Bug	Verified	Normal	Can't find MicroTESK Tracer log file	Alexander Protsenko	Alexander Kamkin	
9603	Verilog Translator	Bug	New	Normal	Interface signals not in port list	Mikhail Lebedev	Alexander Kamkin	
9477	Retrascope RISC-V Benchmark	Bug	New	Normal	an "import "DPI-C" function" construction causes Verilog Translator error	Sergey Smolov	Alexander Kamkin	
9377	MicroTESK for MIPS	Bug	New	Normal	'Failed to construct decoder' warnings in project's build log	Sergey Smolov	Alexander Kamkin	
9376	MicroTESK for MIPS	Bug	New	Normal	Warning: Group MIPS64FpuOp contains two items add_fmt and mfc1 with the same opcode 01000100000000000000000000000000	Sergey Smolov	Alexander Kamkin	
8797	Verilog Translator	Bug	New	Normal	Error when using multiple includes	Alexander Kamkin	Alexander Kamkin	0.1
8683	MicroTESK for MIPS	Bug	Resolved	Normal	Nonoptimal preparator generation	Alexander Kamkin	Alexander Kamkin	0.0
8675	MicroTESK for MIPS	Bug	Resolved	Normal	Executing tlbwi causes CU0	Alexander Kamkin	Alexander Kamkin	0.0
8678	MicroTESK for MIPS	Bug	Resolved	Normal	Exception in exception handler	Alexander Kamkin	Alexander Kamkin	0.0
5057	Retrascope IDE	Bug	New	Normal	[tool][configurator] Для низкоуровневого конфигуратора Arguments не нужно выдавать --arguments	Alexander Kamkin	Alexander Kamkin	0.1
3674	C++TESK Testing ToolKit	Bug	New	Normal	[build] Немного warning`ов	Alexander Protsenko	Alexander Kamkin	1.0
2669	C++TESK Testing ToolKit	Bug	New	Normal	[docs] Add figures to the project description & whitepaper	Alexander Kamkin	Alexander Kamkin	1.0

#	Project	Tracker	Status	Priority	Subject	Author	Assignee	Target version
4110	Verilog Translator	Bug	New	Normal	AST_DECLARATION: adding delay for net declarations	Alexander Kamkin	Alexander Kamkin	0.1
4170	Veritool	Bug	Resolved	Normal	Int128-512: Замечания к коду	Alexander Kamkin	Alexander Kamkin	
4171	Veritool	Bug	Resolved	Normal	Int128-512: Возможно ошибка	Alexander Kamkin	Alexander Kamkin	
3813	Veritool	Bug	Resolved	Normal	В структуре inputs_t нет reset	Alexander Kamkin	Alexander Kamkin	
3381	Veritool	Bug	New	Normal	Не поддерживается Verilog-код с многомерными массивами	Mikhail Chupilko	Alexander Kamkin	
2886	Veritool	Bug	Resolved	Normal	rename vsim_getByte8Value	Mikhail Chupilko	Alexander Kamkin	
10074	MicroTESK	Feature	New	Normal	option that stores boot obj at the generated ld script	Sergey Smolov	Alexander Kamkin	2.6
10071	MicroTESK	Feature	New	Normal	Влияние секции section_text на расположение exception_handler	Alexander Protsenko	Alexander Kamkin	2.6
9455	MicroTESK	Feature	New	Normal	Запись/Чтение в память в диапазоне <X..Y> (MEM[mem_index]<X..Y>)	Alexander Protsenko	Alexander Kamkin	2.6
1830	C++TESK Testing ToolKit	Feature	New	Normal	[lang][core] Задание частоты запуска сценарных функций по отдельности	Igor Melnichenko	Alexander Kamkin	1.1
1772	C++TESK Testing ToolKit	User Story	New	Normal	[docs] Имена типов map, list etc в CTESK	Mikhail Chupilko	Alexander Kamkin	1.0
9870	MicroTESK	Developer Request	New	Normal	Входной параметр в виде инструкции	Alexander Protsenko	Alexander Kamkin	2.6
11653	MicroTESK	Developer Request	New	Normal	При формировании теста не учитывают внутреннее состояние, что критично при тестировании BM	Alexander Protsenko	Alexander Kamkin	
5093	Retrascope IDE	Task	New	High	[cfg][visualizator][zest] Следует открывать граф в области редактора	Alexander Kamkin	Alexander Kamkin	0.1
10107	MicroTESK	Task	New	Normal	Entry point specification in templates	Alexander Kamkin	Alexander Kamkin	2.6
10106	MicroTESK	Task	New	Normal	Support %b as format's specifier	Alexander Kamkin	Alexander Kamkin	2.6
9476	MicroTESK	Task	New	Normal	Поддержка версии ревизий в настройках модели settings.xml	Alexander Protsenko	Alexander Kamkin	2.6
9448	MicroTESK	Task	New	Normal	Вызов метода init() в методе init() другого объекта op в *.mmu	Alexander Protsenko	Alexander Kamkin	2.6
9447	MicroTESK	Task	New	Normal	Доступ к константам *.nml из модулей *.mmu	Alexander Protsenko	Alexander Kamkin	2.6
7729	MicroTESK	Task	New	Normal	On-demand memory access path exploration (coverage extraction)	Alexander Kamkin	Alexander Kamkin	2.6
6461	MicroTESK	Task	New	Normal	Support for inline comments	Alexander Kamkin	Alexander Kamkin	2.6
6345	MicroTESK	Task	New	Normal	Support floating-point numbers in trace "".	Alexander Protsenko	Alexander Kamkin	2.6
5311	MicroTESK	Task	New	Normal	Shortcuts for getting modes from templates	Alexander Kamkin	Alexander Kamkin	2.6
5192	MicroTESK	Task	New	Normal	Assert constructions to check the model state during and after simulation	Andrei Tatarnikov	Alexander Kamkin	2.6
8481	MicroTESK	Task	New	Normal	Need a way to specify the termination address for the test program	Andrei Tatarnikov	Alexander Kamkin	2.6
4061	MicroTESK	Task	New	Normal	Support for endianness	Andrei Tatarnikov	Alexander Kamkin	2.6
10122	MicroTESK	Task	New	Normal	Template libraries for standard features	Alexander Kamkin	Alexander Kamkin	2.6
10304	MicroTESK	Task	New	Normal	deprecation warnings via compilation	Sergey Smolov	Alexander Kamkin	2.6
10676	MicroTESK	Task	New	Normal	Реализовать метод для получения размера операции в nml спецификации	Alexander Protsenko	Alexander Kamkin	2.6
10715	MicroTESK	Task	New	Normal	Подготовка данных для внутренних регистров	Alexander Protsenko	Alexander Kamkin	2.6

#	Project	Tracker	Status	Priority	Subject	Author	Assignee	Target version
8691	MicroTESK	Task	New	Normal	Sign- and zero-extend functions in preparators	Alexander Kamkin	Alexander Kamkin	2.6
10867	MicroTESK	Task	New	Normal	Поддержка big-endian/little-endian для кодировки инструкции	Alexander Protsenko	Alexander Kamkin	
10646	MicroTESK Documentation	Task	New	Normal	MicroTESK Documentation: дополнить описание конструкции mode	Alexander Protsenko	Alexander Kamkin	
9978	MicroTESK for RISC-V	Task	New	Normal	Some templates use rand(-2147483648, 2147483647) for randomizing words	Alexander Kamkin	Alexander Kamkin	0.1
9859	Verilog Translator	Task	New	Normal	modify "ERROR: [Internal] null" line at error log	Sergey Smolov	Alexander Kamkin	0.1
9790	Verilog Translator	Task	New	Normal	external names for unnamed generate blocks	Sergey Smolov	Alexander Kamkin	0.1
4884	Retrascope IDE	Task	New	Normal	[flowchart][visualizator][zest] Визуализация блок-схем	Alexander Kamkin	Alexander Kamkin	0.1
6505	Castle	Task	Feedback	Normal	Вопросы по MetalInfo	Alexander Kamkin	Alexander Kamkin	0.1
6506	Castle	Task	Feedback	Normal	Вопросы по представлению массивов	Alexander Kamkin	Alexander Kamkin	0.1
8982	Verilog Translator	Task	New	Normal	"for" loop unrolling	Sergey Smolov	Alexander Kamkin	0.1
4962	Retrascope	Task	New	Normal	Реализация утилиты Veritool с помощью средств Retrascope	Alexander Kamkin	Alexander Kamkin	2.0
4675	Fortress	Task	New	Normal	Манипуляция с функциями алгебры логики	Alexander Kamkin	Alexander Kamkin	0.4
5348	TestBase	Task	New	Normal	Корректная реализация генератора IntSubExceptionGenerator	Alexander Kamkin	Alexander Kamkin	0.0
5352	TestBase	Task	New	Normal	Implementation of FpMulHardToNRound	Alexander Kamkin	Alexander Kamkin	0.0
5325	TestBase	Task	New	Normal	Sqrt hard-to-round iterator for the nearest rounding needs to be reviewed	Alexander Kamkin	Alexander Kamkin	0.0
5351	TestBase	Task	New	Normal	Implementation of ResultAdapter for FP.MUL	Alexander Kamkin	Alexander Kamkin	0.0
5811	TestBase	Task	New	Normal	Test data generators for basic arithmetic SIMD instructions	Alexander Kamkin	Alexander Kamkin	0.0
4961	Retrascope IDE	Task	New	Normal	[tool][configurator] Конструктор инструментальных цепочек	Alexander Kamkin	Alexander Kamkin	0.1
5271	Retrascope IDE	Task	New	Normal	[task][configurator] Деактивация кнопки ОК	Alexander Kamkin	Alexander Kamkin	0.1
5273	Retrascope IDE	Task	New	Normal	[cfg][visualizator][zest] Получение принтера выражений из конфигурации	Alexander Kamkin	Alexander Kamkin	0.1
5272	Retrascope IDE	Task	New	Normal	[cfg][visualizator][zest] Установка прогресса при построении графа	Alexander Kamkin	Alexander Kamkin	0.1
5276	Retrascope IDE	Task	New	Normal	[cfg][visualizator][zest] Отладить размещение графа с циклами	Alexander Kamkin	Alexander Kamkin	0.1
1828	C++TESK Testing ToolKit	Task	New	Normal	[lang][core] Implementation of temporal sequences	Alexander Kamkin	Alexander Kamkin	1.1
1838	C++TESK Testing ToolKit	Task	New	Normal	[docs] Задokumentировать интерфейсы с ядром C++TESK	Alexander Kamkin	Alexander Kamkin	1.1
4955	C++TESK Testing ToolKit	Task	New	Normal	[lang][core] Способы работы с типами данных uint128_t, uint256_t и т.д.	Alexander Kamkin	Alexander Kamkin	1.0
4932	C++TESK Testing ToolKit	Task	Resolved	Normal	[lang][log] Переопределение вывода полей сообщений	Alexander Kamkin	Alexander Kamkin	1.0
3582	C++TESK Testing ToolKit	Task	New	Normal	[core] C++TESK as Explicit-State Model Checker	Alexander Kamkin	Alexander Kamkin	1.1

#	Project	Tracker	Status	Priority	Subject	Author	Assignee	Target version
2212	C++TESK Testing ToolKit	Task	New	Normal	[docs] Задokumentировать адаптеры по умолчанию	Alexander Kamkin	Alexander Kamkin	1.0
2213	C++TESK Testing ToolKit	Task	New	Normal	[docs] Английскую документацию делать в wiki	Alexander Kamkin	Alexander Kamkin	1.0
4097	Verilog Translator	Task	Open	Normal	Проверка заикливания include-файлов	Alexander Kamkin	Alexander Kamkin	0.1
4126	Verilog Translator	Task	New	Normal	Unification of AST_EDGE and AST_TABLE_EDGE	Alexander Kamkin	Alexander Kamkin	0.1
3781	Veritool	Task	Resolved	Normal	Нумерация тактов	Alexander Kamkin	Alexander Kamkin	
1665	Veritool	Task	Feedback	Normal	start_scen должен быть в библиотеке репозитория тестов	Alexander Kamkin	Alexander Kamkin	
1186	Veritool	Task	New	Normal	Скрипт установки Icarus Verilog	Alexander Kamkin	Alexander Kamkin	
1044	Veritool	Task	Feedback	Normal	Перенос репозитория тестов	Alexander Kamkin	Alexander Kamkin	
387	Veritool	Task	New	Normal	Отладочный вывод значений входов и выходов	Alexander Kamkin	Alexander Kamkin	
7678	MicroTESK	Task	New	Low	Generation of LLVM configuration files from nML specifications	Andrei Tatarnikov	Alexander Kamkin	2.6
6223	MicroTESK	Task	New	Low	User-defined template-oriented extensions (plugins)	Alexander Kamkin	Alexander Kamkin	2.6
4890	Retrascope	Task	New	Low	Разработка и реализация метода RTL-в-TLM абстракции блок-схем	Alexander Kamkin	Alexander Kamkin	2.0
4891	Retrascope	Task	New	Low	Реализация генератора тестов по набору блок-схем	Alexander Kamkin	Alexander Kamkin	2.0
4106	Verilog Translator	Task	New	Low	Support for library and config declaration	Alexander Kamkin	Alexander Kamkin	0.3